**3GPP TSG-RAN WG2 Meeting #126 *R2-24xxxxx***

**Fukuoka, Japan, 20th – 24th May 2024**

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| *CR-Form-v12.2* | | | | | | | | |
| **CHANGE REQUEST** | | | | | | | | |
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|  | **38.331** | **CR** | **xxxx** | **rev** | **-** | **Current version:** | **18.0.0** |  |
|  | | | | | | | | |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* | | | | | | | | |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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| ***Title:*** | Clarification on RETX\_COUNT during an LTM cell switch | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Source to WG:*** | Ericsson | | | | | | | | | |
| ***Source to TSG:*** | R2 | | | | | | | | | |
|  |  | | | | | | | | | |
| ***Work item code:*** | NR\_Mob\_enh2-Core | | | | |  | ***Date:*** | | | 2024-06-06 |
|  |  | | | |  | |  | | |  |
| ***Category:*** | **B** |  | | | | | ***Release:*** | | | Rel-18 |
|  | *Use one of the following categories:* ***F*** *(correction)* ***A*** *(mirror corresponding to a change in an earlier release)* ***B*** *(addition of feature),* ***C*** *(functional modification of feature)* ***D*** *(editorial modification)*  Detailed explanations of the above categories can be found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | | | | | | | | *Use one of the following releases: Rel-8 (Release 8) Rel-9 (Release 9) Rel-10 (Release 10) Rel-11 (Release 11) … Rel-16 (Release 16) Rel-17 (Release 17) Rel-18 (Release 18) Rel-19 (Release 19)* | |
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| ***Reason for change:*** | | In the RAN2#126, the following agreement has been taken regarding the handling of the RETX\_COUNT variable during an LTM cell switch procedure:   * UE initialize RETX\_COUNT when performing LTM fast recovery and LTM cell switch (for all RLC SDUs), when not triggered by other condition, discuss how to capture in CR discussion.   This CR is to capture this agreement. | | | | | | | | |
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| ***Summary of change:*** | | Section 7.1  - Clarified that the variable RETX\_COUNT is reset to its initial value at every LTM cell switch (e.g., when indicated by upper layers). | | | | | | | | |
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| ***Consequences if not approved:*** | | If the CR is not approved, then the RETX\_COUNT may not be reset to 0 after an LTM cell switch and this may cause a early RLF. | | | | | | | | |
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| ***Clauses affected:*** | | 7.1 | | | | | | | | |
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|  | | **Y** | **N** |  | | | |  | | |
| ***Other specs*** | | **X** |  | Other core specifications | | | | TS 38.300 CR 0842  TS 37.340 CR 0392  TS 38.321 CR 1817  TS 38.331 CR 4705 | | |
| ***affected:*** | |  | **X** | Test specifications | | | |  | | |
| ***(show related CRs)*** | |  | **X** | O&M Specifications | | | |  | | |
|  | |  | | | | | | | | |
| ***Other comments:*** | |  | | | | | | | | |
|  | |  | | | | | | | | |
| ***This CR's revision history:*** | |  | | | | | | | | |

START OF CHANGES

## 7.1 State variables

This clause describes the state variables used in AM and UM entities in order to specify the RLC protocol. The state variables defined in this clause are normative.

All state variables and all counters are non-negative integers.

All state variables related to AM data transfer can take values from 0 to 4095 for 12 bit SN or from 0 to 262143 for 18 bit SN. All arithmetic operations contained in the present document on state variables related to AM data transfer are affected by the AM modulus (i.e. final value = [value from arithmetic operation] modulo 4096 for 12 bit SN and 262144 for 18 bit SN).

All state variables related to UM data transfer can take values from 0 to 63 for 6 bit SN or from 0 to 4095 for 12 bit SN. All arithmetic operations contained in the present document on state variables related to UM data transfer are affected by the UM modulus (i.e. final value = [value from arithmetic operation] modulo 64 for 6 bit SN and 4096 for 12 bit SN).

When performing arithmetic comparisons of state variables or SN values, a modulus base shall be used.

TX\_Next\_Ack and RX\_Next shall be assumed as the modulus base at the transmitting side and receiving side of an AM RLC entity, respectively. This modulus base is subtracted from all the values involved, and then an absolute comparison is performed (e.g. RX\_Next <= SN < RX\_Next + AM\_Window\_Size is evaluated as [RX\_Next – RX\_Next] modulo 2[*sn-FieldLength*] <= [SN – RX\_Next] modulo 2[*sn-FieldLength*] < [RX\_Next + AM\_Window\_Size – RX\_Next] modulo 2[*sn-FieldLength*]), where *sn-FieldLength* is 12 or 18 for 12 bit SN and 18 bit SN, respectively.

RX\_Next\_Highest– UM\_Window\_Size shall be assumed as the modulus base at the receiving UM RLC entity. This modulus base is subtracted from all the values involved, and then an absolute comparison is performed (e.g. (RX\_Next\_Highest– UM\_Window\_Size) <= SN < RX\_Next\_Highest is evaluated as [(RX\_Next\_Highest– UM\_Window\_Size) – (RX\_Next\_Highest– UM\_Window\_Size)] modulo 2[*sn-FieldLength*] <= [SN – (RX\_Next\_Highest– UM\_Window\_Size)] modulo 2[*sn-FieldLength*] < [RX\_Next\_Highest– (RX\_Next\_Highest– UM\_Window\_Size)] modulo 2[*sn-FieldLength*]), where *sn-FieldLength* is 6 or 12 for 6 bit SN and 12 bit SN, respectively.

The transmitting side of each AM RLC entity shall maintain the following state variables:

a) TX\_Next\_Ack – Acknowledgement state variable

This state variable holds the value of the SN of the next RLC SDU for which a positive acknowledgment is to be received in-sequence, and it serves as the lower edge of the transmitting window. It is initially set to 0, and is updated whenever the AM RLC entity receives a positive acknowledgment for an RLC SDU with SN = TX\_Next\_Ack.

b) TX\_Next – Send state variable

This state variable holds the value of the SN to be assigned for the next newly generated AMD PDU. It is initially set to 0, and is updated whenever the AM RLC entity constructs an AMD PDU with SN = TX\_Next and contains an RLC SDU or the last segment of a RLC SDU.

c) POLL\_SN – Poll send state variable

This state variable holds the value of the highest SN of the AMD PDU among the AMD PDUs submitted to lower layer when POLL\_SN is set according to clause 5.3.3.2. It is initially set to 0.

The transmitting side of each AM RLC entity shall maintain the following counters:

a) PDU\_WITHOUT\_POLL – Counter

This counter is initially set to 0. It counts the number of AMD PDUs sent since the most recent poll bit was transmitted.

b) BYTE\_WITHOUT\_POLL – Counter

This counter is initially set to 0. It counts the number of data bytes sent since the most recent poll bit was transmitted.

c) RETX\_COUNT – Counter

This counter counts the number of retransmissions of an RLC SDU or RLC SDU segment (see clause 5.3.2). There is one RETX\_COUNT counter maintained per RLC SDU. This counter is reset to zero for each RLC SDU when indicated by upper layer.

The receiving side of each AM RLC entity shall maintain the following state variables:

a) RX\_Next – Receive state variable

This state variable holds the value of the SN following the last in-sequence completely received RLC SDU, and it serves as the lower edge of the receiving window. It is initially set to 0, and is updated whenever the AM RLC entity receives an RLC SDU with SN = RX\_Next.

b) RX\_Next\_Status\_Trigger – *t-Reassembly* state variable

This state variable holds the value of the SN following the SN of the RLC SDU which triggered *t-Reassembly*.

c) RX\_Highest\_Status – Maximum STATUS transmit state variable

This state variable holds the highest possible value of the SN which can be indicated by "ACK\_SN" when a STATUS PDU needs to be constructed. It is initially set to 0.

d) RX\_Next\_Highest – Highest received state variable

This state variable holds the value of the SN following the SN of the RLC SDU with the highest SN among received RLC SDUs. It is initially set to 0.

Each transmitting UM RLC entity shall maintain the following state variables:

a) TX\_Next – UM send state variable

This state variable holds the value of the SN to be assigned for the next newly generated UMD PDU with segment. It is initially set to 0, and is updated after the UM RLC entity submits a UMD PDU including the last segment of an RLC SDU to lower layers.

Each receiving UM RLC entity shall maintain the following state variables:

a) RX\_Next\_Reassembly – UM receive state variable

This state variable holds the value of the earliest SN that is still considered for reassembly. It is initially set to 0. For groupcast and broadcast of NR sidelink communication or for SL-SRB4 of NR sidelink discovery, it is initially set to the SN of the first received UMD PDU containing an SN. For the receiving UM RLC entity configured for MCCH or MTCH, it is up to UE implementation to set the initial value of RX\_Next\_Reassembly to a value before RX\_Next\_Highest.

b) RX\_Timer\_Trigger – UM *t-Reassembly* state variable

This state variable holds the value of the SN following the SN which triggered *t-Reassembly*.

c) RX\_Next\_Highest– UM receive state variable

This state variable holds the value of the SN following the SN of the UMD PDU with the highest SN among received UMD PDUs. It serves as the higher edge of the reassembly window. It is initially set to 0. For groupcast and broadcast of NR sidelink communication or for SL-SRB4 of NR sidelink discovery, it is initially set to the SN of the first received UMD PDU containing an SN. For the receiving UM RLC entity configured for MCCH or MTCH, it is initially set to the SN of the first received UMD PDU containing an SN.

END OF CHANGES