TSG RAN WG1#10 Jan. 18-21 2000

Tdoc R1-00-0111

Beijing, China

Source: TSG RAN WG1 Chairman

### Report from TSG RAN#6

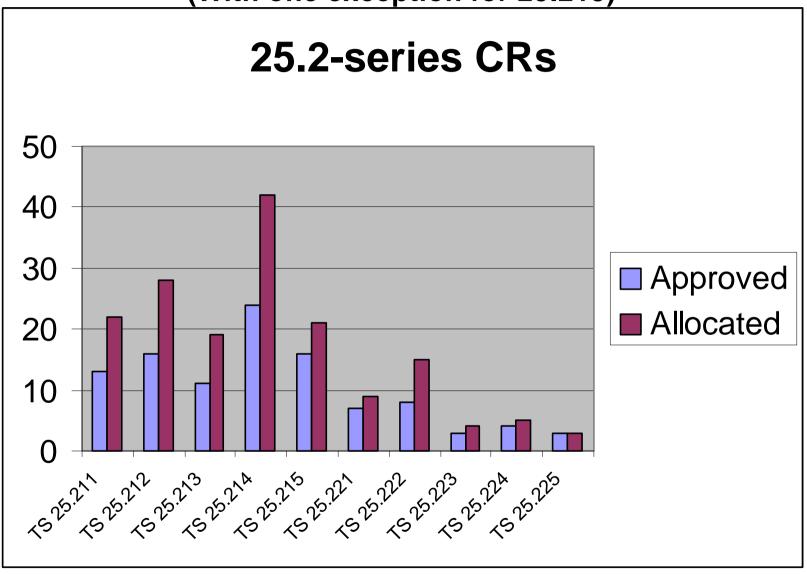
TSG RAN WG1 Chairman
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### WG1 CRsapproved by TSG RAN

(With one exception for 25.215)





## Rel.99 Open Items: Compressed mode with puncturing

- Deadline extended until 3/2000
- WG1 shall discuss the issue and report the matter to TSG RAN#7 (If not solved then full story to be reported inlouding performance & complexity issues etc. If WG1 has agreement then just related CRs to TSG RAN)



### Rel. 99 Open Items: CPCH

- Deadline extended until 03/2000
- RAN Decision: Optional both UE and Node B in Release -99 (WGs not to discuss the optionality issue)
- Note large number of specifications affected across TSG RAN WGs
  - -> Communications of WG1 outcome needed for WG2



### Rel. 99 Open Items: DPCCH Gating

- Postponed to Release 2000
- WG1 views needed what should be the milestone in the workplan in WG1 (proposed 6/2000)



#### Rel.99 Small size turbo interleavers

- Deadline extended to 03/2000
- Other WGs need to know the parameters I.e. the interleaver sizes below 320 asap for the interfaces
  - 160,80,40...?



### Rel.99 Open Items: Out-of-synch handling

- M ilestone extended to 03/2000
- Both FDD and TDD
  - Noted after last WG1, no discussion in TSG RAN
- With TDD missing fully



### Rel.99 Open Items: CPICH SIR

- M ilestone extended for 03/2000
- No actions for WG1, TSG RAN WG4 will make the conclusions as they have the competence on the simulations side

 CR 25.215-017rev1 is pending with decisions depending on WG4 outcome



# Rel.99 Open Items: TDD Cell Parameter Cycling

M ilestone extended for 03/2000



## Rel.99 Open Items: Uplink Synchrous transmission (FDD)

- Postponed to Release 2000
- M ain reasons was the missing support in other W G s.
- Proposal needed for the milestones in workplan for Release -2000



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### Rel.99 Open Items: Zero bits parity bit attachement

- M ilestone extended for 3/2000
- Needs to be commnicated with TSG RAN WG2 what is supposed to be done



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### Annex: RAN WG1 meetings year 2000

- WG1#10 Jan 18-21 (China, Host: Nokia China)
- WG1#11 Feb 29-March 3 (USA, Host: T1P1)
- WG1#12 April 10-14 (Korea, Host: TTA)
- WG1#13 May 22-26 (Japan)
- WG1#14 July 3-7 (Finland, Host: Nokia)
- WG1#15 August 21-25 (Germany, Host: Siemens)
- WG1#16 October 9-13 (Korea, Host: TTA)
- WG1#17 November 20-24 (TBD)
- Note: Dates indicate the week, meeting duration 4 days



## RAN WG1 Specifications & Technical reports

WG1 specifications are version 3.1.0,

 The WG1 technical report were presented for information, the TR on multiplexing and channel coding examples expected to be presented for TSG RAN for approval 3/2000. The report has a new number, TR 25.944



### Annex: RAN WG1 year 2000 Schedule 1/3

- TDD cell parameter cycling: 3/2000
- Out-of-synchronisation refinements: 3/2000
- Small turbo block sizes: 3/2000
- Compressed mode with puncturing: 3/2000
- CPCH issues: 3/2000
- <u>DPCCH gating: 3/2000 -> New milestone needed as part of Release 2000 (Agreed as Release 2000 work item in TSG RAN) (Proposed 06/2000)</u>
- Zero bits parity bit attachment 03/2000



### Annex: RAN WG1 year 2000 Schedule 2/3

- -Hybrid ARQ: Conclusions by 6/2000, Updates to the specifications completed by 9/2000
- TX diversity refinements completed by 9/2000
- -TDD 1.28 M chips/s by 9/2000 -> AH 01 to discuss the details
  - WG1 should allow time for WG4 to work with the issue. (06/2000 principles, CRs or new specs 09/2000)
- -FAUSCH changes completed by 9/2000



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### Annex: RAN WG1 year 2000 Schedule 3/3

- Uplink Synchronous Transmission Details (FDD) ->
   Needs new milestone
- -Positioning (TDD + (FDD refinements)

