TSGR1#9(99)L6

3GPP TSG-RAN WG1#9 Dresden, Germany, 30 November - 3 December 1999

Location Services
TSG RAN WG1
Draft Liaison statement on the inclusion of IPDL in the layer 1 specifications.
TSG RAN WG2

TSG RAN WG1 has approved a change request (a copy of which is attached below) to include the IPDL concept into the layer 1 specifications. In the process of doing this some changes were made to the IPDL parameter list and to the exact use of the parameters. In particular the changes are:

The 'Max_dev' parameter was removed as it can be derived from the other parameters and is redundant. A 'IP_offset' parameter was added to allow all the sectors of the one node B to have synchronised idle periods.

The formula for calculating the idle period position was also modified to reflect these parameter changes.

WG1 requests that WG2 review the changes made to the WG1 specifications and to update the WG2 documentation to realign the parameter list and to remove any duplication of information.

Document R1#9(99)k16 e.g. for 3GPP use the format TP-99xxx or for SMG, use the format P-99-xxx

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10 Idle Periods for IPDL Location method.

To support time difference measurements that need to be made for location services there needs to be Idle Periods created in the DownLink (IPDL) during which time all channels from a node B are temporally seized. During these Idle Periods the visibility of neighbour basestations from the UE is improved thus allowing the measurements to be performed.

The Idle Periods are arranged in a predetermined pseudo random fashion according to higher layer parameters, these parameters are used by layer 1 to arrange and use these Idle Periods. Idle Periods differ from compressed mode in that they are shorter in duration, all channels are silent simultaneously, and no attempt is made to prevent data loss.

In general there are two modes for these Idle Periods:

- Continuous mode, and
- Burst mode

In continuous mode the Idle Periods are active all the time. In burst mode the Idle Periods are arranged in bursts where each burst contains enough Idle Periods to allow a UE to make sufficient measurements for its location to be calculated. The bursts are separated by a period where no Idle Periods occur.

10.1 Parameters of IPDL

The follow parameters are signalled to the UE via higher layers:

IP Status: This is a logic value that indicates if the Idle Periods are arranged in continuous or burst mode. **IP** Spacing: The number of 10ms frames between the start of a frame that contains an Idle Period and the next frame that contains an Idle Period. (Note that there is at most one Idle Period in a frame) **IP Length:** The length of the Idle Periods, expressed in symbols of the CPICH. **IP** offset: A cell specific offset (can be used to synchronise Idle Periods from different sectors within a node B). Seed: A seed for a pseudo random number generator. Additionally in the case of burst mode operation the following parameters are also communicated to the UE. **Burst Start:** The SFN where the first burst of Idle Periods starts. Burst Length: The number of Idle Periods in a burst of Idle Periods. The number of frames of the primary CPICH between the start of a burst and the start of the next burst. **Burst_Freq:**

10.2 Calculation of Idle Period Position

The position of the x^{th} Idle Period relative to the start of a burst, expressed in symbols of the CPICH, is given by the formula (assuming the Idle Periods are indexed from 1, i.e. the first Idle Period is x=1 etc):

 $x * IP _ Spacing * 150 + rand(x \mod 64) \mod Max _ dev + IP _ offset$

where : $Max_dev = 150 - IP_Length$, $rand(n) = (106 * rand(n-1) + 1283) \mod 6075$, and rand(0) = Seed

Continuous mode can be considered as a specific case of the burst mode with just one burst spanning the whole SFN cycle. Note also that x will be reset to x=1 for the first idle period in a SFN cycle for both continuous and burst modes and will also, in the case of burst mode, be reset for the first Idle Period in every burst.

Figure 10.1 below illustrates the Idle Periods for the Burst Mode case.

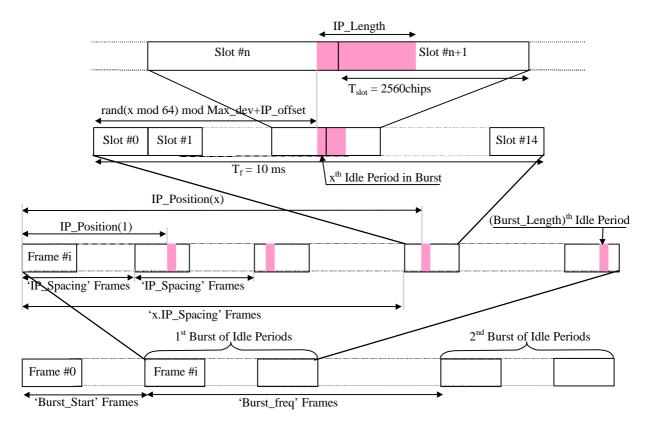


Figure 10.1: Idle Period placement in the case of burst mode operation.