Document R1-99L21 e.g. for 3GPP use the format TP-99xxx or for SMG, use the format P-99-xxx

		CHANGE I	REQI	JEST			e at the bottom of th o fill in this form cor	
		25.211	CR	009R1	Currei	nt Versio	n: <mark>3.0.0</mark>	
GSM (AA.BB) or 3	G (AA.BBB) specifica	ation number \uparrow		ר CR חנ	umber as allocate	d by MCC si	upport team	
For submission to:TSG-RAN #6for approvalXstrategic(for SMGlist expected approval meeting # here ↑for informationfor informationnon-strategicuse only)								
Fo Proposed chan (at least one should be	nge affects:	sion 2 for 3GPP and SMG	The latest		is available from: ftf		//Information/CR-Form	
Source:	Nokia					Date:	2.12.99	
Subject:	20 ms RAC	H message length	า					
Work item:								
(only one category shall be marked	B Addition of	modification of fe		rlier release		lease:	Phase 2 Release 96 Release 97 Release 98 Release 99 Release 00	X
<u>Reason for</u> change:		20 ms RACH mes documents R1-9				NWG1#8	3 (New York 1	2
Clauses affecte	ed: 5.2.2.1	.1 and 5.2.2.1.3						
<u>Other specs</u> affected:	Other 3G cor Other GSM c specificati MS test spec BSS test spe O&M specific	ions ifications cifications	-	$\begin{array}{l} \rightarrow \ \text{List of CF} \\ \rightarrow \ \text{List of CF} \end{array}$	Rs:			
<u>Other</u> comments:								

5.2.2 Common uplink physical channels

5.2.2.1 Physical Random Access Channel (PRACH)

The Physical Random Access Channel (PRACH) is used to carry the RACH.

5.2.2.1.1 RACH transmission

The random-access transmission is based on a Slotted ALOHA approach with fast acquisition indication. The UE can start the transmission at a number of well-defined time-offsets, denoted *access slots*. There are 15 access slots per two frames and they are spaced 5120 chips apart. Timing information on the access slots and the acquisition indication is given in section 7.3. Figure 3 shows the access slot numbers and their spacing to each other. Information on what access slots are available in the current cell is given by higher layers.

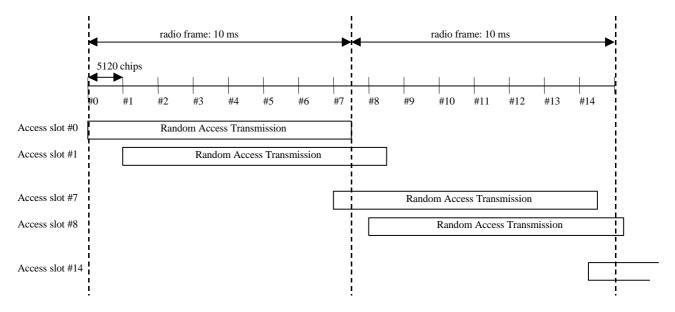


Figure 3: RACH access slot numbers and their spacing

The structure of the random-access transmission is shown in Figure 4. The random-access transmission consists of one or several *preambles* of length 4096 chips and a *message* of length 10 or 20 ms. The UE indicates the length of the message part to the network by using specific signatures and/or access slots. The assignment, which signatures and/or access slots are used for which message length, is performed by higher layers.

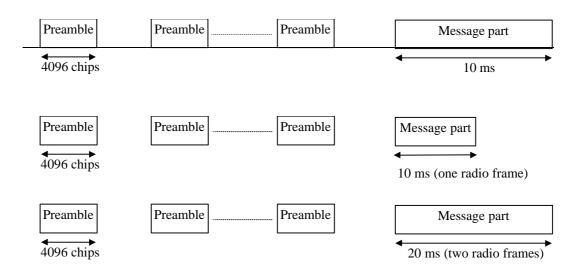


Figure 4: Structure of the random-access transmission.

5.2.2.1.2 RACH preamble part

The preamble part of the random-access burst consists of 256 repetitions of a signature. There are a total of 16 different signatures, based on the Hadamard code set of length 16 (see [4] for more details).

5.2.2.1.3 RACH message part

Figure 5 shows the structure of the Random-access message part radio frame. The 10 ms message part radio frame is split into 15 slots, each of length $T_{slot} = 2560$ chips. Each slot consists of two parts, a data part that carries Layer 2 information and a control part that carries Layer 1 control information. The data and control parts are transmitted in parallel. A 20 ms long message part consists of two consecutive message part radio frames.

The data part consists of $10*2^k$ bits, where k=0,1,2,3. This corresponds to a spreading factor of 256, 128, 64, and 32 respectively for the message data part.

The control part consists of 8 known pilot bits to support channel estimation for coherent detection and 2 TFCI bits. This corresponds to a spreading factor of 256 for the message control part. The pilot bit pattern is described in table 8. The total number of TFCI bits in the random-access message is 15*2 = 30. The TFCI value corresponds to a certain transport format of the current Random-access message.

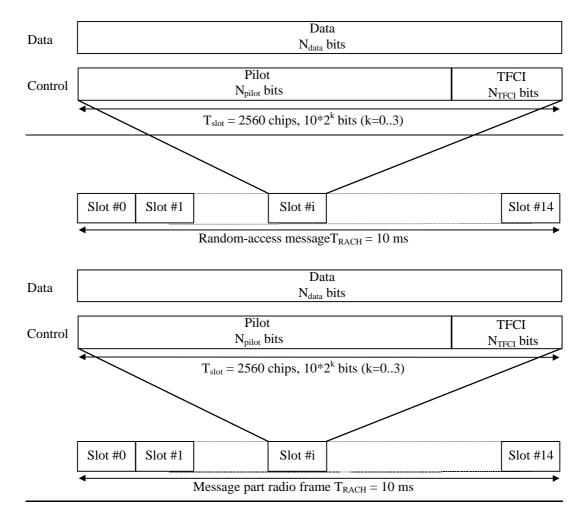


Figure 5: Structure of the random-access message part radio frame

Slot Format #i	Channel Bit Rate (kbps)	Channel Symbol Rate (ksps)	SF	Bits/ Frame	Bits/ Slot	N _{data}
0	15	15	256	150	10	10
1	30	30	128	300	20	20
2	60	60	64	600	40	40
3	120	120	32	1200	80	80

Table 6: Random-access message data fields.

 Table 7: Random-access message control fields

Slot Format #i	Channel Bit Rate (kbps)	Channel Symbol Rate (ksps)	SF	Bits/ Frame	Bits/ Slot	N _{pilot}	NTFCI
0	15	15	256	150	10	8	2

Table 8: Pilot bit	patterns for I	RACH message	part with	$N_{pilot} = 8.$
--------------------	----------------	--------------	-----------	------------------

	N _{pilot} = 8							
Bit #	0	1	2	3	4	5	6	7
Slot #0	1	1	1	1	1	1	1	0
1	1	0	1	0	1	1	1	0
2	1	0	1	1	1	0	1	1
3	1	0	1	0	1	0	1	0
4	1	1	1	0	1	0	1	1
5	1	1	1	1	1	1	1	0
6	1	1	1	1	1	0	1	0
7	1	1	1	0	1	0	1	0
8	1	0	1	1	1	1	1	0
9	1	1	1	1	1	1	1	1
10	1	0	1	1	1	0	1	1
11	1	1	1	0	1	1	1	1
12	1	1	1	0	1	0	1	0
13	1	0	1	0	1	1	1	1
14	1	0	1	0	1	1	1	1