

3GPP TSG RAN Meeting #28**RP-050276****Quebec, Canada, 1 - 3 June 2005****Title CRs to 34.123-1 for approval Batch 6****Source 3GPP TSG RAN WG5 (Testing)****Agenda Item 7.6.5**

WG Tdoc	Spec	CR	R	Cat	Rel	Curr Ver	Title	Work Item
R5-050700	34.123-1	1239	-	F	Rel-5	5.11.1	Tests for HCR TDD RAB combinations #38 thru #61	TEI
R5-050957	34.123-1	1240	-	F	Rel-5	5.11.1	Add TDD to RRC test case 8.4.1.33	TEI
R5-050958	34.123-1	1241	-	F	Rel-5	5.11.1	Correction to Package 4 RRC test case 8.4.1.26 to change TDD content	TEI
R5-050959	34.123-1	1242	-	F	Rel-5	5.11.1	Correction RRC test case 8.4.1.7A (TDD)	TEI
R5-050954	34.123-1	1243	-	F	Rel-5	5.11.1	Correction to Package 4 Inter system cell reselection test case 8.3.9.3	TEI
R5-050970	34.123-1	1244	-	F	Rel-5	5.11.1	Correction to GCF WI-014 MAC-HS test case 7.1.5.1	TEI
R5-050971	34.123-1	1245	-	F	Rel-5	5.11.1	Correction to GCF WI-014 MAC-HS test case 7.1.5.4	TEI
R5-050972	34.123-1	1246	-	F	Rel-5	5.11.1	Correction to GCF WI-014 MAC-HS test case 7.1.5.5	TEI
R5-050985	34.123-1	1247	-	F	Rel-5	5.11.1	Correction to GCF WI-10 NAS Test Cases 12.2.1.2, 12.2.1.5a Proc 1, 12.2.1.5a Proc 2, 12.3.2.7, 12.4.1.2 and 12.6.1.2	TEI
R5-050876	34.123-1	1248	-	F	Rel-5	5.11.1	CR to 34.123-1 Rel-5: Correction to WI-012 RLC test case 7.2.3.28	TEI
R5-050878	34.123-1	1249	-	F	Rel-5	5.11.1	Corrections to WI-10 P4 approved GMM test case 12.2.1.5a Test Procedures 1 & 2	TEI

CHANGE REQUEST

34.123-1 CR 1239 rev - Current version: 5.11.1

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the symbols.

Proposed change affects: | UICC apps ME Radio Access Network Core Network


Title:	Tests for HCR TDD RAB combinations #38 thru #61		
Source:	3GPP TSG RAN WG5 (Testing)		
Work item code:	TEI	Date:	13/04/05
Category:	F	Release:	Rel-5
	<i>Use one of the following categories:</i> F (correction) A (corresponds to a correction in an earlier release) B (addition of feature), C (functional modification of feature) D (editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900 .		<i>Use one of the following releases:</i> 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6)

Reason for change:	Need to be able to test the HCR TDD RAB combinations in the 34.108 document
Summary of change:	Add to section 18.2 Radio Bearer Tests for 3.84 Mcps TDD option 18.2.2.38 thru 18.2.2.61.2.4.
Consequences if not approved:	Will not be able to test the HCR TDD RABs in 34.108 without this change

Clauses affected:	18.2.2										
Other specs affected:	<table border="1" style="display: inline-table; border-collapse: collapse;"> <tr> <td style="padding: 2px;">Y</td> <td style="padding: 2px;">N</td> </tr> <tr> <td style="text-align: center; padding: 2px;">X</td> <td style="padding: 2px;"></td> </tr> <tr> <td style="text-align: center; padding: 2px;">X</td> <td style="padding: 2px;"></td> </tr> <tr> <td style="text-align: center; padding: 2px;">X</td> <td style="padding: 2px;"></td> </tr> </table>	Y	N	X		X		X		Other core specifications	
	Y	N									
	X										
X											
X											
X	Test specifications										
X	O&M Specifications										
Other comments:											

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <http://www.3gpp.org/specs/CR.htm>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked  contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <ftp://ftp.3gpp.org/specs/>. For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

18.2.2.38 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:32 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH

18.2.2.38.1 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:32 DL:8 kbps / PS RAB / Payload 320

18.2.2.38.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.38.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38 for the uplink payload 320 case.

18.2.2.38.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	TFI	RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (32 kbps)	DCCH
TFS	TF0, bits	0x81	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x103	1x60	1x336	1x148
	TF2, bits	1x81	N/A	N/A	2x336	N/A

Uplink TFCs:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF1, TF0, TF0)
UL_TFC3	(TF0, TF0, TF0, TF1, TF0)
UL_TFC4	(TF1, TF0, TF0, TF1, TF0)
UL_TFC5	(TF2, TF1, TF1, TF1, TF0)
UL_TFC6	(TF0, TF0, TF0, TF2, TF0)
UL_TFC7	(TF1, TF0, TF0, TF2, TF0)
UL_TFC8	(TF2, TF1, TF1, TF2, TF0)
UL_TFC9	(TF0, TF0, TF0, TF0, TF1)
UL_TFC10	(TF1, TF0, TF0, TF0, TF1)
UL_TFC11	(TF2, TF1, TF1, TF0, TF1)
UL_TFC12	(TF0, TF0, TF0, TF1, TF1)
UL_TFC13	(TF1, TF0, TF0, TF1, TF1)
UL_TFC14	(TF2, TF1, TF1, TF1, TF1)
UL_TFC15	(TF0, TF0, TF0, TF2, TF1)
UL_TFC16	(TF1, TF0, TF0, TF2, TF1)
UL_TFC17	(TF2, TF1, TF1, TF2, TF1)

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (8 kbps)	DCCH
TFS	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x103	1x60	1x336	1x148
	TF2, bits	1x81	N/A	N/A	N/A	N/A

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>DL_TFC6</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC7</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC8</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>DL_TFC9</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC10</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC11</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	DL_TFC1 DL_TFC7	UL_TFC1 UL_TFC10	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC1, UL_TFC9, UL_TFC10	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL_TFC2 DL_TFC8	UL_TFC2 UL_TFC11	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC2, UL_TFC9, UL_TFC11	RB5: 81 RB6: 103 RB7: 60 RB8: 632	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL_TFC3 DL_TFC9	UL_TFC3 UL_TFC12	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC3, UL_TFC9, UL_TFC12	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL_TFC4 DL_TFC10	UL_TFC4 UL_TFC13	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC4, UL_TFC9, UL_TFC10, UL_TFC12, UL_TFC13	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL_TFC5 DL_TFC11	UL_TFC5 UL_TFC14	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC2, UL_TFC3, UL_TFC5, UL_TFC9, UL_TFC11, UL_TFC12, UL_TFC14	RB5: 81 RB6: 103 RB7: 60 RB8: 632	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL_TFC3 DL_TFC9	UL_TFC6 UL_TFC15	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC6, UL_TFC9, UL_TFC15	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 312
7	DL_TFC4 DL_TFC10	UL_TFC7 UL_TFC16	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC1, UL_TFC6, UL_TFC7, UL_TFC9, UL_TFC10, UL_TFC15, UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 312
8	DL_TFC5 DL_TFC11	UL_TFC8 UL_TFC17	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC2, UL_TFC6, UL_TFC8, UL_TFC9, UL_TFC11, UL_TFC15, UL_TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 312

NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.
RB8: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the uplink TTI for RB8 is 20 ms while the downlink TTI is 40 ms then, to achieve continuous data transmission in uplink the size of the uplink RLC SDU has been set such that it will be transmitted over two subsequent TTIs, i.e. UL RLC SDU SIZE has been set to two times the uplink TFS size minus 8 (the size of a 7 bit length indicator and expansion bit).

18.2.2.38.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub test.

3. At step 15 the UE shall return

- for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
- for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
- for sub-test 3: an RLC SDU on RB8 having the same content as 2 times plus 8 lsb's of the DL RLC SDU sent by the SS in the downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 4: an RLC SDU on RB5 having the same content as sent by SS and on RB8 having the same content as 2 times plus 8 lsb's of the DL RLC SDU sent by the SS in the downlink; and no data shall be received on RB6 and RB7.
- for sub-test 5: an RLC SDU on RB5, RB6, RB7 having the same content as sent by SS and on RB8 having the same content as 2 times plus 8 lsb's of the DL RLC SDU sent by the SS in the downlink.
- for sub-test 6: an RLC SDU on RB8 having the same content as 4 times plus 24 lsb's of the DL RLC SDU sent by the SS in the downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 7: an RLC SDU on RB5 having the same content as sent by SS and on RB8 having the same content as 4 times plus 24 lsb's of the DL RLC SDU sent by the SS in the downlink; and no data shall be received on RB6 and RB7.
- for sub-test 8: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS and on RB8 having the same content as 4 times plus 24 lsb's of the DL RLC SDU sent by the SS in the downlink.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38.2 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:32 DL:8 kbps / PS RAB / Payload 128

18.2.2.38.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.38.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38 for the uplink payload 128 case.

18.2.2.38.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(32 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>5x144</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>UL_TFC9</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC10</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC11</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>UL_TFC12</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC13</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC14</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>UL_TFC15</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC17</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(8 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>DL_TFC6</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC7</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC8</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>DL_TFC9</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC10</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC11</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	DL_TFC1 DL_TFC7	UL_TFC1 UL_TFC10	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC1, UL_TFC9, UL_TFC10	RB5: 39 RB6: 103 RB7: 60 RB8: 248	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL_TFC2 DL_TFC8	UL_TFC2 UL_TFC11	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC2, UL_TFC9, UL_TFC11	RB5: 81 RB6: 103 RB7: 60 RB8: 248	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL_TFC3 DL_TFC9	UL_TFC3 UL_TFC12	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC3, UL_TFC9, UL_TFC12	RB5: 39 RB6: 103 RB7: 60 RB8: 248	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL_TFC4 DL_TFC10	UL_TFC4 UL_TFC13	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC4, UL_TFC9, UL_TFC10, UL_TFC12, UL_TFC13	RB5: 39 RB6: 103 RB7: 60 RB8: 248	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL_TFC5 DL_TFC11	UL_TFC5 UL_TFC14	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC2, UL_TFC3, UL_TFC5, UL_TFC9, UL_TFC11, UL_TFC12, UL_TFC14	RB5: 81 RB6: 103 RB7: 60 RB8: 248	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL_TFC3 DL_TFC9	UL_TFC6 UL_TFC15	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC6, UL_TFC9, UL_TFC15	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 312
7	DL_TFC4 DL_TFC10	UL_TFC7 UL_TFC16	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC1, UL_TFC6, UL_TFC7, UL_TFC9, UL_TFC10, UL_TFC15, UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 312
8	DL_TFC5 DL_TFC11	UL_TFC8 UL_TFC17	DL_TFC0, DL_TFC6, UL_TFC0, UL_TFC9	UL_TFC0, UL_TFC2, UL_TFC6, UL_TFC8, UL_TFC9, UL_TFC11, UL_TFC15, UL_TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 312

NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.
RB8: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the uplink TTI for RB8 is 20 ms while the downlink TTI is 40 ms then, to achieve continuous data transmission in uplink the size of the uplink RLC SDU has been set such that it will be transmitted over two subsequent TTIs, i.e. UL RLC SDU SIZE has been set to two times the uplink TFS size minus 8 (the size of a 7 bit length indicator and expansion bit).

18.2.2.38.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub test.

3. At step 15 the UE shall return

- for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
- for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
- for sub-test 3: an RLC SDU on RB8 having the first 248 bits equal to the content sent by the SS in the downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 4: an RLC SDU on RB5 having the same content as sent by SS and on RB8 having the first 248 bits equal to the content sent by the SS in the downlink; and no data shall be received on RB6 and RB7.
- for sub-test 5: an RLC SDU on RB5, RB6, and RB7 having the same content as sent by SS and on RB8 having the first 248 bits equal to the content sent by the SS in the downlink.
- for sub-test 6: an RLC SDU on RB8 having the same content as 4 times plus 24 lsb's of the DL RLC SDU sent by the SS in the downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 7: an RLC SDU on RB5 having the same content as sent by SS and on RB8 having the same content as 4 times plus 24 lsb's of the DL RLC SDU sent by the SS in the downlink; and no data shall be received on RB6 and RB7.
- for sub-test 8: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS and on RB8 having the same content as 4 times plus 24 lsb's of the DL RLC SDU sent by the SS in the downlink.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38a Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:0 DL:0 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.38a.1 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:0 DL:0 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload size 320.

18.2.2.38a.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.38a.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38a for the uplink payload 320 case.

18.2.2.38a.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (0 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>N/A</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(0 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>N/A</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Downlink TFCs:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC4</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC5</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
<u>1</u>	<u>DL_TFC1</u> <u>DL_TFC4</u>	<u>UL_TFC1</u> <u>UL_TFC4</u>	<u>DL_TFC0, DL_TFC3,</u> <u>UL_TFC0, UL_TFC3,</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 0</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>2</u>	<u>DL_TFC2</u> <u>DL_TFC5</u>	<u>UL_TFC2</u> <u>UL_TFC5</u>	<u>DL_TFC0, DL_TFC3,</u> <u>UL_TFC0, UL_TFC3,</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC5</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 0</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: No data</u>

NOTE 1: UL_TFC0, UL_TFC1, UL_TFC2 and UL_TFC3 are part of minimum set of TFCIs.

NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.

18.2.2.38a.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38a.2 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:0 DL:0 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload size 128.

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38a for the uplink payload 128 case.

See test case 18.2.2.38a.1 for test procedure and test requirement.

Except Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (0 kbps)</u>	<u>DCCH</u>
TFS	<u>TF0, bits</u>	0x81	0x103	0x60	0x144	0x148
	<u>TF1, bits</u>	1x39	1x103	1x60	N/A	1x148
	<u>TF2, bits</u>	1x81	N/A	N/A	N/A	N/A

18.2.2.38b Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.38b.1 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320, TTI 40 ms.

18.2.2.38b.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.38b.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38b for the uplink payload 320, and uplink TTI 40 ms case.

18.2.2.38b.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (8 kbps)</u>	<u>DCCH</u>
TFS	<u>TF0, bits</u>	0x81	0x103	0x60	0x336	0x148
	<u>TF1, bits</u>	1x39	1x103	1x60	1x336	1x148
	<u>TF2, bits</u>	1x81	N/A	N/A	N/A	N/A

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>UL_TFC9</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC10</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC11</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (8 kbps)	DCCH
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Downlink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>DL_TFC6</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC7</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC8</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>DL_TFC9</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC10</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC11</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
1	<u>DL_TFC1</u> <u>DL_TFC7</u>	<u>UL_TFC1</u> <u>UL_TFC7</u>	<u>DL_TFC0, DL_TFC6,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC7</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 312</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
2	<u>DL_TFC2</u> <u>DL_TFC8</u>	<u>UL_TFC2</u> <u>UL_TFC8</u>	<u>DL_TFC0, DL_TFC6,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC8</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 312</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: No data</u>
3	<u>DL_TFC3</u> <u>DL_TFC9</u>	<u>UL_TFC3</u> <u>UL_TFC9</u>	<u>DL_TFC0, DL_TFC6,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC9</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 312</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312</u>
4	<u>DL_TFC4</u> <u>DL_TFC10</u>	<u>UL_TFC4</u> <u>UL_TFC10</u>	<u>DL_TFC0, DL_TFC6,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC9,</u> <u>UL_TFC10</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 312</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312</u>
5	<u>DL_TFC5</u> <u>DL_TFC11</u>	<u>UL_TFC5</u> <u>UL_TFC11</u>	<u>DL_TFC0, DL_TFC6,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC9,</u> <u>UL_TFC11</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 312</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 312</u>
<p><u>NOTE 1: UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3 and UL_TFC6 are part of minimum set of TFCs.</u></p> <p><u>NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u></p> <p><u>RB8: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB8 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.38b.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.

- for sub-test 3: an RLC SDU on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 4: an RLC SDU on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 5: an RLC SDU on RB5, RB6, RB7 and RB8 having the same content as sent by SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38b.2 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128, TTI 80 ms.

18.2.2.38b.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.38b.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38b for the uplink payload 128, and uplink TTI 80 ms case.

18.2.2.38b.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (8 kbps)</u>	<u>DCCH</u>
TFS	TF0, bits	0x81	0x103	0x60	0x144	0x148
	TF1, bits	1x39	1x103	1x60	1x144	1x148
	TF2, bits	1x81	N/A	N/A	5x144	N/A

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF1, TF0, TF0)
UL_TFC3	(TF0, TF0, TF0, TF1, TF0)
UL_TFC4	(TF1, TF0, TF0, TF1, TF0)
UL_TFC5	(TF2, TF1, TF1, TF1, TF0)
UL_TFC6	(TF0, TF0, TF0, TF2, TF0)
UL_TFC7	(TF1, TF0, TF0, TF2, TF0)
UL_TFC8	(TF2, TF1, TF1, TF2, TF0)
UL_TFC9	(TF0, TF0, TF0, TF0, TF1)
UL_TFC10	(TF1, TF0, TF0, TF0, TF1)
UL_TFC11	(TF2, TF1, TF1, TF0, TF1)
UL_TFC12	(TF0, TF0, TF0, TF1, TF1)
UL_TFC13	(TF1, TF0, TF0, TF1, TF1)
UL_TFC14	(TF2, TF1, TF1, TF1, TF1)
UL_TFC15	(TF0, TF0, TF0, TF2, TF1)
UL_TFC16	(TF1, TF0, TF0, TF2, TF1)
UL_TFC17	(TF2, TF1, TF1, TF2, TF1)

Downlink TFS:

		<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (8 kbps)</u>	<u>DCCH</u>
TFS	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x103	1x60	1x336	1x148

	TF2, bits	1x81	N/A	N/A	N/A	N/A
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[Downlink TFCS:](#)

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0, TF0, TF0, TF0, TF0)
DL_TFC1	(TF1, TF0, TF0, TF0, TF0)
DL_TFC2	(TF2, TF1, TF1, TF0, TF0)
DL_TFC3	(TF0, TF0, TF0, TF1, TF0)
DL_TFC4	(TF1, TF0, TF0, TF1, TF0)
DL_TFC5	(TF2, TF1, TF1, TF1, TF0)
DL_TFC6	(TF0, TF0, TF0, TF0, TF1)
DL_TFC7	(TF1, TF0, TF0, TF0, TF1)
DL_TFC8	(TF2, TF1, TF1, TF0, TF1)
DL_TFC9	(TF0, TF0, TF0, TF1, TF1)
DL_TFC10	(TF1, TF0, TF0, TF1, TF1)
DL_TFC11	(TF2, TF1, TF1, TF1, TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	<u>DL TFC1</u> <u>DL TFC7</u>	<u>UL TFC1</u> <u>UL TFC10</u>	<u>DL TFC0, DL TFC6,</u> <u>UL TFC0, UL TFC9</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC9,</u> <u>UL TFC10</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 56</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
2	<u>DL TFC2</u> <u>DL TFC8</u>	<u>UL TFC2</u> <u>UL TFC11</u>	<u>DL TFC0, DL TFC6,</u> <u>UL TFC0, UL TFC9</u>	<u>UL TFC0,</u> <u>UL TFC2,</u> <u>UL TFC9,</u> <u>UL TFC11</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 56</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: No data</u>
3	<u>DL TFC3</u> <u>DL TFC9</u>	<u>UL TFC3</u> <u>UL TFC12</u>	<u>DL TFC0, DL TFC6,</u> <u>UL TFC0, UL TFC9</u>	<u>UL TFC0,</u> <u>UL TFC3,</u> <u>UL TFC9,</u> <u>UL TFC12</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 56</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312</u>
4	<u>DL TFC4</u> <u>DL TFC10</u>	<u>UL TFC4</u> <u>UL TFC13</u>	<u>DL TFC0, DL TFC6,</u> <u>UL TFC0, UL TFC9</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC9,</u> <u>UL TFC10,</u> <u>UL TFC12,</u> <u>UL TFC13</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 56</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312</u>
5	<u>DL TFC5</u> <u>DL TFC11</u>	<u>UL TFC5</u> <u>UL TFC14</u>	<u>DL TFC0, DL TFC6,</u> <u>UL TFC0, UL TFC9</u>	<u>UL TFC0,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC5,</u> <u>UL TFC9,</u> <u>UL TFC11,</u> <u>UL TFC12,</u> <u>UL TFC14</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 56</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 312</u>
6	<u>DL TFC3</u> <u>DL TFC9</u>	<u>UL TFC6</u> <u>UL TFC15</u>	<u>DL TFC0, DL TFC6,</u> <u>UL TFC0, UL TFC9</u>	<u>UL TFC0,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC15</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 312</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312</u>
7	<u>DL TFC4</u> <u>DL TFC10</u>	<u>UL TFC7</u> <u>UL TFC16</u>	<u>DL TFC0, DL TFC6,</u> <u>UL TFC0, UL TFC9</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC6,</u> <u>UL TFC7,</u> <u>UL TFC9,</u> <u>UL TFC10,</u> <u>UL TFC15,</u> <u>UL TFC16</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 312</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312</u>
8	<u>DL TFC5</u> <u>DL TFC11</u>	<u>UL TFC8</u> <u>UL TFC17</u>	<u>DL TFC0, DL TFC6,</u> <u>UL TFC0, UL TFC9</u>	<u>UL TFC0,</u> <u>UL TFC2,</u> <u>UL TFC6,</u> <u>UL TFC8,</u> <u>UL TFC9,</u> <u>UL TFC11,</u> <u>UL TFC15,</u> <u>UL TFC17</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 312</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 312</u>

NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.
 RB8: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the uplink TTI for RB8 is 80 ms while the downlink TTI is 40 ms then, to achieve continous data transmission in uplink the size of the uplink RLC SDU has been set such that 2 of them will be transmitted over a TTI, i.e. UL RLC SDU SIZE has been set to ½ the uplink TFS size minus 8 (the size of a 7 bit length indicator and expansion bit) in the next smaller whole numbers of octets.

18.2.2.38b.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.

2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.

3. At step 15a and step 15b the UE shall return

- for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
- for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
- for sub-test 3: an RLC SDU on RB8 having the same content as the first 56 lsb's sent by SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 4: an RLC SDU on RB5 having the same content as sent by SS and on RB8 having the same content as the first 56 lsb's sent by the SS in the downlink; and no data shall be received on RB6 and RB7.
- for sub-test 5: an RLC SDU on RB5, RB6, and RB7 having the same content as sent by SS and on RB8 having same content as the first 56 lsb's sent by the SS in the downlink.
- for sub-test 6: an RLC SDU on RB8 having the same content as the DL RLC SDU sent by the SS in the downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 7: an RLC SDU on RB5 having the same content as sent by SS and on RB8 having the same content as the DL RLC SDU sent by the SS in the downlink; and no data shall be received on RB6 and RB7.
- for sub-test 8: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS and on RB8 having the same content as the DL RLC SDU sent by the SS in the downlink.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38c Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:32 DL:32 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.38c.1 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:32 DL:32 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320.

18.2.2.38c.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.38c.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38c for the uplink payload 320 case.

18.2.2.38c.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (32 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>UL_TFC9</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC10</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC11</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>UL_TFC12</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC13</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC14</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>UL_TFC15</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(32 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>DL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>DL_TFC9</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC10</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC11</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>DL_TFC12</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC13</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC14</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>DL_TFC15</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC17</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
1	DL_TFC1 , DL_TFC10	UL_TFC1 , UL_TFC10	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC10	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL_TFC2 , DL_TFC11	UL_TFC2 , UL_TFC11	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC11	RB5: 81 RB6: 103 RB7: 60 RB8: 312	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL_TFC3 , DL_TFC12	UL_TFC3 , UL_TFC12	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC12	RB5: 81 RB6: 103 RB7: 60 RB8: 312	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL_TFC4 , DL_TFC13	UL_TFC4 , UL_TFC13	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC4 , UL_TFC9 , UL_TFC10 , UL_TFC12 , UL_TFC13	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL_TFC5 , DL_TFC14	UL_TFC5 , UL_TFC14	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC5 , UL_TFC9 , UL_TFC11 , UL_TFC12 , UL_TFC14	RB5: 81 RB6: 103 RB7: 60 RB8: 312	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL_TFC6 , DL_TFC15	UL_TFC6 , UL_TFC15	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC9 , UL_TFC15	RB5: 81 RB6: 103 RB7: 60 RB8: 632	RB5: No data RB6: No data RB7: No data RB8: 632
7	DL_TFC7 , DL_TFC16	UL_TFC7 , UL_TFC16	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC7 , UL_TFC9 , UL_TFC10 , UL_TFC15 , UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: 39 RB6: No data RB7: No data RB8: 632

NOTE 1: [UL_TFC0](#), [UL_TFC1](#), [UL_TFC2](#), [UL_TFC3](#) and [UL_TFC15](#) are part of minimum set of TFCs.

NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.

[RB8](#): Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).

18.2.2.38c.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
 - for sub-test 3 and 6: an RLC SDU on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4 and 7: an RLC SDU on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 5 and 8: an RLC SDU on RB5, RB6, RB7 and RB8 having the same content as sent by SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38c.2 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:32 DL:32 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128.

18.2.2.38c.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.38c.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38c for the uplink payload 128 case.

18.2.2.38c.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (32 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>5x144</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>UL_TFC9</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC10</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC11</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>UL_TFC12</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC13</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC14</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>UL_TFC15</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC17</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(32 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>DL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>DL_TFC9</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC10</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC11</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>DL_TFC12</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC13</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC14</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>DL_TFC15</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC17</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
1	DL_TFC1 , DL_TFC10	UL_TFC1 , UL_TFC10	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC10	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL_TFC2 , DL_TFC11	UL_TFC2 , UL_TFC11	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC11	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL_TFC3 , DL_TFC12	UL_TFC3 , UL_TFC12	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC12	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL_TFC4 , DL_TFC13	UL_TFC4 , UL_TFC13	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC4 , UL_TFC9 , UL_TFC10 , UL_TFC12 , UL_TFC13	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL_TFC5 , DL_TFC14	UL_TFC5 , UL_TFC14	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC5 , UL_TFC9 , UL_TFC11 , UL_TFC12 , UL_TFC14	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL_TFC6 , DL_TFC15	UL_TFC6 , UL_TFC15	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC9 , UL_TFC15	RB5: 81 RB6: 103 RB7: 60 RB8: 632	RB5: No data RB6: No data RB7: No data RB8: 632
7	DL_TFC7 , DL_TFC16	UL_TFC7 , UL_TFC16	DL_TFC0 , DL_TFC9 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC7 , UL_TFC9 , UL_TFC10 , UL_TFC15 , UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: 39 RB6: No data RB7: No data RB8: 632

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
8	<u>DL TFC8,</u> <u>DL TFC17</u>	<u>UL TFC8,</u> <u>UL TFC17</u>	<u>DL TFC0,</u> <u>DL TFC9,</u> <u>UL TFC0,</u> <u>UL TFC9</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC6,</u> <u>UL TFC8,</u> <u>UL TFC9,</u> <u>UL TFC11,</u> <u>UL TFC15,</u> <u>UL TFC17</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 632</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 632</u>
<p>NOTE 1: <u>UL TFC0, UL TFC1, UL TFC2, , UL TFC3 and UL TFC15 are part of minimum set of TFCs.</u></p> <p>NOTE 2: <u>See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.38c.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
 - for sub-test 3: an RLC SDU on RB8 having the same content as the first 120 lsb's sent by SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4: an RLC SDU on RB5 having the same content as sent by SS and on RB8 having the same content as the first 120 lsb's sent by the SS in the downlink; and no data shall be received on RB6 and RB7.
 - for sub-test 5: an RLC SDU on RB5, RB6, and RB7 having the same content as sent by SS and on RB8 having same content as the first 120 lsb's sent by the SS in the downlink.
 - for sub-test 6: an RLC SDU on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 7: an RLC SDU on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 8: an RLC SDU on RB5, RB6, RB7 and RB8 having the same content as sent by SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38d Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.38d.1 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320.

18.2.2.38d.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.38d.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38d for the uplink payload 320 case.

18.2.2.38d.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 + RB9 (64 kbps, 20 ms TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x340</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x340</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x340</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>3x340</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>4x340</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8+RB9, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>UL_TFC9</u>	<u>(TF0, TF0, TF0, TF3, TF0)</u>
<u>UL_TFC10</u>	<u>(TF1, TF0, TF0, TF3, TF0)</u>
<u>UL_TFC11</u>	<u>(TF2, TF1, TF1, TF3, TF0)</u>
<u>UL_TFC12</u>	<u>(TF0, TF0, TF0, TF4, TF0)</u>
<u>UL_TFC13</u>	<u>(TF1, TF0, TF0, TF4, TF0)</u>
<u>UL_TFC14</u>	<u>(TF2, TF1, TF1, TF4, TF0)</u>
<u>UL_TFC15</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC17</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>UL_TFC18</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC19</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC20</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>UL_TFC21</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC22</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC23</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>
<u>UL_TFC24</u>	<u>(TF0, TF0, TF0, TF3, TF1)</u>
<u>UL_TFC25</u>	<u>(TF1, TF0, TF0, TF3, TF1)</u>
<u>UL_TFC26</u>	<u>(TF2, TF1, TF1, TF3, TF1)</u>
<u>UL_TFC27</u>	<u>(TF0, TF0, TF0, TF4, TF1)</u>
<u>UL_TFC28</u>	<u>(TF1, TF0, TF0, TF4, TF1)</u>
<u>UL_TFC29</u>	<u>(TF2, TF1, TF1, TF4, TF1)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8 + RB9</u> <u>(64 kbps,</u> <u>20 ms TT)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x340</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x340</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x340</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>3x340</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>4x340</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8+RB9, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>DL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>DL_TFC9</u>	<u>(TF0, TF0, TF0, TF3, TF0)</u>
<u>DL_TFC10</u>	<u>(TF1, TF0, TF0, TF3, TF0)</u>
<u>DL_TFC11</u>	<u>(TF2, TF1, TF1, TF3, TF0)</u>
<u>DL_TFC12</u>	<u>(TF0, TF0, TF0, TF4, TF0)</u>
<u>DL_TFC13</u>	<u>(TF1, TF0, TF0, TF4, TF0)</u>
<u>DL_TFC14</u>	<u>(TF2, TF1, TF1, TF4, TF0)</u>
<u>DL_TFC15</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC17</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>DL_TFC18</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC20</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>DL_TFC21</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC22</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC23</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>
<u>DL_TFC24</u>	<u>(TF0, TF0, TF0, TF3, TF1)</u>
<u>DL_TFC25</u>	<u>(TF1, TF0, TF0, TF3, TF1)</u>
<u>DL_TFC26</u>	<u>(TF2, TF1, TF1, TF3, TF1)</u>
<u>DL_TFC27</u>	<u>(TF0, TF0, TF0, TF4, TF1)</u>
<u>DL_TFC28</u>	<u>(TF1, TF0, TF0, TF4, TF1)</u>
<u>DL_TFC29</u>	<u>(TF2, TF1, TF1, TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits)</u> Note 1	<u>Test data size (bits)</u> Note 1
1	DL TFC1 , DL TFC16	UL TFC1 , UL TFC16	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC15 , UL TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 312 RB9: 312	RB5: 39 RB6: No data RB7: No data RB8: No data RB9: No data
2	DL TFC2 , DL TFC17	UL TFC2 , UL TFC17	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC15 , UL TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 312 RB9: 312	RB5: 81 RB6: 103 RB7: 60 RB8: No data RB9: No data
3	DL TFC3 , DL TFC18	UL TFC3 , UL TFC18	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC3 , UL TFC15 , UL TFC18	RB5: 39 RB6: 103 RB7: 60 RB8: 312 RB9: 312	RB5: No data RB6: No data RB7: No data RB8: 312 RB9: No data
4	DL TFC4 , DL TFC19	UL TFC4 , UL TFC19	DL TFC0 , DL TFC15 , DUL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC3 , UL TFC4 , UL TFC15 , UL TFC16 , UL TFC18 , UL TFC19	RB5: 39 RB6: 103 RB7: 60 RB8: 312 RB9: 312	RB5: 39 RB6: No data RB7: No data RB8: 312 RB9: No data
5	DL TFC5 , DL TFC20	UL TFC5 , UL TFC20	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC3 , UL TFC5 , UL TFC15 , UL TFC17 , UL TFC18 , UL TFC20	RB5: 81 RB6: 103 RB7: 60 RB8: 312 RB9: 312	RB5: 81 RB6: 103 RB7: 60 RB8: 312 RB9: No data
6	DL TFC6 , DL TFC21	UL TFC6 , UL TFC21	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC6 , UL TFC15 , UL TFC21	RB5: 39 RB6: 103 RB7: 60 RB8: 632 RB9: 632	RB5: No data RB6: No data RB7: No data RB8: 632 RB9: No data
7	DL TFC7 , DL TFC22	UL TFC7 , UL TFC22	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC6 , UL TFC7 , UL TFC15 , UL TFC16 , UL TFC21 , UL TFC22	RB5: 39 RB6: 103 RB7: 60 RB8: 632 RB9: 632	RB5: 39 RB6: No data RB7: No data RB8: 632 RB9: No data
8	DL TFC8 , DL TFC23	UL TFC8 , UL TFC23	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC6 , UL TFC8 , UL TFC15 , UL TFC17 , UL TFC21 , UL TFC23	RB5: 81 RB6: 103 RB7: 60 RB8: 632 RB9: 632	RB5: 81 RB6: 103 RB7: 60 RB8: 632 RB9: No data
9	DL TFC9 , DL TFC24	UL TFC9 , UL TFC24	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC9 , UL TFC15 , UL TFC24	RB5: 39 RB6: 103 RB7: 60 RB8: 952 RB9: 952	RB5: No data RB6: No data RB7: No data RB8: 952 RB9: No data

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits)</u> <u>Note 1</u>	<u>Test data size (bits)</u> <u>Note 1</u>
10	DL TFC10 , DL TFC25	UL TFC10 , UL TFC25	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC9 , UL TFC10 , UL TFC15 , UL TFC16 , UL TFC24 , UL TFC25	RB5: 39 RB6: 103 RB7: 60 RB8: 952 RB9: 952	RB5: 39 RB6: No data RB7: No data RB8: 952 RB9: No data
11	DL TFC11 , DL TFC26	UL TFC11 , UL TFC26	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC9 , UL TFC11 , UL TFC15 , UL TFC17 , UL TFC24 , UL TFC26	RB5: 81 RB6: 103 RB7: 60 RB8: 952 RB9: 952	RB5: 81 RB6: 103 RB7: 60 RB8: 952 RB9: No data
12	DL TFC12 , DL TFC27	UL TFC12 , UL TFC27	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 1272 RB9: 1272	RB5: No data RB6: No data RB7: No data RB8: 1272 RB9: No data
13	DL TFC13 , DL TFC28	UL TFC13 , UL TFC28	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 1272 RB9: 1272	RB5: 39 RB6: No data RB7: No data RB8: 1272 RB9: No data
14	DL TFC14 , DL TFC29	UL TFC14 , UL TFC29	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 1272 RB9: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 1272 RB9: No data
15	DL TFC14 , DL TFC29	UL TFC14 , UL TFC29	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 1272 RB9: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: No data RB9: 1272
<p>NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB8 and RB9: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</p>						

18.2.2.38d.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return

- for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7, RB8 or RB9.
- for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8 or RB9.
- for sub-test 3, 6, 9 and 12: an RLC SDU on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6, RB7 or RB9.
- for sub-test 4, 7, 10 and 13: an RLC SDU on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6, RB7 or RB9.
- for sub-test 5, 8, 11 and 14: an RLC SDU on RB5, RB6, RB7 and RB8 having the same content as sent by SS. No data shall be received on RB9.
- for sub-test 15: an RLC SDU on RB5, RB6, RB7 and RB9 having the same content as sent by SS; and no data shall be received on RB8.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38d.2 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128.

18.2.2.38d.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.38d.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38d for the uplink payload 128 case.

18.2.2.38d.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 + RB9 (64 kbps, 20 ms TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x148</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x148</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>3x148</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>7x148</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>10x148</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8+RB9, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>UL_TFC9</u>	<u>(TF0, TF0, TF0, TF3, TF0)</u>
<u>UL_TFC10</u>	<u>(TF1, TF0, TF0, TF3, TF0)</u>
<u>UL_TFC11</u>	<u>(TF2, TF1, TF1, TF3, TF0)</u>
<u>UL_TFC12</u>	<u>(TF0, TF0, TF0, TF4, TF0)</u>
<u>UL_TFC13</u>	<u>(TF1, TF0, TF0, TF4, TF0)</u>
<u>UL_TFC14</u>	<u>(TF2, TF1, TF1, TF4, TF0)</u>
<u>UL_TFC15</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC17</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>UL_TFC18</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC19</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC20</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>UL_TFC21</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC22</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC23</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>
<u>UL_TFC24</u>	<u>(TF0, TF0, TF0, TF3, TF1)</u>
<u>UL_TFC25</u>	<u>(TF1, TF0, TF0, TF3, TF1)</u>
<u>UL_TFC26</u>	<u>(TF2, TF1, TF1, TF3, TF1)</u>
<u>UL_TFC27</u>	<u>(TF0, TF0, TF0, TF4, TF1)</u>
<u>UL_TFC28</u>	<u>(TF1, TF0, TF0, TF4, TF1)</u>
<u>UL_TFC29</u>	<u>(TF2, TF1, TF1, TF4, TF1)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8 + RB9</u> <u>(64 kbps,</u> <u>20 ms TT)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x340</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x340</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x340</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>3x340</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>4x340</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8+RB9, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>DL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>DL_TFC9</u>	<u>(TF0, TF0, TF0, TF3, TF0)</u>
<u>DL_TFC10</u>	<u>(TF1, TF0, TF0, TF3, TF0)</u>
<u>DL_TFC11</u>	<u>(TF2, TF1, TF1, TF3, TF0)</u>
<u>DL_TFC12</u>	<u>(TF0, TF0, TF0, TF4, TF0)</u>
<u>DL_TFC13</u>	<u>(TF1, TF0, TF0, TF4, TF0)</u>
<u>DL_TFC14</u>	<u>(TF2, TF1, TF1, TF4, TF0)</u>
<u>DL_TFC15</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC17</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>DL_TFC18</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC20</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>DL_TFC21</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC22</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC23</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>
<u>DL_TFC24</u>	<u>(TF0, TF0, TF0, TF3, TF1)</u>
<u>DL_TFC25</u>	<u>(TF1, TF0, TF0, TF3, TF1)</u>
<u>DL_TFC26</u>	<u>(TF2, TF1, TF1, TF3, TF1)</u>
<u>DL_TFC27</u>	<u>(TF0, TF0, TF0, TF4, TF1)</u>
<u>DL_TFC28</u>	<u>(TF1, TF0, TF0, TF4, TF1)</u>
<u>DL_TFC29</u>	<u>(TF2, TF1, TF1, TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits)</u> <u>Note 1</u>	<u>Test data size (bits)</u> <u>Note 1</u>
1	DL TFC1 , DL TFC16	UL TFC1 , UL TFC16	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC15 , UL TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 120 RB9: 120	RB5: 39 RB6: No data RB7: No data RB8: No data RB9: No data
2	DL TFC2 , DL TFC17	UL TFC2 , UL TFC17	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC15 , UL TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 120 RB9: 120	RB5: 81 RB6: 103 RB7: 60 RB8: No data RB9: No data
3	DL TFC3 , DL TFC18	UL TFC3 , UL TFC18	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC3 , UL TFC15 , UL TFC18	RB5: 39 RB6: 103 RB7: 60 RB8: 120 RB9: 120	RB5: No data RB6: No data RB7: No data RB8: 312 RB9: No data
4	DL TFC4 , DL TFC19	UL TFC4 , UL TFC19	DL TFC0 , DL TFC15 , DUL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC3 , UL TFC4 , UL TFC15 , UL TFC16 , UL TFC18 , UL TFC19	RB5: 39 RB6: 103 RB7: 60 RB8: 120 RB9: 120	RB5: 39 RB6: No data RB7: No data RB8: 312 RB9: No data
5	DL TFC5 , DL TFC20	UL TFC5 , UL TFC20	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC3 , UL TFC5 , UL TFC15 , UL TFC17 , UL TFC18 , UL TFC20	RB5: 81 RB6: 103 RB7: 60 RB8: 120 RB9: 120	RB5: 81 RB6: 103 RB7: 60 RB8: 312 RB9: No data
6	DL TFC6 , DL TFC21	UL TFC6 , UL TFC21	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC6 , UL TFC15 , UL TFC21	RB5: 39 RB6: 103 RB7: 60 RB8: 376 RB9: 376	RB5: No data RB6: No data RB7: No data RB8: 632 RB9: No data
7	DL TFC7 , DL TFC22	UL TFC7 , UL TFC22	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC6 , UL TFC7 , UL TFC15 , UL TFC16 , UL TFC21 , UL TFC22	RB5: 39 RB6: 103 RB7: 60 RB8: 376 RB9: 376	RB5: 39 RB6: No data RB7: No data RB8: 632 RB9: No data
8	DL TFC8 , DL TFC23	UL TFC8 , UL TFC23	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC6 , UL TFC8 , UL TFC15 , UL TFC17 , UL TFC21 , UL TFC23	RB5: 81 RB6: 103 RB7: 60 RB8: 376 RB9: 376	RB5: 81 RB6: 103 RB7: 60 RB8: 632 RB9: No data
9	DL TFC9 , DL TFC24	UL TFC9 , UL TFC24	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC9 , UL TFC15 , UL TFC24	RB5: 39 RB6: 103 RB7: 60 RB8: 888 RB9: 888	RB5: No data RB6: No data RB7: No data RB8: 952 RB9: No data

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) Note 1</u>	<u>Test data size (bits) Note 1</u>
<u>10</u>	<u>DL TFC10, DL TFC25</u>	<u>UL TFC10, UL TFC25</u>	<u>DL TFC0, DL TFC15, UL TFC0, UL TFC15</u>	<u>UL TFC0, UL TFC1, UL TFC9, UL TFC10, UL TFC15, UL TFC16, UL TFC24, UL TFC25</u>	<u>RB5: 39 RB6: 103 RB7: 60 RB8: 888 RB9: 888</u>	<u>RB5: 39 RB6: No data RB7: No data RB8: 952 RB9: No data</u>
<u>11</u>	<u>DL TFC11, DL TFC26</u>	<u>UL TFC11, UL TFC26</u>	<u>DL TFC0, DL TFC15, UL TFC0, UL TFC15</u>	<u>UL TFC0, UL TFC2, UL TFC9, UL TFC11, UL TFC15, UL TFC17, UL TFC24, UL TFC26</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 888 RB9: 888</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 952 RB9: No data</u>
<u>12</u>	<u>DL TFC12, DL TFC27</u>	<u>UL TFC12, UL TFC27</u>	<u>DL TFC0, DL TFC15, UL TFC0, UL TFC15</u>	<u>UL TFC0, UL TFC12, UL TFC15, UL TFC27</u>	<u>RB5: 39 RB6: 103 RB7: 60 RB8: 1272 RB9: 1272</u>	<u>RB5: No data RB6: No data RB7: No data RB8: 1272 RB9: No data</u>
<u>13</u>	<u>DL TFC13, DL TFC28</u>	<u>UL TFC13, UL TFC28</u>	<u>DL TFC0, DL TFC15, UL TFC0, UL TFC15</u>	<u>UL TFC0, UL TFC1, UL TFC12, UL TFC13, UL TFC15, UL TFC16, UL TFC27, UL TFC28</u>	<u>RB5: 39 RB6: 103 RB7: 60 RB8: 1272 RB9: 1272</u>	<u>RB5: 39 RB6: No data RB7: No data RB8: 1272 RB9: No data</u>
<u>14</u>	<u>DL TFC14, DL TFC29</u>	<u>UL TFC14, UL TFC29</u>	<u>DL TFC0, DL TFC15, UL TFC0, UL TFC15</u>	<u>UL TFC0, UL TFC2, UL TFC12, UL TFC14, UL TFC15, UL TFC17, UL TFC27, UL TFC29</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 1272 RB9: 1272</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 1272 RB9: No data</u>
<u>15</u>	<u>DL TFC14, DL TFC29</u>	<u>UL TFC14, UL TFC29</u>	<u>DL TFC0, DL TFC15, UL TFC0, UL TFC15</u>	<u>UL TFC0, UL TFC2, UL TFC12, UL TFC14, UL TFC15, UL TFC17, UL TFC27, UL TFC29</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 1272 RB9: 1272</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: No data RB9: 1272</u>
<p><u>NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB8 and RB9: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.38d.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return

- for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7, RB8 or RB9.
- for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8 or RB9.
- for sub-test 3: an RLC SDU on RB8 having the same content as the first 120 lsb's sent by SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 4: an RLC SDU on RB5 having the same content as sent by SS and on RB8 having the same content as the first 120 lsb's sent by the SS in the downlink; and no data shall be received on RB6 and RB7.
- for sub-test 5: an RLC SDU on RB5, RB6, and RB7 having the same content as sent by SS and on RB8 having same content as the first 120 lsb's sent by the SS in the downlink.
- for sub-test 6: an RLC SDU on RB8 having the same content as the first 376 lsb's sent by SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 7: an RLC SDU on RB5 having the same content as sent by SS and on RB8 having the same content as the first 376 lsb's sent by the SS in the downlink; and no data shall be received on RB6 and RB7.
- for sub-test 8: an RLC SDU on RB5, RB6, and RB7 having the same content as sent by SS and on RB8 having same content as the first 376 lsb's sent by the SS in the downlink.
- for sub-test 9: an RLC SDU on RB8 having the same content as the first 888 lsb's sent by SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 10: an RLC SDU on RB5 having the same content as sent by SS and on RB8 having the same content as the first 888 lsb's sent by the SS in the downlink; and no data shall be received on RB6 and RB7.
- for sub-test 11: an RLC SDU on RB5, RB6, and RB7 having the same content as sent by SS and on RB8 having same content as the first 888 lsb's sent by the SS in the downlink.
- for sub-test 12: an RLC SDU on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6, RB7 or RB9.
- for sub-test 13: an RLC SDU on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6, RB7 or RB9.
- for sub-test 14: an RLC SDU on RB5, RB6, RB7 and RB8 having the same content as sent by SS. No data shall be received on RB9.
- for sub-test 15: an RLC SDU on RB5, RB6, RB7 and RB9 having the same content as sent by SS; and no data shall be received on RB8.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38e Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:0 DL:0 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.38e.1 Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:0 DL:0 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320.

18.2.2.38e.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.38e.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38e for the uplink payload 320 case.

18.2.2.38e.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5</u> (RAB subflow #1)	<u>RB6</u> (RAB subflow #2)	<u>RB7</u> (RAB subflow #3)	<u>RB8</u> (0 kbps)	<u>DCCH</u>
TFS	<u>TF0, bits</u>	0x81	0x103	0x60	0x336	0x148
	<u>TF1, bits</u>	1x39	1x53	1x60	N/A	1x148
	<u>TF2, bits</u>	1x42	1x63	N/A	N/A	N/A
	<u>TF3, bits</u>	1x55	1x84	N/A	N/A	N/A
	<u>TF4, bits</u>	1x75	1x103	N/A	N/A	N/A
	<u>TF5, bits</u>	1x81	N/A	N/A	N/A	N/A

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	(TF0, TF0, TF0, TF0, TF0)
<u>UL_TFC1</u>	(TF1, TF0, TF0, TF0, TF0)
<u>UL_TFC2</u>	(TF2, TF1, TF0, TF0, TF0)
<u>UL_TFC3</u>	(TF3, TF2, TF0, TF0, TF0)
<u>UL_TFC4</u>	(TF4, TF3, TF0, TF0, TF0)
<u>UL_TFC5</u>	(TF5, TF1, TF1, TF0, TF0)
<u>UL_TFC6</u>	(TF0, TF0, TF0, TF0, TF1)
<u>UL_TFC7</u>	(TF1, TF0, TF0, TF0, TF1)
<u>UL_TFC8</u>	(TF2, TF1, TF0, TF0, TF1)
<u>UL_TFC9</u>	(TF3, TF2, TF0, TF0, TF1)
<u>UL_TFC10</u>	(TF4, TF3, TF0, TF0, TF1)
<u>UL_TFC11</u>	(TF5, TF1, TF1, TF0, TF1)

Downlink TFS:

	<u>TFI</u>	<u>RB5</u> (RAB subflow #1)	<u>RB6</u> (RAB subflow #2)	<u>RB7</u> (RAB subflow #3)	<u>RB8</u> (0 kbps)	<u>DCCH</u>
TFS	<u>TF0, bits</u>	1x0	0x103	0x60	0x336	0x148
	<u>TF1, bits</u>	1x39	1x53	1x60	N/A	1x148
	<u>TF2, bits</u>	1x42	1x63	N/A	N/A	N/A
	<u>TF3, bits</u>	1x55	1x84	N/A	N/A	N/A
	<u>TF4, bits</u>	1x75	1x103	N/A	N/A	N/A
	<u>TF5, bits</u>	1x81	N/A	N/A	N/A	N/A

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	(TF0, TF0, TF0, TF0, TF0)
<u>DL_TFC1</u>	(TF1, TF0, TF0, TF0, TF0)
<u>DL_TFC2</u>	(TF2, TF1, TF0, TF0, TF0)
<u>DL_TFC3</u>	(TF3, TF2, TF0, TF0, TF0)
<u>DL_TFC4</u>	(TF4, TF3, TF0, TF0, TF0)
<u>DL_TFC5</u>	(TF5, TF1, TF1, TF0, TF0)
<u>DL_TFC6</u>	(TF0, TF0, TF0, TF0, TF1)
<u>DL_TFC7</u>	(TF1, TF0, TF0, TF0, TF1)
<u>DL_TFC8</u>	(TF2, TF1, TF0, TF0, TF1)
<u>DL_TFC9</u>	(TF3, TF2, TF0, TF0, TF1)
<u>DL_TFC10</u>	(TF4, TF3, TF0, TF0, TF1)
<u>DL_TFC11</u>	(TF5, TF1, TF1, TF0, TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	<u>DL_TFC1,</u> <u>DL_TFC7</u>	<u>UL_TFC1,</u> <u>UL_TFC7</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 0 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
2	<u>DL_TFC2,</u> <u>DL_TFC8</u>	<u>UL_TFC2,</u> <u>UL_TFC8</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 0 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
3	<u>DL_TFC3,</u> <u>DL_TFC9</u>	<u>UL_TFC3,</u> <u>UL_TFC9</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC9</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 0 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
4	<u>DL_TFC4,</u> <u>DL_TFC10</u>	<u>UL_TFC4,</u> <u>UL_TFC10</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC10</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 0 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
5	<u>DL_TFC5,</u> <u>DL_TFC11</u>	<u>UL_TFC5,</u> <u>UL_TFC11</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC11</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 0 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: No data</u>
NOTE 1: <u>UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5 and UL_TFC6 are part of minimum set of TFCIs.</u>						
NOTE 2: <u>See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u>						

18.2.2.38e.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return

- for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
- for sub-test 2,3 and 4: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
- for sub-test 5: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38e.2 Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:0 DL:0 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128.

18.2.2.38e.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.38e.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38e for the uplink payload 128 case.

18.2.2.38e.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (0 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x128</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>N/A</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF0, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF3, TF2, TF0, TF0, TF0)</u>
<u>UL_TFC4</u>	<u>(TF4, TF3, TF0, TF0, TF0)</u>
<u>UL_TFC5</u>	<u>(TF5, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF0, TF0, TF1)</u>
<u>UL_TFC9</u>	<u>(TF3, TF2, TF0, TF0, TF1)</u>
<u>UL_TFC10</u>	<u>(TF4, TF3, TF0, TF0, TF1)</u>
<u>UL_TFC11</u>	<u>(TF5, TF1, TF1, TF0, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (0 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>N/A</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

	TF4, bits	1x75	1x103	N/A	N/A	N/A
	TF5, bits	1x81	N/A	N/A	N/A	N/A

[Downlink TFCS:](#)

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0, TF0, TF0, TF0, TF0)
DL_TFC1	(TF1, TF0, TF0, TF0, TF0)
DL_TFC2	(TF2, TF1, TF0, TF0, TF0)
DL_TFC3	(TF3, TF2, TF0, TF0, TF0)
DL_TFC4	(TF4, TF3, TF0, TF0, TF0)
DL_TFC5	(TF5, TF1, TF1, TF0, TF0)
DL_TFC6	(TF0, TF0, TF0, TF0, TF1)
DL_TFC7	(TF1, TF0, TF0, TF0, TF1)
DL_TFC8	(TF2, TF1, TF0, TF0, TF1)
DL_TFC9	(TF3, TF2, TF0, TF0, TF1)
DL_TFC10	(TF4, TF3, TF0, TF0, TF1)
DL_TFC11	(TF5, TF1, TF1, TF0, TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	<u>DL_TFC1,</u> <u>DL_TFC7</u>	<u>UL_TFC1,</u> <u>UL_TFC7</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 0 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
2	<u>DL_TFC2,</u> <u>DL_TFC8</u>	<u>UL_TFC2,</u> <u>UL_TFC8</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 0 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
3	<u>DL_TFC3,</u> <u>DL_TFC9</u>	<u>UL_TFC3,</u> <u>UL_TFC9</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC9</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 0 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
4	<u>DL_TFC4,</u> <u>DL_TFC10</u>	<u>UL_TFC4,</u> <u>UL_TFC10</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC10</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 0 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
5	<u>DL_TFC5,</u> <u>DL_TFC11</u>	<u>UL_TFC5,</u> <u>UL_TFC11</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC11</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 0 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: No data</u>

NOTE 1: UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5 and UL_TFC6 are part of minimum set of TFCIs.

NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.

18.2.2.38e.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return

- for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
- for sub-test 2,3 and 4: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
- for sub-test 5: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38f Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.38f.1 Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320 TTI 40 ms.

18.2.2.38f.1.1 Conformance requirement

See clause 18.2.2.4.1.

18.2.2.38f.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38f for the uplink payload 320 and 40 ms TTI case.

18.2.2.38f.1.3 Method of test

See clause 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (8 kbps, 40 ms TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF0, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF3, TF2, TF0, TF0, TF0)</u>
<u>UL_TFC4</u>	<u>(TF4, TF3, TF0, TF0, TF0)</u>
<u>UL_TFC5</u>	<u>(TF5, TF4, TF1, TF0, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF0, TF1, TF0)</u>
<u>UL_TFC9</u>	<u>(TF3, TF2, TF0, TF1, TF0)</u>
<u>UL_TFC10</u>	<u>(TF4, TF3, TF0, TF1, TF0)</u>
<u>UL_TFC11</u>	<u>(TF5, TF4, TF1, TF1, TF0)</u>
<u>UL_TFC12</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC13</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC14</u>	<u>(TF2, TF1, TF0, TF0, TF1)</u>
<u>UL_TFC15</u>	<u>(TF3, TF2, TF0, TF0, TF1)</u>
<u>UL_TFC16</u>	<u>(TF4, TF3, TF0, TF0, TF1)</u>
<u>UL_TFC17</u>	<u>(TF5, TF4, TF1, TF0, TF1)</u>
<u>UL_TFC18</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC19</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC20</u>	<u>(TF2, TF1, TF0, TF1, TF1)</u>
<u>UL_TFC21</u>	<u>(TF3, TF2, TF0, TF1, TF1)</u>
<u>UL_TFC22</u>	<u>(TF4, TF3, TF0, TF1, TF1)</u>
<u>UL_TFC23</u>	<u>(TF5, TF4, TF1, TF1, TF1)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(8 kbps, 40 ms</u> <u>TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0,TF0,TF0,TF0,TF0)</u>
<u>DL_TFC1</u>	<u>(TF1,TF0,TF0,TF0,TF0)</u>
<u>DL_TFC2</u>	<u>(TF2,TF1,TF0,TF0,TF0)</u>
<u>DL_TFC3</u>	<u>(TF3,TF2,TF0,TF0,TF0)</u>
<u>DL_TFC4</u>	<u>(TF4,TF3,TF0,TF0,TF0)</u>
<u>DL_TFC5</u>	<u>(TF5,TF4,TF1,TF0,TF0)</u>
<u>DL_TFC6</u>	<u>(TF0,TF0,TF0,TF1,TF0)</u>
<u>DL_TFC7</u>	<u>(TF1,TF0,TF0,TF1,TF0)</u>
<u>DL_TFC8</u>	<u>(TF2,TF1,TF0,TF1,TF0)</u>
<u>DL_TFC9</u>	<u>(TF3,TF2,TF0,TF1,TF0)</u>
<u>DL_TFC10</u>	<u>(TF4,TF3,TF0,TF1,TF0)</u>
<u>DL_TFC11</u>	<u>(TF5,TF4,TF1,TF1,TF0)</u>
<u>DL_TFC12</u>	<u>(TF0,TF0,TF0,TF0,TF1)</u>
<u>DL_TFC13</u>	<u>(TF1,TF0,TF0,TF0,TF1)</u>
<u>DL_TFC14</u>	<u>(TF2,TF1,TF0,TF0,TF1)</u>
<u>DL_TFC15</u>	<u>(TF3,TF2,TF0,TF0,TF1)</u>
<u>DL_TFC16</u>	<u>(TF4,TF3,TF0,TF0,TF1)</u>
<u>DL_TFC17</u>	<u>(TF5,TF4,TF1,TF0,TF1)</u>
<u>DL_TFC18</u>	<u>(TF0,TF0,TF0,TF1,TF1)</u>
<u>DL_TFC19</u>	<u>(TF1,TF0,TF0,TF1,TF1)</u>
<u>DL_TFC20</u>	<u>(TF2,TF1,TF0,TF1,TF1)</u>
<u>DL_TFC21</u>	<u>(TF3,TF2,TF0,TF1,TF1)</u>
<u>DL_TFC22</u>	<u>(TF4,TF3,TF0,TF1,TF1)</u>
<u>DL_TFC23</u>	<u>(TF5,TF4,TF1,TF1,TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size</u> (note 2)	<u>Test data size</u> (note 2)
<u>1</u>	<u>DL TFC1,</u> <u>DL TFC13</u>	<u>UL TFC1,</u> <u>UL TFC13</u>	<u>DL TFC0, DL TFC12,</u> <u>UL TFC0, UL TFC12</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC12,</u> <u>UL TFC13</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>2</u>	<u>DL TFC2,</u> <u>DL TFC14</u>	<u>UL TFC2,</u> <u>UL TFC14</u>	<u>DL TFC0, DL TFC12,</u> <u>UL TFC0, UL TFC12</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC12,</u> <u>UL TFC14</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>3</u>	<u>DL TFC3,</u> <u>DL TFC15</u>	<u>UL TFC3,</u> <u>UL TFC15</u>	<u>DL TFC0, DL TFC12,</u> <u>UL TFC0, UL TFC12</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC12,</u> <u>UL TFC15</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>4</u>	<u>DL TFC4,</u> <u>DL TFC16</u>	<u>UL TFC4,</u> <u>UL TFC16</u>	<u>DL TFC0, DL TFC12,</u> <u>UL TFC0, UL TFC12</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC12,</u> <u>UL TFC16</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>5</u>	<u>DL TFC5,</u> <u>DL TFC17</u>	<u>UL TFC5,</u> <u>UL TFC17</u>	<u>DL TFC0, DL TFC12,</u> <u>UL TFC0, UL TFC12</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC12,</u> <u>UL TFC17</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: No data</u>
<u>6</u>	<u>DL TFC6,</u> <u>DL TFC18</u>	<u>UL TFC6,</u> <u>UL TFC18</u>	<u>DL TFC0, DL TFC12,</u> <u>UL TFC0, UL TFC12</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC12,</u> <u>UL TFC18</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>

7	DL_TFC7, DL_TFC19	UL_TFC7, UL_TFC19	DL_TFC0, DL_TFC12, UL_TFC0, UL_TFC12	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC7, UL_TFC12, UL_TFC13, UL_TFC18, UL_TFC19	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 312 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 312 bits
8	DL_TFC8, DL_TFC20	UL_TFC8, UL_TFC20	DL_TFC0, DL_TFC12, UL_TFC0, UL_TFC12	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC8, UL_TFC12, UL_TFC14, UL_TFC18, UL_TFC20	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 312 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 312 bits
9	DL_TFC9, DL_TFC21	UL_TFC9, UL_TFC21	DL_TFC0, DL_TFC12, UL_TFC0, UL_TFC12	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC18, UL_TFC21	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 312 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 312 bits
10	DL_TFC10, DL_TFC22	UL_TFC10, UL_TFC22	DL_TFC0, DL_TFC12, UL_TFC0, UL_TFC12	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC10, UL_TFC12, UL_TFC16, UL_TFC18, UL_TFC22	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 312 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 312 bits
11	DL_TFC11, DL_TFC23	UL_TFC11, UL_TFC23	DL_TFC0, DL_TFC12, UL_TFC0, UL_TFC12	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC11, UL_TFC12, UL_TFC17, UL_TFC18, UL_TFC23	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 312 bits	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 312 bits

NOTE 1: [UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6](#) and [UL_TFC12](#) are part of minimum set of TFCs.

NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.

RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size parameter has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).

18.2.2.38f.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2,3 and 4: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
 - for sub-test 5: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.
 - for sub-test 6: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 7: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
 - for sub-test 8,9 and 10: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
 - for sub-test 11: RLC SDUs on RB5, RB6 , RB7 and RB8 having the same content as sent by the SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38f.2 Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128 TTI 80 ms.

18.2.2.38f.2.1 Conformance requirement

See clause 18.2.2.4.1.

18.2.2.38f.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38f for the uplink payload 128 and 80 ms TTI case.

18.2.2.38f.2.3 Method of test

See clause 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (8 kbps, 80 ms TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>5x144</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF0, TF0, TF0)
UL_TFC3	(TF3, TF2, TF0, TF0, TF0)
UL_TFC4	(TF4, TF3, TF0, TF0, TF0)
UL_TFC5	(TF5, TF4, TF1, TF0, TF0)
UL_TFC6	(TF0, TF0, TF0, TF1, TF0)
UL_TFC7	(TF1, TF0, TF0, TF1, TF0)
UL_TFC8	(TF2, TF1, TF0, TF1, TF0)
UL_TFC9	(TF3, TF2, TF0, TF1, TF0)
UL_TFC10	(TF4, TF3, TF0, TF1, TF0)
UL_TFC11	(TF5, TF4, TF1, TF1, TF0)
UL_TFC12	(TF0, TF0, TF0, TF2, TF0)
UL_TFC13	(TF1, TF0, TF0, TF2, TF0)
UL_TFC14	(TF2, TF1, TF0, TF2, TF0)
UL_TFC15	(TF3, TF2, TF0, TF2, TF0)
UL_TFC16	(TF4, TF3, TF0, TF2, TF0)
UL_TFC17	(TF5, TF4, TF1, TF2, TF0)
UL_TFC18	(TF0, TF0, TF0, TF0, TF1)
UL_TFC19	(TF1, TF0, TF0, TF0, TF1)
UL_TFC20	(TF2, TF1, TF0, TF0, TF1)
UL_TFC21	(TF3, TF2, TF0, TF0, TF1)
UL_TFC22	(TF4, TF3, TF0, TF0, TF1)
UL_TFC23	(TF5, TF4, TF1, TF0, TF1)
UL_TFC24	(TF0, TF0, TF0, TF1, TF1)
UL_TFC25	(TF1, TF0, TF0, TF1, TF1)
UL_TFC26	(TF2, TF1, TF0, TF1, TF1)
UL_TFC27	(TF3, TF2, TF0, TF1, TF1)
UL_TFC28	(TF4, TF3, TF0, TF1, TF1)
UL_TFC29	(TF5, TF4, TF1, TF1, TF1)
UL_TFC30	(TF0, TF0, TF0, TF2, TF1)
UL_TFC31	(TF1, TF0, TF0, TF2, TF1)

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (8 kbps, 40 ms TTI)	DCCH
TFS	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x53	1x60	1x336	1x148
	TF2, bits	1x42	1x63	N/A	N/A	N/A
	TF3, bits	1x55	1x84	N/A	N/A	N/A
	TF4, bits	1x75	1x103	N/A	N/A	N/A
	TF5, bits	1x81	N/A	N/A	N/A	N/A

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0,TF0,TF0,TF0,TF0)</u>
<u>DL_TFC1</u>	<u>(TF1,TF0,TF0,TF0,TF0)</u>
<u>DL_TFC2</u>	<u>(TF2,TF1,TF0,TF0,TF0)</u>
<u>DL_TFC3</u>	<u>(TF3,TF2,TF0,TF0,TF0)</u>
<u>DL_TFC4</u>	<u>(TF4,TF3,TF0,TF0,TF0)</u>
<u>DL_TFC5</u>	<u>(TF5,TF4,TF1,TF0,TF0)</u>
<u>DL_TFC6</u>	<u>(TF0,TF0,TF0,TF1,TF0)</u>
<u>DL_TFC7</u>	<u>(TF1,TF0,TF0,TF1,TF0)</u>
<u>DL_TFC8</u>	<u>(TF2,TF1,TF0,TF1,TF0)</u>
<u>DL_TFC9</u>	<u>(TF3,TF2,TF0,TF1,TF0)</u>
<u>DL_TFC10</u>	<u>(TF4,TF3,TF0,TF1,TF0)</u>
<u>DL_TFC11</u>	<u>(TF5,TF4,TF1,TF1,TF0)</u>
<u>DL_TFC12</u>	<u>(TF0,TF0,TF0,TF0,TF1)</u>
<u>DL_TFC13</u>	<u>(TF1,TF0,TF0,TF0,TF1)</u>
<u>DL_TFC14</u>	<u>(TF2,TF1,TF0,TF0,TF1)</u>
<u>DL_TFC15</u>	<u>(TF3,TF2,TF0,TF0,TF1)</u>
<u>DL_TFC16</u>	<u>(TF4,TF3,TF0,TF0,TF1)</u>
<u>DL_TFC17</u>	<u>(TF5,TF4,TF1,TF0,TF1)</u>
<u>DL_TFC18</u>	<u>(TF0,TF0,TF0,TF1,TF1)</u>
<u>DL_TFC19</u>	<u>(TF1,TF0,TF0,TF1,TF1)</u>
<u>DL_TFC20</u>	<u>(TF2,TF1,TF0,TF1,TF1)</u>
<u>DL_TFC21</u>	<u>(TF3,TF2,TF0,TF1,TF1)</u>
<u>DL_TFC22</u>	<u>(TF4,TF3,TF0,TF1,TF1)</u>
<u>DL_TFC23</u>	<u>(TF5,TF4,TF1,TF1,TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size</u> (note 2)	<u>Test data size</u> (note 2)
<u>1</u>	<u>DL TFC1,</u> <u>DL TFC13</u>	<u>UL TFC1,</u> <u>UL TFC19</u>	<u>DL TFC0, DL TFC12,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC18,</u> <u>UL TFC19</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 56 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>2</u>	<u>DL TFC2,</u> <u>DL TFC14</u>	<u>UL TFC2,</u> <u>UL TFC20</u>	<u>DL TFC0, DL TFC12,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC18,</u> <u>UL TFC20</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 56 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>3</u>	<u>DL TFC3,</u> <u>DL TFC15</u>	<u>UL TFC3,</u> <u>UL TFC21</u>	<u>DL TFC0, DL TFC12,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC18,</u> <u>UL TFC21</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 56 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>4</u>	<u>DL TFC4,</u> <u>DL TFC16</u>	<u>UL TFC4,</u> <u>UL TFC22</u>	<u>DL TFC0, DL TFC12,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC18,</u> <u>UL TFC22</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 56 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>5</u>	<u>DL TFC5,</u> <u>DL TFC17</u>	<u>UL TFC5,</u> <u>UL TFC23</u>	<u>DL TFC0, DL TFC12,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC18,</u> <u>UL TFC23</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 56 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: No data</u>
<u>6</u>	<u>DL TFC6,</u> <u>DL TFC18</u>	<u>UL TFC6,</u> <u>UL TFC24</u>	<u>DL TFC0, DL TFC12,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC18,</u> <u>UL TFC24</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 56 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>

<u>7</u>	<u>DL_TFC7,</u> <u>DL_TFC19</u>	<u>UL_TFC7,</u> <u>UL_TFC25</u>	<u>DL_TFC0, DL_TFC12,</u> <u>UL_TFC0, UL_TFC18</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC18,</u> <u>UL_TFC19,</u> <u>UL_TFC24,</u> <u>UL_TFC25</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 56 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>8</u>	<u>DL_TFC8,</u> <u>DL_TFC20</u>	<u>UL_TFC8,</u> <u>UL_TFC26</u>	<u>DL_TFC0, DL_TFC12,</u> <u>UL_TFC0, UL_TFC18</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC18,</u> <u>UL_TFC20,</u> <u>UL_TFC24,</u> <u>UL_TFC26</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 56 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>9</u>	<u>DL_TFC9,</u> <u>DL_TFC21</u>	<u>UL_TFC9,</u> <u>UL_TFC27</u>	<u>DL_TFC0, DL_TFC12,</u> <u>UL_TFC0, UL_TFC18</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC9,</u> <u>UL_TFC18,</u> <u>UL_TFC21,</u> <u>UL_TFC24,</u> <u>UL_TFC27</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 56 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>10</u>	<u>DL_TFC10,</u> <u>DL_TFC22</u>	<u>UL_TFC10,</u> <u>UL_TFC28</u>	<u>DL_TFC0, DL_TFC12,</u> <u>UL_TFC0, UL_TFC18</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC10,</u> <u>UL_TFC18,</u> <u>UL_TFC22,</u> <u>UL_TFC24,</u> <u>UL_TFC28</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 56 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>11</u>	<u>DL_TFC11,</u> <u>DL_TFC23</u>	<u>UL_TFC11,</u> <u>UL_TFC29</u>	<u>DL_TFC0, DL_TFC12,</u> <u>UL_TFC0, UL_TFC18</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC11,</u> <u>UL_TFC18,</u> <u>UL_TFC23,</u> <u>UL_TFC24,</u> <u>UL_TFC29</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 56 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>

12	DL_TFC6, DL_TFC18	UL_TFC6, UL_TFC30	DL_TFC0, DL_TFC12, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC30	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 312 bits	RB5: No data RB6: No data RB7: No data RB8: 312 bits
13	DL_TFC7, DL_TFC19	UL_TFC13, UL_TFC31	DL_TFC0, DL_TFC12, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC13, UL_TFC18, UL_TFC19, UL_TFC24, UL_TFC31	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 312 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 312 bits
<p>NOTE 1: UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6 and UL_TFC18 are part of minimum set of TFCIs.</p> <p>NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the uplink TTI for RB8 is 80 ms while the downlink TTI is 40 ms then, to achieve continuous data transmission in uplink the size of the uplink RLC SDU has been set such that 2 of them will be transmitted over a TTI, i.e. UL RLC SDU SIZE has been set to ½ the uplink TFS size minus 8 (the size of a 7 bit length indicator and expansion bit) in the next smaller whole numbers of octets.</p>						

18.2.2.38f.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. [At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.](#)
2. [At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.](#)
3. [At step 15 the UE shall return](#)
 - [for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.](#)
 - [for sub-test 2,3 and 4: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.](#)
 - [for sub-test 5: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.](#)
 - [for sub-test 6: an RLC SDU on RB8 having the same content as the first 56 lsb's sent by SS; and no data shall be received on RB5, RB6 and RB7.](#)
 - [for sub-test 7: RLC SDUs on RB5 having the same content as sent by the SS; on RB8 having the same content as the first 56 lsb's sent by the SS; and no data shall be received on RB6 and RB7.](#)
 - [for sub-test 8,9 and 10: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; on RB8 having the same content as the first 56 lsb's sent by the SS; and no data shall be received on RB7.](#)
 - [for sub-test 11: RLC SDUs on RB5, RB6 , and RB7 having the same content as sent by the SS; and on RB8 having the same content as the first 56 lsb's sent by the SS.](#)

- for sub-test 12: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.

- for sub-test 13: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38g Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:16 DL:16 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.38g.1 Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:16 DL:16 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320.

18.2.2.38g.1.1 Conformance requirement

See clause 18.2.2.4.1.

18.2.2.38g.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38g for the uplink payload 320 case.

18.2.2.38g.1.3 Method of test

See clause 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (16 kbps, 40 ms TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF0, TF0, TF0)
UL_TFC3	(TF3, TF2, TF0, TF0, TF0)
UL_TFC4	(TF4, TF3, TF0, TF0, TF0)
UL_TFC5	(TF5, TF4, TF1, TF0, TF0)
UL_TFC6	(TF0, TF0, TF0, TF1, TF0)
UL_TFC7	(TF1, TF0, TF0, TF1, TF0)
UL_TFC8	(TF3, TF2, TF0, TF1, TF0)
UL_TFC9	(TF4, TF3, TF0, TF1, TF0)
UL_TFC10	(TF5, TF4, TF1, TF1, TF0)
UL_TFC11	(TF0, TF0, TF0, TF2, TF0)
UL_TFC12	(TF1, TF0, TF0, TF2, TF0)
UL_TFC13	(TF3, TF2, TF0, TF2, TF0)
UL_TFC14	(TF4, TF3, TF0, TF2, TF0)
UL_TFC15	(TF5, TF4, TF1, TF2, TF0)
UL_TFC16	(TF0, TF0, TF0, TF0, TF1)
UL_TFC17	(TF1, TF0, TF0, TF0, TF1)
UL_TFC18	(TF2, TF1, TF0, TF0, TF1)
UL_TFC19	(TF3, TF2, TF0, TF0, TF1)
UL_TFC20	(TF4, TF3, TF0, TF0, TF1)
UL_TFC21	(TF5, TF4, TF1, TF0, TF1)
UL_TFC22	(TF0, TF0, TF0, TF1, TF1)
UL_TFC23	(TF1, TF0, TF0, TF1, TF1)
UL_TFC24	(TF3, TF2, TF0, TF1, TF1)
UL_TFC25	(TF4, TF3, TF0, TF1, TF1)
UL_TFC26	(TF5, TF4, TF1, TF1, TF1)
UL_TFC27	(TF0, TF0, TF0, TF2, TF1)
UL_TFC28	(TF1, TF0, TF0, TF2, TF1)
UL_TFC29	(TF3, TF2, TF0, TF2, TF1)
UL_TFC30	(TF4, TF3, TF0, TF2, TF1)
UL_TFC31	(TF5, TF4, TF1, TF2, TF1)

Downlink TFS:

		<u>RB5</u> (RAB subflow #1)	<u>RB6</u> (RAB subflow #2)	<u>RB7</u> (RAB subflow #3)	<u>RB8</u> (16 kbps, 40 ms TTI)	<u>DCCH</u>
<u>TFS</u>	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x53	1x60	1x336	1x148
	TF2, bits	1x42	1x63	N/A	2x336	N/A
	TF3, bits	1x55	1x84	N/A	N/A	N/A
	TF4, bits	1x75	1x103	N/A	N/A	N/A
	TF5, bits	1x81	N/A	N/A	N/A	N/A

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0,TF0,TF0,TF0,TF0)</u>
<u>DL_TFC1</u>	<u>(TF1,TF0,TF0,TF0,TF0)</u>
<u>DL_TFC2</u>	<u>(TF2,TF1,TF0,TF0,TF0)</u>
<u>DL_TFC3</u>	<u>(TF3,TF2,TF0,TF0,TF0)</u>
<u>DL_TFC4</u>	<u>(TF4,TF3,TF0,TF0,TF0)</u>
<u>DL_TFC5</u>	<u>(TF5,TF4,TF1,TF0,TF0)</u>
<u>DL_TFC6</u>	<u>(TF0,TF0,TF0,TF1,TF0)</u>
<u>DL_TFC7</u>	<u>(TF1,TF0,TF0,TF1,TF0)</u>
<u>DL_TFC8</u>	<u>(TF2,TF1,TF0,TF1,TF0)</u>
<u>DL_TFC9</u>	<u>(TF3,TF2,TF0,TF1,TF0)</u>
<u>DL_TFC10</u>	<u>(TF4,TF3,TF0,TF1,TF0)</u>
<u>DL_TFC11</u>	<u>(TF5,TF4,TF1,TF1,TF0)</u>
<u>DL_TFC12</u>	<u>(TF0,TF0,TF0,TF2,TF0)</u>
<u>DL_TFC13</u>	<u>(TF1,TF0,TF0,TF2,TF0)</u>
<u>DL_TFC14</u>	<u>(TF2,TF1,TF0,TF2,TF0)</u>
<u>DL_TFC15</u>	<u>(TF3,TF2,TF0,TF2,TF0)</u>
<u>DL_TFC16</u>	<u>(TF4,TF3,TF0,TF2,TF0)</u>
<u>DL_TFC17</u>	<u>(TF5,TF4,TF1,TF2,TF0)</u>
<u>DL_TFC18</u>	<u>(TF0,TF0,TF0,TF0,TF1)</u>
<u>DL_TFC19</u>	<u>(TF1,TF0,TF0,TF0,TF1)</u>
<u>DL_TFC20</u>	<u>(TF2,TF1,TF0,TF0,TF1)</u>
<u>DL_TFC21</u>	<u>(TF3,TF2,TF0,TF0,TF1)</u>
<u>DL_TFC22</u>	<u>(TF4,TF3,TF0,TF0,TF1)</u>
<u>DL_TFC23</u>	<u>(TF5,TF4,TF1,TF0,TF1)</u>
<u>DL_TFC24</u>	<u>(TF0,TF0,TF0,TF1,TF1)</u>
<u>DL_TFC25</u>	<u>(TF1,TF0,TF0,TF1,TF1)</u>
<u>DL_TFC26</u>	<u>(TF2,TF1,TF0,TF1,TF1)</u>
<u>DL_TFC27</u>	<u>(TF3,TF2,TF0,TF1,TF1)</u>
<u>DL_TFC28</u>	<u>(TF4,TF3,TF0,TF1,TF1)</u>
<u>DL_TFC29</u>	<u>(TF5,TF4,TF1,TF1,TF1)</u>
<u>DL_TFC30</u>	<u>(TF0,TF0,TF0,TF2,TF1)</u>
<u>DL_TFC31</u>	<u>(TF1,TF0,TF0,TF2,TF1)</u>
<u>DL_TFC32</u>	<u>(TF2,TF1,TF0,TF2,TF1)</u>
<u>DL_TFC33</u>	<u>(TF3,TF2,TF0,TF2,TF1)</u>
<u>DL_TFC34</u>	<u>(TF4,TF3,TF0,TF2,TF1)</u>
<u>DL_TFC35</u>	<u>(TF5,TF4,TF1,TF2,TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size</u> (note 2)	<u>Test data size</u> (note 2)
<u>1</u>	<u>DL TFC1,</u> <u>DL TFC19</u>	<u>UL TFC1,</u> <u>UL TFC17</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC16</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC16,</u> <u>UL TFC17</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>2</u>	<u>DL TFC2,</u> <u>DL TFC20</u>	<u>UL TFC2,</u> <u>UL TFC18</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC16</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC16,</u> <u>UL TFC18</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>3</u>	<u>DL TFC3,</u> <u>DL TFC21</u>	<u>UL TFC3,</u> <u>UL TFC19</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC16</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC16,</u> <u>UL TFC19</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>4</u>	<u>DL TFC4,</u> <u>DL TFC22</u>	<u>UL TFC4,</u> <u>UL TFC20</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC16</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC16,</u> <u>UL TFC20</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>5</u>	<u>DL TFC5,</u> <u>DL TFC23</u>	<u>UL TFC5,</u> <u>UL TFC21</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC16</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC16,</u> <u>UL TFC21</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: No data</u>
<u>6</u>	<u>DL TFC6,</u> <u>DL TFC24</u>	<u>UL TFC6,</u> <u>UL TFC22</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC16</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC16,</u> <u>UL TFC22</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>

<u>7</u>	<u>DL_TFC7,</u> <u>DL_TFC25</u>	<u>UL_TFC7,</u> <u>UL_TFC23</u>	<u>DL_TFC0, DL_TFC18,</u> <u>UL_TFC0, UL_TFC16</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC16,</u> <u>UL_TFC17,</u> <u>UL_TFC22,</u> <u>UL_TFC23</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>8</u>	<u>DL_TFC8,</u> <u>DL_TFC26</u>	<u>UL_TFC8,</u> <u>UL_TFC24</u>	<u>DL_TFC0, DL_TFC18,</u> <u>UL_TFC0, UL_TFC16</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC16,</u> <u>UL_TFC19,</u> <u>UL_TFC22,</u> <u>UL_TFC24</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>9</u>	<u>DL_TFC9,</u> <u>DL_TFC27</u>	<u>UL_TFC8,</u> <u>UL_TFC24</u>	<u>DL_TFC0, DL_TFC18,</u> <u>UL_TFC0, UL_TFC16</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC16,</u> <u>UL_TFC19,</u> <u>UL_TFC22,</u> <u>UL_TFC24</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>10</u>	<u>DL_TFC10,</u> <u>DL_TFC28</u>	<u>UL_TFC9,</u> <u>UL_TFC25</u>	<u>DL_TFC0, DL_TFC18,</u> <u>UL_TFC0, UL_TFC16</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC9,</u> <u>UL_TFC16,</u> <u>UL_TFC20,</u> <u>UL_TFC22,</u> <u>UL_TFC25</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>11</u>	<u>DL_TFC11,</u> <u>DL_TFC29</u>	<u>UL_TFC10,</u> <u>UL_TFC26</u>	<u>DL_TFC0, DL_TFC18,</u> <u>UL_TFC0, UL_TFC16</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC10,</u> <u>UL_TFC16,</u> <u>UL_TFC21,</u> <u>UL_TFC22,</u> <u>UL_TFC26</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>

12	DL_TFC12, DL_TFC30	UL_TFC11, UL_TFC27	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC16	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC11, UL_TFC16, UL_TFC27	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits	RB5: No data RB6: No data RB7: No data RB8: 632 bits
13	DL_TFC13, DL_TFC31	UL_TFC12, UL_TFC28	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC16	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC11, UL_TFC12, UL_TFC16, UL_TFC17, UL_TFC27, UL_TFC28	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 632 bits
14	DL_TFC14, DL_TFC32	UL_TFC13, UL_TFC29	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC16	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC11, UL_TFC13, UL_TFC16, UL_TFC19, UL_TFC27, UL_TFC29	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 632 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 632 bits
15	DL_TFC15, DL_TFC33	UL_TFC13, UL_TFC29	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC16	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC11, UL_TFC13, UL_TFC16, UL_TFC19, UL_TFC27, UL_TFC29	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 632 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 632 bits
16	DL_TFC16, DL_TFC34	UL_TFC14, UL_TFC30	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC16	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC11, UL_TFC14, UL_TFC16, UL_TFC20, UL_TFC27, UL_TFC30	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 632 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 632 bits

17	<u>DL_TFC17,</u> <u>DL_TFC35</u>	<u>UL_TFC15,</u> <u>UL_TFC31</u>	<u>DL_TFC0, DL_TFC18,</u> <u>UL_TFC0, UL_TFC16</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC11,</u> <u>UL_TFC15,</u> <u>UL_TFC16,</u> <u>UL_TFC21,</u> <u>UL_TFC27,</u> <u>UL_TFC31</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 632 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 632 bits</u>
<p>NOTE 1: <u>UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6 and UL_TFC16 are part of minimum set of TFCIs.</u></p> <p>NOTE 2: <u>See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size parameter has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.38g.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2,3 and 4: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
 - for sub-test 5: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.
 - for sub-test 6: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 7: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
 - for sub-test 8: RLC SDUs on RB5 having the same content as 1 times plus 13 lsb's of the test data sent by the SS in downlink ; RLC SDUs on RB6 having the same content as 1 times plus 10 lsb's of the test data sent by the SS in downlink ; RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB7.- for sub-test 9 and 10: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
 - for sub-test 11: RLC SDUs on RB5, RB6 , RB7 and RB8 having the same content as sent by the SS.

- for sub-test 12: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 14: RLC SDUs on RB5 having the same content as 1 times plus 13 lsb's of the test data sent by the SS in downlink ; RLC SDUs on RB6 having the same content as 1 times plus 10 lsb's of the test data sent by the SS in downlink ; RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB7.
- for sub-test 15 and 16: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
- for sub-test 17: RLC SDUs on RB5, RB6 , RB7 and RB8 having the same content as sent by the SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38g.2 Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:16 DL:16 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128.

18.2.2.38g.2.1 Conformance requirement

See clause 18.2.2.4.1.

18.2.2.38g.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38g for the uplink payload 128 case.

18.2.2.38g.2.3 Method of test

See clause 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (16 kbps, 40 ms TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>5x144</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF0, TF0, TF0)
UL_TFC3	(TF3, TF2, TF0, TF0, TF0)
UL_TFC4	(TF4, TF3, TF0, TF0, TF0)
UL_TFC5	(TF5, TF4, TF1, TF0, TF0)
UL_TFC6	(TF0, TF0, TF0, TF1, TF0)
UL_TFC7	(TF1, TF0, TF0, TF1, TF0)
UL_TFC8	(TF3, TF2, TF0, TF1, TF0)
UL_TFC9	(TF4, TF3, TF0, TF1, TF0)
UL_TFC10	(TF5, TF4, TF1, TF1, TF0)
UL_TFC11	(TF0, TF0, TF0, TF2, TF0)
UL_TFC12	(TF1, TF0, TF0, TF2, TF0)
UL_TFC13	(TF3, TF2, TF0, TF2, TF0)
UL_TFC14	(TF4, TF3, TF0, TF2, TF0)
UL_TFC15	(TF5, TF4, TF1, TF2, TF0)
UL_TFC16	(TF0, TF0, TF0, TF0, TF1)
UL_TFC17	(TF1, TF0, TF0, TF0, TF1)
UL_TFC18	(TF2, TF1, TF0, TF0, TF1)
UL_TFC19	(TF3, TF2, TF0, TF0, TF1)
UL_TFC20	(TF4, TF3, TF0, TF0, TF1)
UL_TFC21	(TF5, TF4, TF1, TF0, TF1)
UL_TFC22	(TF0, TF0, TF0, TF1, TF1)
UL_TFC23	(TF1, TF0, TF0, TF1, TF1)
UL_TFC24	(TF3, TF2, TF0, TF1, TF1)
UL_TFC25	(TF4, TF3, TF0, TF1, TF1)
UL_TFC26	(TF5, TF4, TF1, TF1, TF1)
UL_TFC27	(TF0, TF0, TF0, TF2, TF1)
UL_TFC28	(TF1, TF0, TF0, TF2, TF1)
UL_TFC29	(TF3, TF2, TF0, TF2, TF1)
UL_TFC30	(TF4, TF3, TF0, TF2, TF1)

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (16 kbps, 40 ms TTI)	DCCH
TFS	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x53	1x60	1x336	1x148
	TF2, bits	1x42	1x63	N/A	2x336	N/A
	TF3, bits	1x55	1x84	N/A	N/A	N/A
	TF4, bits	1x75	1x103	N/A	N/A	N/A
	TF5, bits	1x81	N/A	N/A	N/A	N/A

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0,TF0,TF0,TF0,TF0)</u>
<u>DL_TFC1</u>	<u>(TF1,TF0,TF0,TF0,TF0)</u>
<u>DL_TFC2</u>	<u>(TF2,TF1,TF0,TF0,TF0)</u>
<u>DL_TFC3</u>	<u>(TF3,TF2,TF0,TF0,TF0)</u>
<u>DL_TFC4</u>	<u>(TF4,TF3,TF0,TF0,TF0)</u>
<u>DL_TFC5</u>	<u>(TF5,TF4,TF1,TF0,TF0)</u>
<u>DL_TFC6</u>	<u>(TF0,TF0,TF0,TF1,TF0)</u>
<u>DL_TFC7</u>	<u>(TF1,TF0,TF0,TF1,TF0)</u>
<u>DL_TFC8</u>	<u>(TF2,TF1,TF0,TF1,TF0)</u>
<u>DL_TFC9</u>	<u>(TF3,TF2,TF0,TF1,TF0)</u>
<u>DL_TFC10</u>	<u>(TF4,TF3,TF0,TF1,TF0)</u>
<u>DL_TFC11</u>	<u>(TF5,TF4,TF1,TF1,TF0)</u>
<u>DL_TFC12</u>	<u>(TF0,TF0,TF0,TF2,TF0)</u>
<u>DL_TFC13</u>	<u>(TF1,TF0,TF0,TF2,TF0)</u>
<u>DL_TFC14</u>	<u>(TF2,TF1,TF0,TF2,TF0)</u>
<u>DL_TFC15</u>	<u>(TF3,TF2,TF0,TF2,TF0)</u>
<u>DL_TFC16</u>	<u>(TF4,TF3,TF0,TF2,TF0)</u>
<u>DL_TFC17</u>	<u>(TF5,TF4,TF1,TF2,TF0)</u>
<u>DL_TFC18</u>	<u>(TF0,TF0,TF0,TF0,TF1)</u>
<u>DL_TFC19</u>	<u>(TF1,TF0,TF0,TF0,TF1)</u>
<u>DL_TFC20</u>	<u>(TF2,TF1,TF0,TF0,TF1)</u>
<u>DL_TFC21</u>	<u>(TF3,TF2,TF0,TF0,TF1)</u>
<u>DL_TFC22</u>	<u>(TF4,TF3,TF0,TF0,TF1)</u>
<u>DL_TFC23</u>	<u>(TF5,TF4,TF1,TF0,TF1)</u>
<u>DL_TFC24</u>	<u>(TF0,TF0,TF0,TF1,TF1)</u>
<u>DL_TFC25</u>	<u>(TF1,TF0,TF0,TF1,TF1)</u>
<u>DL_TFC26</u>	<u>(TF2,TF1,TF0,TF1,TF1)</u>
<u>DL_TFC27</u>	<u>(TF3,TF2,TF0,TF1,TF1)</u>
<u>DL_TFC28</u>	<u>(TF4,TF3,TF0,TF1,TF1)</u>
<u>DL_TFC29</u>	<u>(TF5,TF4,TF1,TF1,TF1)</u>
<u>DL_TFC30</u>	<u>(TF0,TF0,TF0,TF2,TF1)</u>
<u>DL_TFC31</u>	<u>(TF1,TF0,TF0,TF2,TF1)</u>
<u>DL_TFC32</u>	<u>(TF2,TF1,TF0,TF2,TF1)</u>
<u>DL_TFC33</u>	<u>(TF3,TF2,TF0,TF2,TF1)</u>
<u>DL_TFC34</u>	<u>(TF4,TF3,TF0,TF2,TF1)</u>
<u>DL_TFC35</u>	<u>(TF5,TF4,TF1,TF2,TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> <u>(note 1)</u>	<u>UL RLC SDU size</u> <u>(note 2)</u>	<u>Test data size</u> <u>(note 2)</u>
<u>1</u>	<u>DL TFC1,</u> <u>DL TFC19</u>	<u>UL TFC1,</u> <u>UL TFC17</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC16</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC16,</u> <u>UL TFC17</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>2</u>	<u>DL TFC2,</u> <u>DL TFC20</u>	<u>UL TFC2,</u> <u>UL TFC18</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC16</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC16,</u> <u>UL TFC18</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>3</u>	<u>DL TFC3,</u> <u>DL TFC21</u>	<u>UL TFC3,</u> <u>UL TFC19</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC16</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC16,</u> <u>UL TFC19</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>4</u>	<u>DL TFC4,</u> <u>DL TFC22</u>	<u>UL TFC4,</u> <u>UL TFC20</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC16</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC16,</u> <u>UL TFC20</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>5</u>	<u>DL TFC5,</u> <u>DL TFC23</u>	<u>UL TFC5,</u> <u>UL TFC21</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC16</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC16,</u> <u>UL TFC21</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: No data</u>
<u>6</u>	<u>DL TFC6,</u> <u>DL TFC24</u>	<u>UL TFC6,</u> <u>UL TFC22</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC16</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC16,</u> <u>UL TFC22</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>

<u>7</u>	<u>DL_TFC7,</u> <u>DL_TFC25</u>	<u>UL_TFC7,</u> <u>UL_TFC23</u>	<u>DL_TFC0, DL_TFC18,</u> <u>UL_TFC0, UL_TFC16</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC16,</u> <u>UL_TFC17,</u> <u>UL_TFC22,</u> <u>UL_TFC23</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>8</u>	<u>DL_TFC8,</u> <u>DL_TFC26</u>	<u>UL_TFC8,</u> <u>UL_TFC24</u>	<u>DL_TFC0, DL_TFC18,</u> <u>UL_TFC0, UL_TFC16</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC16,</u> <u>UL_TFC19,</u> <u>UL_TFC22,</u> <u>UL_TFC24</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>9</u>	<u>DL_TFC9,</u> <u>DL_TFC27</u>	<u>UL_TFC8,</u> <u>UL_TFC24</u>	<u>DL_TFC0, DL_TFC18,</u> <u>UL_TFC0, UL_TFC16</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC16,</u> <u>UL_TFC19,</u> <u>UL_TFC22,</u> <u>UL_TFC24</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>10</u>	<u>DL_TFC10,</u> <u>DL_TFC28</u>	<u>UL_TFC9,</u> <u>UL_TFC25</u>	<u>DL_TFC0, DL_TFC18,</u> <u>UL_TFC0, UL_TFC16</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC9,</u> <u>UL_TFC16,</u> <u>UL_TFC20,</u> <u>UL_TFC22,</u> <u>UL_TFC25</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>11</u>	<u>DL_TFC11,</u> <u>DL_TFC29</u>	<u>UL_TFC10,</u> <u>UL_TFC26</u>	<u>DL_TFC0, DL_TFC18,</u> <u>UL_TFC0, UL_TFC16</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC10,</u> <u>UL_TFC16,</u> <u>UL_TFC21,</u> <u>UL_TFC22,</u> <u>UL_TFC26</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>

12	DL_TFC12, DL_TFC30	UL_TFC11, UL_TFC27	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC16	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC11, UL_TFC16, UL_TFC27	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits	RB5: No data RB6: No data RB7: No data RB8: 632 bits
13	DL_TFC13, DL_TFC31	UL_TFC12, UL_TFC28	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC16	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC11, UL_TFC12, UL_TFC16, UL_TFC17, UL_TFC27, UL_TFC28	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 632 bits
14	DL_TFC14, DL_TFC32	UL_TFC13, UL_TFC29	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC16	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC11, UL_TFC13, UL_TFC16, UL_TFC19, UL_TFC27, UL_TFC29	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 632 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 632 bits
15	DL_TFC15, DL_TFC33	UL_TFC13, UL_TFC29	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC16	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC11, UL_TFC13, UL_TFC16, UL_TFC19, UL_TFC27, UL_TFC29	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 632 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 632 bits
16	DL_TFC16, DL_TFC34	UL_TFC14, UL_TFC30	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC16	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC11, UL_TFC14, UL_TFC16, UL_TFC20, UL_TFC27, UL_TFC30	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 632 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 632 bits

17	<u>DL_TFC17, DL_TFC35</u>	<u>UL_TFC15</u>	<u>DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC16</u>	<u>UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC11, UL_TFC15, UL_TFC16, UL_TFC21, UL_TFC27</u>	<u>RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits</u>	<u>RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits</u>
<p><u>NOTE 1: UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6 and UL_TFC16 are part of minimum set of TFCIs.</u></p> <p><u>NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size parameter has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.38g.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2,3 and 4: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
 - for sub-test 5: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.
 - for sub-test 6: an RLC SDU on RB8 having the same content as the first 120 lsb's sent by SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 7: RLC SDUs on RB5 having the same content as sent by the SS; RB8 having the same content as the first 120 lsb's sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 8: RLC SDUs on RB5 having the same content as 1 times plus 13 lsb's of the test data sent by the SS in downlink; RLC SDUs on RB6 having the same content as 1 times plus 10 lsb's of the test data sent by the SS in downlink; RLC SDUs on RB8 having the same content as the first 120 lsb's sent by SS; and no data shall be received on RB7.
 - for sub-test 9 and 10: RLC SDUs on RB5, and RB6 having the same content as sent by the SS; RB8 having the same content as the first 120 lsb's sent by SS; and no data shall be received on RB7.
 - for sub-test 11: RLC SDUs on RB5, RB6, and RB7 having the same content as sent by the SS; and RB8 having the same content as the first 120 lsb's sent by SS.

- for sub-test 12: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 14: RLC SDUs on RB5 having the same content as 1 times plus 13 lsb's of the test data sent by the SS in downlink; RLC SDUs on RB6 having the same content as 1 times plus 10 lsb's of the test data sent by the SS in downlink; RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB7.
- for sub-test 15 and 16: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
- for sub-test 17: RLC SDUs on RB5, RB6, RB7 and RB8 having the same content as sent by the SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38h Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:32 DL:32 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.38h.1 Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:32 DL:32 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320.

18.2.2.38h.1.1 Conformance requirement

See clause 18.2.2.4.1.

18.2.2.38h.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38h for the uplink payload 320 case.

18.2.2.38h.1.3 Method of test

See clause 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (32 kbps, 20 ms TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF0, TF0, TF0, TF1, TF0)
UL_TFC2	(TF0, TF0, TF0, TF2, TF0)
UL_TFC3	(TF5, TF4, TF1, TF0, TF0)
UL_TFC4	(TF5, TF4, TF1, TF1, TF0)
UL_TFC5	(TF5, TF4, TF1, TF2, TF0)
UL_TFC6	(TF4, TF3, TF0, TF0, TF0)
UL_TFC7	(TF4, TF3, TF0, TF1, TF0)
UL_TFC8	(TF4, TF3, TF0, TF2, TF0)
UL_TFC9	(TF3, TF2, TF0, TF0, TF0)
UL_TFC10	(TF3, TF2, TF0, TF1, TF0)
UL_TFC11	(TF3, TF2, TF0, TF2, TF0)
UL_TFC12	(TF2, TF1, TF0, TF0, TF0)
UL_TFC13	(TF2, TF1, TF0, TF1, TF0)
UL_TFC14	(TF2, TF1, TF0, TF2, TF0)
UL_TFC15	(TF1, TF0, TF0, TF0, TF0)
UL_TFC16	(TF1, TF0, TF0, TF1, TF0)
UL_TFC17	(TF1, TF0, TF0, TF2, TF0)
UL_TFC18	(TF0, TF0, TF0, TF0, TF1)
UL_TFC19	(TF0, TF0, TF0, TF1, TF1)
UL_TFC20	(TF0, TF0, TF0, TF2, TF1)
UL_TFC21	(TF5, TF4, TF1, TF0, TF1)
UL_TFC22	(TF5, TF4, TF1, TF1, TF1)
UL_TFC23	(TF4, TF3, TF0, TF0, TF1)
UL_TFC24	(TF4, TF3, TF0, TF1, TF1)
UL_TFC25	(TF3, TF2, TF0, TF0, TF1)
UL_TFC26	(TF3, TF2, TF0, TF1, TF1)
UL_TFC27	(TF2, TF1, TF0, TF0, TF1)
UL_TFC28	(TF2, TF1, TF0, TF1, TF1)
UL_TFC29	(TF1, TF0, TF0, TF0, TF1)
UL_TFC30	(TF1, TF0, TF0, TF1, TF1)
UL_TFC31	(TF1, TF0, TF0, TF2, TF1)

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (32 kbps, 20 ms TTI)	DCCH
TFS	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x53	1x60	1x336	1x148
	TF2, bits	1x42	1x63	N/A	2x336	N/A
	TF3, bits	1x55	1x84	N/A	N/A	N/A
	TF4, bits	1x75	1x103	N/A	N/A	N/A
	TF5, bits	1x81	N/A	N/A	N/A	N/A

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0,TF0,TF0,TF0,TF0)</u>
<u>DL_TFC1</u>	<u>(TF0,TF0,TF0,TF1,TF0)</u>
<u>DL_TFC2</u>	<u>(TF0,TF0,TF0,TF2,TF0)</u>
<u>DL_TFC3</u>	<u>(TF5,TF4,TF1,TF0,TF0)</u>
<u>DL_TFC4</u>	<u>(TF5,TF4,TF1,TF1,TF0)</u>
<u>DL_TFC5</u>	<u>(TF5,TF4,TF1,TF2,TF0)</u>
<u>DL_TFC6</u>	<u>(TF4,TF3,TF0,TF0,TF0)</u>
<u>DL_TFC7</u>	<u>(TF4,TF3,TF0,TF1,TF0)</u>
<u>DL_TFC8</u>	<u>(TF4,TF3,TF0,TF2,TF0)</u>
<u>DL_TFC9</u>	<u>(TF3,TF2,TF0,TF0,TF0)</u>
<u>DL_TFC10</u>	<u>(TF3,TF2,TF0,TF1,TF0)</u>
<u>DL_TFC11</u>	<u>(TF3,TF2,TF0,TF2,TF0)</u>
<u>DL_TFC12</u>	<u>(TF2,TF1,TF0,TF0,TF0)</u>
<u>DL_TFC13</u>	<u>(TF2,TF1,TF0,TF1,TF0)</u>
<u>DL_TFC14</u>	<u>(TF2,TF1,TF0,TF2,TF0)</u>
<u>DL_TFC15</u>	<u>(TF1,TF0,TF0,TF0,TF0)</u>
<u>DL_TFC16</u>	<u>(TF1,TF0,TF0,TF1,TF0)</u>
<u>DL_TFC17</u>	<u>(TF1,TF0,TF0,TF2,TF0)</u>
<u>DL_TFC18</u>	<u>(TF0,TF0,TF0,TF0,TF1)</u>
<u>DL_TFC19</u>	<u>(TF0,TF0,TF0,TF1,TF1)</u>
<u>DL_TFC20</u>	<u>(TF0,TF0,TF0,TF2,TF1)</u>
<u>DL_TFC21</u>	<u>(TF5,TF4,TF1,TF0,TF1)</u>
<u>DL_TFC22</u>	<u>(TF5,TF4,TF1,TF1,TF1)</u>
<u>DL_TFC23</u>	<u>(TF4,TF3,TF0,TF0,TF1)</u>
<u>DL_TFC24</u>	<u>(TF4,TF3,TF0,TF1,TF1)</u>
<u>DL_TFC25</u>	<u>(TF3,TF2,TF0,TF0,TF1)</u>
<u>DL_TFC26</u>	<u>(TF3,TF2,TF0,TF1,TF1)</u>
<u>DL_TFC27</u>	<u>(TF2,TF1,TF0,TF0,TF1)</u>
<u>DL_TFC28</u>	<u>(TF2,TF1,TF0,TF1,TF1)</u>
<u>DL_TFC29</u>	<u>(TF1,TF0,TF0,TF0,TF1)</u>
<u>DL_TFC30</u>	<u>(TF1,TF0,TF0,TF1,TF1)</u>
<u>DL_TFC31</u>	<u>(TF1,TF0,TF0,TF2,TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size</u> (note 2)	<u>Test data size</u> (note 2)
1	<u>DL TFC1,</u> <u>DL TFC19</u>	<u>UL TFC1,</u> <u>UL TFC19</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC3,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC18,</u> <u>UL TFC19</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
2	<u>DL TFC2,</u> <u>DL TFC20</u>	<u>UL TFC2,</u> <u>UL TFC20</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC18,</u> <u>UL TFC20</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 632 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 632 bits</u>
3	<u>DL TFC3,</u> <u>DL TFC21</u>	<u>UL TFC3,</u> <u>UL TFC21</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC3,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC18,</u> <u>UL TFC21</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: No data</u>
4	<u>DL TFC4,</u> <u>DL TFC22</u>	<u>UL TFC4,</u> <u>UL TFC22</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC3,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC18,</u> <u>UL TFC19,</u> <u>UL TFC21,</u> <u>UL TFC22</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>
5	<u>DL TFC5,</u> <u>DL TFC22</u>	<u>UL TFC5,</u> <u>UL TFC22</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC18,</u> <u>UL TFC20,</u> <u>UL TFC21,</u> <u>UL TFC22</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 632 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 632 bits</u>
6	<u>DL TFC6,</u> <u>DL TFC23</u>	<u>UL TFC6,</u> <u>UL TFC23</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC3,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC18,</u> <u>UL TFC23</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>

7	DL_TFC7, DL_TFC24	UL_TFC7, UL_TFC24	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC7, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC18, UL_TFC19, UL_TFC23, UL_TFC24	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 312 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 312 bits
8	DL_TFC8, DL_TFC24	UL_TFC8, UL_TFC24	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC8, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC18, UL_TFC19, UL_TFC23, UL_TFC24	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 632 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 632 bits
9	DL_TFC9, DL_TFC25	UL_TFC9, UL_TFC25	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC9, UL_TFC18, UL_TFC25	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 312 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: No data
10	DL_TFC10, DL_TFC26	UL_TFC10, UL_TFC26	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC10, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC18, UL_TFC19, UL_TFC23, UL_TFC26	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 312 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 312 bits
11	DL_TFC11, DL_TFC26	UL_TFC11, UL_TFC26	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC11, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC18, UL_TFC19, UL_TFC23, UL_TFC26	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 632 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 632 bits
12	DL_TFC12, DL_TFC27	UL_TFC12, UL_TFC27	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC18, UL_TFC27	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 312 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: No data

13	DL_TFC13 , DL_TFC28	UL_TFC13 , UL_TFC28	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC18	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC6 , UL_TFC13 , UL_TFC9 , UL_TFC12 , UL_TFC15 , UL_TFC18 , UL_TFC19 , UL_TFC23 , UL_TFC28	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 312 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 312 bits
14	DL_TFC14 , DL_TFC28	UL_TFC14 , UL_TFC28	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC18	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC6 , UL_TFC14 , UL_TFC9 , UL_TFC12 , UL_TFC15 , UL_TFC18 , UL_TFC19 , UL_TFC23 , UL_TFC28	RB5: 42bits RB6: 53bits RB7: 60 bits RB8: 632 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 632 bits
15	DL_TFC15 , DL_TFC29	UL_TFC15 , UL_TFC29	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC18	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC6 , UL_TFC9 , UL_TFC12 , UL_TFC15 , UL_TFC18 , UL_TFC29	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 312 bits	RB5: 39 bits RB6: No data RB7: No data RB8: No data
16	DL_TFC16 , DL_TFC30	UL_TFC16 , UL_TFC30	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC18	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC6 , UL_TFC9 , UL_TFC12 , UL_TFC15 , UL_TFC13 , UL_TFC18 , UL_TFC19 , UL_TFC29 , UL_TFC30	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 312 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 312 bits
17	DL_TFC17 , DL_TFC31	UL_TFC17 , UL_TFC31	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC18	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC9 , UL_TFC12 , UL_TFC15 , UL_TFC17 , UL_TFC18 , UL_TFC20 , UL_TFC29 , UL_TFC31	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 632 bits
<p>NOTE 1: UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC9, UL_TFC12, UL_TFC15, and UL_TFC18 are part of minimum set of TFCIs.</p> <p>NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size parameter has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</p>						

18.2.2.38h.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1 and 2: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 3: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.
 - for sub-test 4 and 5: RLC SDUs on RB5, RB6, RB7 and RB8 having the same content as sent by the SS.
 - for sub-test 6: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
 - for sub-test 7 and 8: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
 - for sub-test 9: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
 - for sub-test 10 and 11: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
 - for sub-test 12: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
 - for sub-test 13 and 14: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
 - for sub-test 15: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 16 and 17: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message (in subtests 8, 11, and 14 this may make RB8 temporarily slow down).

18.2.2.38h.2 Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:32 DL:32 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128.

18.2.2.38h.2.1 Conformance requirement

See clause 18.2.2.4.1.

18.2.2.38h.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38h for the uplink payload 128 case.

18.2.2.38h.2.3 Method of test

See clause 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(32 kbps, 20 ms</u> <u>TTI)</u>	<u>DCCH</u>
TFS	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>5x144</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC3</u>	<u>(TF5, TF4, TF1, TF0, TF0)</u>
<u>UL_TFC4</u>	<u>(TF5, TF4, TF1, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF5, TF4, TF1, TF2, TF0)</u>
<u>UL_TFC6</u>	<u>(TF4, TF3, TF0, TF0, TF0)</u>
<u>UL_TFC7</u>	<u>(TF4, TF3, TF0, TF1, TF0)</u>
<u>UL_TFC8</u>	<u>(TF4, TF3, TF0, TF2, TF0)</u>
<u>UL_TFC9</u>	<u>(TF3, TF2, TF0, TF0, TF0)</u>
<u>UL_TFC10</u>	<u>(TF3, TF2, TF0, TF1, TF0)</u>
<u>UL_TFC11</u>	<u>(TF3, TF2, TF0, TF2, TF0)</u>
<u>UL_TFC12</u>	<u>(TF2, TF1, TF0, TF0, TF0)</u>
<u>UL_TFC13</u>	<u>(TF2, TF1, TF0, TF1, TF0)</u>
<u>UL_TFC14</u>	<u>(TF2, TF1, TF0, TF2, TF0)</u>
<u>UL_TFC15</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC16</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC17</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC18</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC19</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC20</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC21</u>	<u>(TF5, TF4, TF1, TF0, TF1)</u>
<u>UL_TFC22</u>	<u>(TF5, TF4, TF1, TF1, TF1)</u>
<u>UL_TFC23</u>	<u>(TF4, TF3, TF0, TF0, TF1)</u>
<u>UL_TFC24</u>	<u>(TF4, TF3, TF0, TF1, TF1)</u>
<u>UL_TFC25</u>	<u>(TF3, TF2, TF0, TF0, TF1)</u>
<u>UL_TFC26</u>	<u>(TF3, TF2, TF0, TF1, TF1)</u>
<u>UL_TFC27</u>	<u>(TF2, TF1, TF0, TF0, TF1)</u>
<u>UL_TFC28</u>	<u>(TF2, TF1, TF0, TF1, TF1)</u>
<u>UL_TFC29</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC30</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC31</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(32 kbps, 20 ms</u> <u>TTI)</u>	<u>DCCH</u>
TFS	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0,TF0,TF0,TF0,TF0)</u>
<u>DL_TFC1</u>	<u>(TF0,TF0,TF0,TF1,TF0)</u>
<u>DL_TFC2</u>	<u>(TF0,TF0,TF0,TF2,TF0)</u>
<u>DL_TFC3</u>	<u>(TF5,TF4,TF1,TF0,TF0)</u>
<u>DL_TFC4</u>	<u>(TF5,TF4,TF1,TF1,TF0)</u>
<u>DL_TFC5</u>	<u>(TF5,TF4,TF1,TF2,TF0)</u>
<u>DL_TFC6</u>	<u>(TF4,TF3,TF0,TF0,TF0)</u>
<u>DL_TFC7</u>	<u>(TF4,TF3,TF0,TF1,TF0)</u>
<u>DL_TFC8</u>	<u>(TF4,TF3,TF0,TF2,TF0)</u>
<u>DL_TFC9</u>	<u>(TF3,TF2,TF0,TF0,TF0)</u>
<u>DL_TFC10</u>	<u>(TF3,TF2,TF0,TF1,TF0)</u>
<u>DL_TFC11</u>	<u>(TF3,TF2,TF0,TF2,TF0)</u>
<u>DL_TFC12</u>	<u>(TF2,TF1,TF0,TF0,TF0)</u>
<u>DL_TFC13</u>	<u>(TF2,TF1,TF0,TF1,TF0)</u>
<u>DL_TFC14</u>	<u>(TF2,TF1,TF0,TF2,TF0)</u>
<u>DL_TFC15</u>	<u>(TF1,TF0,TF0,TF0,TF0)</u>
<u>DL_TFC16</u>	<u>(TF1,TF0,TF0,TF1,TF0)</u>
<u>DL_TFC17</u>	<u>(TF1,TF0,TF0,TF2,TF0)</u>
<u>DL_TFC18</u>	<u>(TF0,TF0,TF0,TF0,TF1)</u>
<u>DL_TFC19</u>	<u>(TF0,TF0,TF0,TF1,TF1)</u>
<u>DL_TFC20</u>	<u>(TF0,TF0,TF0,TF2,TF1)</u>
<u>DL_TFC21</u>	<u>(TF5,TF4,TF1,TF0,TF1)</u>
<u>DL_TFC22</u>	<u>(TF5,TF4,TF1,TF1,TF1)</u>
<u>DL_TFC23</u>	<u>(TF4,TF3,TF0,TF0,TF1)</u>
<u>DL_TFC24</u>	<u>(TF4,TF3,TF0,TF1,TF1)</u>
<u>DL_TFC25</u>	<u>(TF3,TF2,TF0,TF0,TF1)</u>
<u>DL_TFC26</u>	<u>(TF3,TF2,TF0,TF1,TF1)</u>
<u>DL_TFC27</u>	<u>(TF2,TF1,TF0,TF0,TF1)</u>
<u>DL_TFC28</u>	<u>(TF2,TF1,TF0,TF1,TF1)</u>
<u>DL_TFC29</u>	<u>(TF1,TF0,TF0,TF0,TF1)</u>
<u>DL_TFC30</u>	<u>(TF1,TF0,TF0,TF1,TF1)</u>
<u>DL_TFC31</u>	<u>(TF1,TF0,TF0,TF2,TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size</u> (note 2)	<u>Test data size</u> (note 2)
1	<u>DL TFC1,</u> <u>DL TFC19</u>	<u>UL TFC1,</u> <u>UL TFC19</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC3,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC18,</u> <u>UL TFC19</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
2	<u>DL TFC2,</u> <u>DL TFC20</u>	<u>UL TFC2,</u> <u>UL TFC20</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC18,</u> <u>UL TFC20</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 632 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 632 bits</u>
3	<u>DL TFC3,</u> <u>DL TFC21</u>	<u>UL TFC3,</u> <u>UL TFC21</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC3,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC18,</u> <u>UL TFC21</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: No data</u>
4	<u>DL TFC4,</u> <u>DL TFC22</u>	<u>UL TFC4,</u> <u>UL TFC22</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC3,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC18,</u> <u>UL TFC19,</u> <u>UL TFC21,</u> <u>UL TFC22</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>
5	<u>DL TFC5,</u> <u>DL TFC22</u>	<u>UL TFC5,</u> <u>UL TFC22</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC18,</u> <u>UL TFC20,</u> <u>UL TFC21,</u> <u>UL TFC22</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 632 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 632 bits</u>
6	<u>DL TFC6,</u> <u>DL TFC23</u>	<u>UL TFC6,</u> <u>UL TFC23</u>	<u>DL TFC0, DL TFC18,</u> <u>UL TFC0, UL TFC18</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC3,</u> <u>UL TFC6,</u> <u>UL TFC9,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC18,</u> <u>UL TFC23</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>

7	DL_TFC7, DL_TFC24	UL_TFC7, UL_TFC24	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC7, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC18, UL_TFC19, UL_TFC23, UL_TFC24	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 120 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 312 bits
8	DL_TFC8, DL_TFC24	UL_TFC8, UL_TFC24	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC8, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC18, UL_TFC19, UL_TFC23, UL_TFC24	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 632 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 632 bits
9	DL_TFC9, DL_TFC25	UL_TFC9, UL_TFC25	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC9, UL_TFC18, UL_TFC25	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 120 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: No data
10	DL_TFC10, DL_TFC26	UL_TFC10, UL_TFC26	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC10, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC18, UL_TFC19, UL_TFC23, UL_TFC26	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 120 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 312 bits
11	DL_TFC11, DL_TFC26	UL_TFC11, UL_TFC26	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC11, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC18, UL_TFC19, UL_TFC23, UL_TFC26	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 632 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 632 bits
12	DL_TFC12, DL_TFC27	UL_TFC12, UL_TFC27	DL_TFC0, DL_TFC18, UL_TFC0, UL_TFC18	UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC9, UL_TFC12, UL_TFC15, UL_TFC18, UL_TFC27	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 120 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: No data

13	DL_TFC13 , DL_TFC28	UL_TFC13 , UL_TFC28	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC18	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC6 , UL_TFC13 , UL_TFC9 , UL_TFC12 , UL_TFC15 , UL_TFC18 , UL_TFC19 , UL_TFC23 , UL_TFC28	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 120 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 312 bits
14	DL_TFC14 , DL_TFC28	UL_TFC14 , UL_TFC28	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC18	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC6 , UL_TFC14 , UL_TFC9 , UL_TFC12 , UL_TFC15 , UL_TFC18 , UL_TFC19 , UL_TFC23 , UL_TFC28	RB5: 42bits RB6: 53bits RB7: 60 bits RB8: 632 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 632 bits
15	DL_TFC15 , DL_TFC29	UL_TFC15 , UL_TFC29	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC18	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC6 , UL_TFC9 , UL_TFC12 , UL_TFC15 , UL_TFC18 , UL_TFC29	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 120 bits	RB5: 39 bits RB6: No data RB7: No data RB8: No data
16	DL_TFC16 , DL_TFC30	UL_TFC16 , UL_TFC30	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC18	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC6 , UL_TFC9 , UL_TFC12 , UL_TFC15 , UL_TFC13 , UL_TFC18 , UL_TFC19 , UL_TFC29 , UL_TFC30	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 120 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 312 bits
17	DL_TFC17 , DL_TFC31	UL_TFC17 , UL_TFC31	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC18	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC9 , UL_TFC12 , UL_TFC15 , UL_TFC17 , UL_TFC18 , UL_TFC20 , UL_TFC29 , UL_TFC31	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 632 bits
<p>NOTE 1: UL_TFC0, UL_TFC1, UL_TFC3, UL_TFC6, UL_TFC9, UL_TFC12, UL_TFC15, and UL_TFC18 are part of minimum set of TFCIs.</p> <p>NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size parameter has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</p>						

18.2.2. 38h.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1: RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6, and RB7.
 - for sub-test 2: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 3: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.
 - for sub-test 4: RLC SDUs on RB5, RB6, and RB7 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink.
 - for sub-test 5: RLC SDUs on RB5, RB6, RB7 and RB8 having the same content as sent by the SS.
 - for sub-test 6: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
 - for sub-test 7: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB7.
 - for sub-test 8: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
 - for sub-test 9: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
 - for sub-test 10: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB7.
 - for sub-test 11: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
 - for sub-test 12: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
 - for sub-test 13: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB7.
 - for sub-test 14: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
 - for sub-test 15: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 16: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.

- for sub-test 17: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message (in subtests 8, 11, and 14 this may make RB8 temporarily slow down).

18.2.2.38i Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.38i.1 Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320.

18.2.2.38i.1.1 Conformance requirement

See clause 18.2.2.4.1.

18.2.2.38i.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38i for the uplink payload 320 case.

18.2.2.38i.1.3 Method of test

See clause 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (64 kbps, 20 ms TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF0, TF0, TF0)
UL_TFC3	(TF3, TF2, TF0, TF0, TF0)
UL_TFC4	(TF4, TF3, TF0, TF0, TF0)
UL_TFC5	(TF5, TF4, TF1, TF0, TF0)
UL_TFC6	(TF0, TF0, TF0, TF1, TF0)
UL_TFC7	(TF1, TF0, TF0, TF1, TF0)
UL_TFC8	(TF2, TF1, TF0, TF1, TF0)
UL_TFC9	(TF3, TF2, TF0, TF1, TF0)
UL_TFC10	(TF4, TF3, TF0, TF1, TF0)
UL_TFC11	(TF5, TF4, TF1, TF1, TF0)
UL_TFC12	(TF0, TF0, TF0, TF2, TF0)
UL_TFC13	(TF1, TF0, TF0, TF2, TF0)
UL_TFC14	(TF2, TF1, TF0, TF2, TF0)
UL_TFC15	(TF3, TF2, TF0, TF2, TF0)
UL_TFC16	(TF4, TF3, TF0, TF2, TF0)
UL_TFC17	(TF5, TF4, TF1, TF2, TF0)
UL_TFC18	(TF0, TF0, TF0, TF4, TF0)
UL_TFC19	(TF1, TF0, TF0, TF4, TF0)
UL_TFC20	(TF2, TF1, TF0, TF4, TF0)
UL_TFC21	(TF3, TF2, TF0, TF4, TF0)
UL_TFC22	(TF4, TF3, TF0, TF4, TF0)
UL_TFC23	(TF5, TF4, TF1, TF4, TF0)
UL_TFC24	(TF0, TF0, TF0, TF0, TF1)
UL_TFC25	(TF1, TF0, TF0, TF0, TF1)
UL_TFC26	(TF2, TF1, TF0, TF0, TF1)
UL_TFC27	(TF3, TF2, TF0, TF0, TF1)
UL_TFC28	(TF4, TF3, TF0, TF0, TF1)
UL_TFC29	(TF5, TF4, TF1, TF0, TF1)
UL_TFC30	(TF0, TF0, TF0, TF1, TF1)
UL_TFC31	(TF1, TF0, TF0, TF1, TF1)
UL_TFC32	(TF2, TF1, TF0, TF1, TF1)
UL_TFC33	(TF3, TF2, TF0, TF1, TF1)
UL_TFC34	(TF4, TF3, TF0, TF1, TF1)
UL_TFC35	(TF5, TF4, TF1, TF1, TF1)
UL_TFC36	(TF0, TF0, TF0, TF2, TF1)
UL_TFC37	(TF1, TF0, TF0, TF2, TF1)
UL_TFC38	(TF2, TF1, TF0, TF2, TF1)
UL_TFC39	(TF3, TF2, TF0, TF2, TF1)
UL_TFC40	(TF4, TF3, TF0, TF2, TF1)
UL_TFC41	(TF5, TF4, TF1, TF2, TF1)
UL_TFC42	(TF0, TF0, TF0, TF4, TF1)
UL_TFC43	(TF1, TF0, TF0, TF4, TF1)
UL_TFC44	(TF2, TF1, TF0, TF4, TF1)
UL_TFC45	(TF3, TF2, TF0, TF4, TF1)
UL_TFC46	(TF4, TF3, TF0, TF4, TF1)
UL_TFC47	(TF5, TF4, TF1, TF4, TF1)

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (64 kbps, 20 ms TTI)	DCCH
TFS	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x53	1x60	1x336	1x148
	TF2, bits	1x42	1x63	N/A	2x336	N/A
	TF3, bits	1x55	1x84	N/A	3x336	N/A
	TF4, bits	1x75	1x103	N/A	4x336	N/A
	TF5, bits	1x81	N/A	N/A	N/A	N/A

[Downlink TFCS:](#)

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0,TF0,TF0,TF0,TF0)
DL_TFC1	(TF1,TF0,TF0,TF0,TF0)
DL_TFC2	(TF2,TF1,TF0,TF0,TF0)
DL_TFC3	(TF3,TF2,TF0,TF0,TF0)
DL_TFC4	(TF4,TF3,TF0,TF0,TF0)
DL_TFC5	(TF5,TF4,TF1,TF0,TF0)
DL_TFC6	(TF0,TF0,TF0,TF1,TF0)
DL_TFC7	(TF1,TF0,TF0,TF1,TF0)
DL_TFC8	(TF2,TF1,TF0,TF1,TF0)
DL_TFC9	(TF3,TF2,TF0,TF1,TF0)
DL_TFC10	(TF4,TF3,TF0,TF1,TF0)
DL_TFC11	(TF5,TF4,TF1,TF1,TF0)
DL_TFC12	(TF0,TF0,TF0,TF2,TF0)
DL_TFC13	(TF1,TF0,TF0,TF2,TF0)
DL_TFC14	(TF2,TF1,TF0,TF2,TF0)
DL_TFC15	(TF3,TF2,TF0,TF2,TF0)
DL_TFC16	(TF4,TF3,TF0,TF2,TF0)
DL_TFC17	(TF5,TF4,TF1,TF2,TF0)
DL_TFC18	(TF0,TF0,TF0,TF3,TF0)
DL_TFC19	(TF1,TF0,TF0,TF3,TF0)
DL_TFC20	(TF2,TF1,TF0,TF3,TF0)
DL_TFC21	(TF3,TF2,TF0,TF3,TF0)
DL_TFC22	(TF4,TF3,TF0,TF3,TF0)
DL_TFC23	(TF5,TF4,TF1,TF3,TF0)
DL_TFC24	(TF0,TF0,TF0,TF4,TF0)
DL_TFC25	(TF1,TF0,TF0,TF4,TF0)
DL_TFC26	(TF2,TF1,TF0,TF4,TF0)
DL_TFC27	(TF3,TF2,TF0,TF4,TF0)
DL_TFC28	(TF4,TF3,TF0,TF4,TF0)
DL_TFC29	(TF5,TF4,TF1,TF4,TF0)
DL_TFC30	(TF0,TF0,TF0,TF0,TF1)
DL_TFC31	(TF1,TF0,TF0,TF0,TF1)
DL_TFC32	(TF2,TF1,TF0,TF0,TF1)
DL_TFC33	(TF3,TF2,TF0,TF0,TF1)
DL_TFC34	(TF4,TF3,TF0,TF0,TF1)
DL_TFC35	(TF5,TF4,TF1,TF0,TF1)
DL_TFC36	(TF0,TF0,TF0,TF1,TF1)
DL_TFC37	(TF1,TF0,TF0,TF1,TF1)
DL_TFC38	(TF2,TF1,TF0,TF1,TF1)
DL_TFC39	(TF3,TF2,TF0,TF1,TF1)
DL_TFC40	(TF4,TF3,TF0,TF1,TF1)
DL_TFC41	(TF5,TF4,TF1,TF1,TF1)
DL_TFC42	(TF0,TF0,TF0,TF2,TF1)
DL_TFC43	(TF1,TF0,TF0,TF2,TF1)
DL_TFC44	(TF2,TF1,TF0,TF2,TF1)
DL_TFC45	(TF3,TF2,TF0,TF2,TF1)
DL_TFC46	(TF4,TF3,TF0,TF2,TF1)
DL_TFC47	(TF5,TF4,TF1,TF2,TF1)
DL_TFC48	(TF0,TF0,TF0,TF3,TF1)
DL_TFC49	(TF1,TF0,TF0,TF3,TF1)
DL_TFC50	(TF2,TF1,TF0,TF3,TF1)
DL_TFC51	(TF3,TF2,TF0,TF3,TF1)
DL_TFC52	(TF4,TF3,TF0,TF3,TF1)
DL_TFC53	(TF5,TF4,TF1,TF3,TF1)
DL_TFC54	(TF0,TF0,TF0,TF4,TF1)
DL_TFC55	(TF1,TF0,TF0,TF4,TF1)
DL_TFC56	(TF2,TF1,TF0,TF4,TF1)
DL_TFC57	(TF3,TF2,TF0,TF4,TF1)
DL_TFC58	(TF4,TF3,TF0,TF4,TF1)
DL_TFC59	(TF5,TF4,TF1,TF4,TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> <u>(note 1)</u>	<u>UL RLC SDU size</u> <u>(note 2)</u>	<u>Test data size</u> <u>(note 2)</u>
<u>1</u>	<u>DL TFC1,</u> <u>DL TFC31</u>	<u>UL TFC1,</u> <u>UL TFC25</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC25</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>2</u>	<u>DL TFC2,</u> <u>DL TFC32</u>	<u>UL TFC2,</u> <u>UL TFC26</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC26</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>3</u>	<u>DL TFC3,</u> <u>DL TFC33</u>	<u>UL TFC3,</u> <u>UL TFC27</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC27</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>4</u>	<u>DL TFC4,</u> <u>DL TFC34</u>	<u>UL TFC4,</u> <u>UL TFC28</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC28</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>5</u>	<u>DL TFC5,</u> <u>DL TFC35</u>	<u>UL TFC5,</u> <u>UL TFC29</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC29</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: No data</u>
<u>6</u>	<u>DL TFC6,</u> <u>DL TFC36</u>	<u>UL TFC6,</u> <u>UL TFC30</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC30</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>

<u>7</u>	<u>DL_TFC7,</u> <u>DL_TFC37</u>	<u>UL_TFC7,</u> <u>UL_TFC31</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC24,</u> <u>UL_TFC25,</u> <u>UL_TFC30,</u> <u>UL_TFC31</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>8</u>	<u>DL_TFC8,</u> <u>DL_TFC38</u>	<u>UL_TFC8,</u> <u>UL_TFC32</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC24,</u> <u>UL_TFC26,</u> <u>UL_TFC30,</u> <u>UL_TFC32</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>9</u>	<u>DL_TFC9,</u> <u>DL_TFC39</u>	<u>UL_TFC9,</u> <u>UL_TFC33</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC9,</u> <u>UL_TFC24,</u> <u>UL_TFC27,</u> <u>UL_TFC30,</u> <u>UL_TFC33</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>10</u>	<u>DL_TFC10,</u> <u>DL_TFC40</u>	<u>UL_TFC10,</u> <u>UL_TFC34</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC10,</u> <u>UL_TFC24,</u> <u>UL_TFC28,</u> <u>UL_TFC30,</u> <u>UL_TFC34</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>11</u>	<u>DL_TFC11,</u> <u>DL_TFC41</u>	<u>UL_TFC11,</u> <u>UL_TFC35</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC11,</u> <u>UL_TFC24,</u> <u>UL_TFC29,</u> <u>UL_TFC30,</u> <u>UL_TFC35</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>

12	DL_TFC12, DL_TFC42	UL_TFC12, UL_TFC36	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC24, UL_TFC36	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits	RB5: No data RB6: No data RB7: No data RB8: 632 bits
13	DL_TFC13, DL_TFC43	UL_TFC13, UL_TFC37	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC13, UL_TFC24, UL_TFC25, UL_TFC36, UL_TFC37	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 632 bits
14	DL_TFC14, DL_TFC44	UL_TFC14, UL_TFC38	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC14, UL_TFC24, UL_TFC26, UL_TFC36, UL_TFC38	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 632 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 632 bits
15	DL_TFC15, DL_TFC45	UL_TFC15, UL_TFC39	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC15, UL_TFC24, UL_TFC27, UL_TFC36, UL_TFC39	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 632 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 632 bits
16	DL_TFC16, DL_TFC46	UL_TFC16, UL_TFC40	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC16, UL_TFC24, UL_TFC28, UL_TFC36, UL_TFC40	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 632 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 632 bits

17	DL_TFC17, DL_TFC47	UL_TFC17, UL_TFC41	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC17, UL_TFC24, UL_TFC29, UL_TFC36, UL_TFC41	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits
18	DL_TFC18, DL_TFC48	UL_TFC18, UL_TFC42	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC24, UL_TFC42	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits	RB5: No data RB6: No data RB7: No data RB8: 952 bits
19	DL_TFC19, DL_TFC49	UL_TFC19, UL_TFC43	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC19, UL_TFC24, UL_TFC25, UL_TFC42, UL_TFC43	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 952 bits
20	DL_TFC20, DL_TFC50	UL_TFC20, UL_TFC44	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC20, UL_TFC24, UL_TFC26, UL_TFC42, UL_TFC44	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 1272 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 952 bits
21	DL_TFC21, DL_TFC51	UL_TFC21, UL_TFC45	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC21, UL_TFC24, UL_TFC27, UL_TFC42, UL_TFC45	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 1272 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 952 bits

<u>22</u>	<u>DL_TFC22,</u> <u>DL_TFC52</u>	<u>UL_TFC22,</u> <u>UL_TFC46</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC22,</u> <u>UL_TFC24,</u> <u>UL_TFC28,</u> <u>UL_TFC42,</u> <u>UL_TFC46</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 952 bits</u>
<u>23</u>	<u>DL_TFC23,</u> <u>DL_TFC53</u>	<u>UL_TFC23,</u> <u>UL_TFC47</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC23,</u> <u>UL_TFC24,</u> <u>UL_TFC29,</u> <u>UL_TFC42,</u> <u>UL_TFC47</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 952 bits</u>
<u>24</u>	<u>DL_TFC24,</u> <u>DL_TFC54</u>	<u>UL_TFC18,</u> <u>UL_TFC42</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC24,</u> <u>UL_TFC42</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 1272 bits</u>
<u>25</u>	<u>DL_TFC25,</u> <u>DL_TFC55</u>	<u>UL_TFC19,</u> <u>UL_TFC43</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC19,</u> <u>UL_TFC24,</u> <u>UL_TFC25,</u> <u>UL_TFC42,</u> <u>UL_TFC43</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 1272 bits</u>
<u>26</u>	<u>DL_TFC26,</u> <u>DL_TFC56</u>	<u>UL_TFC20,</u> <u>UL_TFC44</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC20,</u> <u>UL_TFC24,</u> <u>UL_TFC26,</u> <u>UL_TFC42,</u> <u>UL_TFC44</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: 1272 bits</u>

<u>27</u>	<u>DL_TFC27,</u> <u>DL_TFC57</u>	<u>UL_TFC21,</u> <u>UL_TFC45</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC21,</u> <u>UL_TFC24,</u> <u>UL_TFC27,</u> <u>UL_TFC42,</u> <u>UL_TFC45</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: 1272 bits</u>
<u>28</u>	<u>DL_TFC28,</u> <u>DL_TFC58</u>	<u>UL_TFC22,</u> <u>UL_TFC46</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC22,</u> <u>UL_TFC24,</u> <u>UL_TFC28,</u> <u>UL_TFC42,</u> <u>UL_TFC46</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 1272 bits</u>
<u>29</u>	<u>DL_TFC29,</u> <u>DL_TFC59</u>	<u>UL_TFC23,</u> <u>UL_TFC47</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC23,</u> <u>UL_TFC24,</u> <u>UL_TFC29,</u> <u>UL_TFC42,</u> <u>UL_TFC47</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>
<p><u>NOTE 1: UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6 and UL_TFC24 are part of minimum set of TFCIs.</u></p> <p><u>NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size parameter has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.38i.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2,3 and 4: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
 - for sub-test 5: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.

- for sub-test 6: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 7: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 8,9 and 10: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
- for sub-test 11: RLC SDUs on RB5, RB6 , RB7 and RB8 having the same content as sent by the SS.
- for sub-test 12: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 14,15 and 16: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
- for sub-test 17: RLC SDUs on RB5, RB6 , RB7 and RB8 having the same content as sent by the SS.
- for sub-test 18: RLC SDUs on RB8 having the same content as 1 times plus 320 lsb's of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6, and RB7.
- for sub-test 19: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as 1 times plus 320 lsb's of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6, and RB7.
- for sub-test 20,21 and 22: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as 1 times plus 320 lsb's of the test data sent by the SS in downlink; and no data shall be received on RB7.
- for sub-test 23: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and RLC SDUs on RB8 having the same content as 1 times plus 320 lsb's of the test data sent by the SS in downlink.
- for sub-test 24: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 25: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 26,27 and 28: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
- for sub-test 29: RLC SDUs on RB5, RB6 , RB7 and RB8 having the same content as sent by the SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38i.2 Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128.

18.2.2.38i.2.1 Conformance requirement

See clause 18.2.2.4.1.

18.2.2.38i.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38i for the uplink payload 128 case.

18.2.2.38i.2.3 Method of test

See clause 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(64 kbps, 20 ms</u> <u>TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>3x144</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>7x144</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>10x144</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF0, TF0, TF0)
UL_TFC3	(TF3, TF2, TF0, TF0, TF0)
UL_TFC4	(TF4, TF3, TF0, TF0, TF0)
UL_TFC5	(TF5, TF4, TF1, TF0, TF0)
UL_TFC6	(TF0, TF0, TF0, TF1, TF0)
UL_TFC7	(TF1, TF0, TF0, TF1, TF0)
UL_TFC8	(TF2, TF1, TF0, TF1, TF0)
UL_TFC9	(TF3, TF2, TF0, TF1, TF0)
UL_TFC10	(TF4, TF3, TF0, TF1, TF0)
UL_TFC11	(TF5, TF4, TF1, TF1, TF0)
UL_TFC12	(TF0, TF0, TF0, TF2, TF0)
UL_TFC13	(TF1, TF0, TF0, TF2, TF0)
UL_TFC14	(TF2, TF1, TF0, TF2, TF0)
UL_TFC15	(TF3, TF2, TF0, TF2, TF0)
UL_TFC16	(TF4, TF3, TF0, TF2, TF0)
UL_TFC17	(TF5, TF4, TF1, TF2, TF0)
UL_TFC18	(TF0, TF0, TF0, TF4, TF0)
UL_TFC19	(TF1, TF0, TF0, TF4, TF0)
UL_TFC20	(TF2, TF1, TF0, TF4, TF0)
UL_TFC21	(TF3, TF2, TF0, TF4, TF0)
UL_TFC22	(TF4, TF3, TF0, TF4, TF0)
UL_TFC23	(TF5, TF4, TF1, TF4, TF0)
UL_TFC24	(TF0, TF0, TF0, TF0, TF1)
UL_TFC25	(TF1, TF0, TF0, TF0, TF1)
UL_TFC26	(TF2, TF1, TF0, TF0, TF1)
UL_TFC27	(TF3, TF2, TF0, TF0, TF1)
UL_TFC28	(TF4, TF3, TF0, TF0, TF1)
UL_TFC29	(TF5, TF4, TF1, TF0, TF1)
UL_TFC30	(TF0, TF0, TF0, TF1, TF1)
UL_TFC31	(TF1, TF0, TF0, TF1, TF1)
UL_TFC32	(TF2, TF1, TF0, TF1, TF1)
UL_TFC33	(TF3, TF2, TF0, TF1, TF1)
UL_TFC34	(TF4, TF3, TF0, TF1, TF1)
UL_TFC35	(TF5, TF4, TF1, TF1, TF1)
UL_TFC36	(TF0, TF0, TF0, TF2, TF1)
UL_TFC37	(TF1, TF0, TF0, TF2, TF1)
UL_TFC38	(TF2, TF1, TF0, TF2, TF1)
UL_TFC39	(TF3, TF2, TF0, TF2, TF1)
UL_TFC40	(TF4, TF3, TF0, TF2, TF1)
UL_TFC41	(TF5, TF4, TF1, TF2, TF1)
UL_TFC42	(TF0, TF0, TF0, TF4, TF1)
UL_TFC43	(TF1, TF0, TF0, TF4, TF1)
UL_TFC44	(TF2, TF1, TF0, TF4, TF1)
UL_TFC45	(TF3, TF2, TF0, TF4, TF1)
UL_TFC46	(TF4, TF3, TF0, TF4, TF1)
UL_TFC47	(TF5, TF4, TF1, TF4, TF1)

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (64 kbps, 20 ms TTI)	DCCH
TFS	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x53	1x60	1x336	1x148
	TF2, bits	1x42	1x63	N/A	2x336	N/A
	TF3, bits	1x55	1x84	N/A	3x336	N/A
	TF4, bits	1x75	1x103	N/A	4x336	N/A
	TF5, bits	1x81	N/A	N/A	N/A	N/A

[Downlink TFCS:](#)

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0,TF0,TF0,TF0,TF0)
DL_TFC1	(TF1,TF0,TF0,TF0,TF0)
DL_TFC2	(TF2,TF1,TF0,TF0,TF0)
DL_TFC3	(TF3,TF2,TF0,TF0,TF0)
DL_TFC4	(TF4,TF3,TF0,TF0,TF0)
DL_TFC5	(TF5,TF4,TF1,TF0,TF0)
DL_TFC6	(TF0,TF0,TF0,TF1,TF0)
DL_TFC7	(TF1,TF0,TF0,TF1,TF0)
DL_TFC8	(TF2,TF1,TF0,TF1,TF0)
DL_TFC9	(TF3,TF2,TF0,TF1,TF0)
DL_TFC10	(TF4,TF3,TF0,TF1,TF0)
DL_TFC11	(TF5,TF4,TF1,TF1,TF0)
DL_TFC12	(TF0,TF0,TF0,TF2,TF0)
DL_TFC13	(TF1,TF0,TF0,TF2,TF0)
DL_TFC14	(TF2,TF1,TF0,TF2,TF0)
DL_TFC15	(TF3,TF2,TF0,TF2,TF0)
DL_TFC16	(TF4,TF3,TF0,TF2,TF0)
DL_TFC17	(TF5,TF4,TF1,TF2,TF0)
DL_TFC18	(TF0,TF0,TF0,TF3,TF0)
DL_TFC19	(TF1,TF0,TF0,TF3,TF0)
DL_TFC20	(TF2,TF1,TF0,TF3,TF0)
DL_TFC21	(TF3,TF2,TF0,TF3,TF0)
DL_TFC22	(TF4,TF3,TF0,TF3,TF0)
DL_TFC23	(TF5,TF4,TF1,TF3,TF0)
DL_TFC24	(TF0,TF0,TF0,TF4,TF0)
DL_TFC25	(TF1,TF0,TF0,TF4,TF0)
DL_TFC26	(TF2,TF1,TF0,TF4,TF0)
DL_TFC27	(TF3,TF2,TF0,TF4,TF0)
DL_TFC28	(TF4,TF3,TF0,TF4,TF0)
DL_TFC29	(TF5,TF4,TF1,TF4,TF0)
DL_TFC30	(TF0,TF0,TF0,TF0,TF1)
DL_TFC31	(TF1,TF0,TF0,TF0,TF1)
DL_TFC32	(TF2,TF1,TF0,TF0,TF1)
DL_TFC33	(TF3,TF2,TF0,TF0,TF1)
DL_TFC34	(TF4,TF3,TF0,TF0,TF1)
DL_TFC35	(TF5,TF4,TF1,TF0,TF1)
DL_TFC36	(TF0,TF0,TF0,TF1,TF1)
DL_TFC37	(TF1,TF0,TF0,TF1,TF1)
DL_TFC38	(TF2,TF1,TF0,TF1,TF1)
DL_TFC39	(TF3,TF2,TF0,TF1,TF1)
DL_TFC40	(TF4,TF3,TF0,TF1,TF1)
DL_TFC41	(TF5,TF4,TF1,TF1,TF1)
DL_TFC42	(TF0,TF0,TF0,TF2,TF1)
DL_TFC43	(TF1,TF0,TF0,TF2,TF1)
DL_TFC44	(TF2,TF1,TF0,TF2,TF1)
DL_TFC45	(TF3,TF2,TF0,TF2,TF1)
DL_TFC46	(TF4,TF3,TF0,TF2,TF1)
DL_TFC47	(TF5,TF4,TF1,TF2,TF1)
DL_TFC48	(TF0,TF0,TF0,TF3,TF1)
DL_TFC49	(TF1,TF0,TF0,TF3,TF1)
DL_TFC50	(TF2,TF1,TF0,TF3,TF1)
DL_TFC51	(TF3,TF2,TF0,TF3,TF1)
DL_TFC52	(TF4,TF3,TF0,TF3,TF1)
DL_TFC53	(TF5,TF4,TF1,TF3,TF1)
DL_TFC54	(TF0,TF0,TF0,TF4,TF1)
DL_TFC55	(TF1,TF0,TF0,TF4,TF1)
DL_TFC56	(TF2,TF1,TF0,TF4,TF1)
DL_TFC57	(TF3,TF2,TF0,TF4,TF1)
DL_TFC58	(TF4,TF3,TF0,TF4,TF1)
DL_TFC59	(TF5,TF4,TF1,TF4,TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> <u>(note 1)</u>	<u>UL RLC SDU size</u> <u>(note 2)</u>	<u>Test data size</u> <u>(note 2)</u>
<u>1</u>	<u>DL TFC1,</u> <u>DL TFC31</u>	<u>UL TFC1,</u> <u>UL TFC25</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC25</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>2</u>	<u>DL TFC2,</u> <u>DL TFC32</u>	<u>UL TFC2,</u> <u>UL TFC26</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC26</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>3</u>	<u>DL TFC3,</u> <u>DL TFC33</u>	<u>UL TFC3,</u> <u>UL TFC27</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC27</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>4</u>	<u>DL TFC4,</u> <u>DL TFC34</u>	<u>UL TFC4,</u> <u>UL TFC28</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC28</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>5</u>	<u>DL TFC5,</u> <u>DL TFC35</u>	<u>UL TFC5,</u> <u>UL TFC29</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC29</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: No data</u>
<u>6</u>	<u>DL TFC6,</u> <u>DL TFC36</u>	<u>UL TFC6,</u> <u>UL TFC30</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC30</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>

<u>7</u>	<u>DL_TFC7,</u> <u>DL_TFC37</u>	<u>UL_TFC7,</u> <u>UL_TFC31</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC24,</u> <u>UL_TFC25,</u> <u>UL_TFC30,</u> <u>UL_TFC31</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>8</u>	<u>DL_TFC8,</u> <u>DL_TFC38</u>	<u>UL_TFC8,</u> <u>UL_TFC32</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC24,</u> <u>UL_TFC26,</u> <u>UL_TFC30,</u> <u>UL_TFC32</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>9</u>	<u>DL_TFC9,</u> <u>DL_TFC39</u>	<u>UL_TFC9,</u> <u>UL_TFC33</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC9,</u> <u>UL_TFC24,</u> <u>UL_TFC27,</u> <u>UL_TFC30,</u> <u>UL_TFC33</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>10</u>	<u>DL_TFC10,</u> <u>DL_TFC40</u>	<u>UL_TFC10,</u> <u>UL_TFC34</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC10,</u> <u>UL_TFC24,</u> <u>UL_TFC28,</u> <u>UL_TFC30,</u> <u>UL_TFC34</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>11</u>	<u>DL_TFC11,</u> <u>DL_TFC41</u>	<u>UL_TFC11,</u> <u>UL_TFC35</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC11,</u> <u>UL_TFC24,</u> <u>UL_TFC29,</u> <u>UL_TFC30,</u> <u>UL_TFC35</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>

12	DL_TFC12, DL_TFC42	UL_TFC12, UL_TFC36	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC24, UL_TFC36	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 376 bits	RB5: No data RB6: No data RB7: No data RB8: 632 bits
13	DL_TFC13, DL_TFC43	UL_TFC13, UL_TFC37	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC13, UL_TFC24, UL_TFC25, UL_TFC36, UL_TFC37	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 376 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 632 bits
14	DL_TFC14, DL_TFC44	UL_TFC14, UL_TFC38	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC14, UL_TFC24, UL_TFC26, UL_TFC36, UL_TFC38	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 376 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 632 bits
15	DL_TFC15, DL_TFC45	UL_TFC15, UL_TFC39	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC15, UL_TFC24, UL_TFC27, UL_TFC36, UL_TFC39	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 376 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 632 bits
16	DL_TFC16, DL_TFC46	UL_TFC16, UL_TFC40	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC16, UL_TFC24, UL_TFC28, UL_TFC36, UL_TFC40	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 376 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 632 bits

17	DL_TFC17, DL_TFC47	UL_TFC17, UL_TFC41	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC17, UL_TFC24, UL_TFC29, UL_TFC36, UL_TFC41	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 376 bits	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits
18	DL_TFC18, DL_TFC48	UL_TFC18, UL_TFC42	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC24, UL_TFC42	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits	RB5: No data RB6: No data RB7: No data RB8: 952 bits
19	DL_TFC19, DL_TFC49	UL_TFC19, UL_TFC43	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC19, UL_TFC24, UL_TFC25, UL_TFC42, UL_TFC43	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 952 bits
20	DL_TFC20, DL_TFC50	UL_TFC20, UL_TFC44	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC20, UL_TFC24, UL_TFC26, UL_TFC42, UL_TFC44	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 1272 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 952 bits
21	DL_TFC21, DL_TFC51	UL_TFC21, UL_TFC45	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC21, UL_TFC24, UL_TFC27, UL_TFC42, UL_TFC45	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 1272 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 952 bits

<u>22</u>	<u>DL_TFC22,</u> <u>DL_TFC52</u>	<u>UL_TFC22,</u> <u>UL_TFC46</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC22,</u> <u>UL_TFC24,</u> <u>UL_TFC28,</u> <u>UL_TFC42,</u> <u>UL_TFC46</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 952 bits</u>
<u>23</u>	<u>DL_TFC23,</u> <u>DL_TFC53</u>	<u>UL_TFC23,</u> <u>UL_TFC47</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC23,</u> <u>UL_TFC24,</u> <u>UL_TFC29,</u> <u>UL_TFC42,</u> <u>UL_TFC47</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 952 bits</u>
<u>24</u>	<u>DL_TFC24,</u> <u>DL_TFC54</u>	<u>UL_TFC18,</u> <u>UL_TFC42</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC24,</u> <u>UL_TFC42</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 1272 bits</u>
<u>25</u>	<u>DL_TFC25,</u> <u>DL_TFC55</u>	<u>UL_TFC19,</u> <u>UL_TFC43</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC19,</u> <u>UL_TFC24,</u> <u>UL_TFC25,</u> <u>UL_TFC42,</u> <u>UL_TFC43</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 1272 bits</u>
<u>26</u>	<u>DL_TFC26,</u> <u>DL_TFC56</u>	<u>UL_TFC20,</u> <u>UL_TFC44</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC20,</u> <u>UL_TFC24,</u> <u>UL_TFC26,</u> <u>UL_TFC42,</u> <u>UL_TFC44</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: 1272 bits</u>

27	DL_TFC27, DL_TFC57	UL_TFC21, UL_TFC45	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC21, UL_TFC24, UL_TFC27, UL_TFC42, UL_TFC45	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 1272 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 1272 bits
28	DL_TFC28, DL_TFC58	UL_TFC22, UL_TFC46	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC22, UL_TFC24, UL_TFC28, UL_TFC42, UL_TFC46	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 1272 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 1272 bits
29	DL_TFC29, DL_TFC59	UL_TFC23, UL_TFC47	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC23, UL_TFC24, UL_TFC29, UL_TFC42, UL_TFC47	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits
<p>NOTE 1: UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6 and UL_TFC24 are part of minimum set of TFCIs.</p> <p>NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size parameter has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</p>						

[18.2.2.38i.2.4 Test requirements](#)

[See 18.2.1.2 for definition of step 10 and step 15.](#)

- [1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.](#)
- [2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.](#)
- [3. At step 15 the UE shall return](#)
 - [- for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.](#)
 - [- for sub-test 2,3 and 4: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.](#)
 - [- for sub-test 5: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.](#)

- for sub-test 6: RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 7: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
 - for sub-test 8,9 and 10: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB7.
 - for sub-test 11: RLC SDUs on RB5, RB6 , and RB7 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink.
 - for sub-test 12: RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 13: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS; and no data shall be received on RB6 and RB7.
 - for sub-test 14,15 and 16: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS; and no data shall be received on RB7.
 - for sub-test 17: RLC SDUs on RB5, RB6 , and RB7 having the same content as sent by the SS; and RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS.
 - for sub-test 18: RLC SDUs on RB8 having the same content as 1 times plus 320 lsb's of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6, and RB7.
 - for sub-test 19: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as 1 times plus 320 lsb's of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6, and RB7.
 - for sub-test 20,21 and 22: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as 1 times plus 320 lsb's of the test data sent by the SS in downlink; and no data shall be received on RB7.
 - for sub-test 23: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and RLC SDUs on RB8 having the same content as 1 times plus 320 lsb's of the test data sent by the SS in downlink.
 - for sub-test 24: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 25: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
 - for sub-test 26,27 and 28: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
 - for sub-test 29: RLC SDUs on RB5, RB6 , RB7 and RB8 having the same content as sent by the SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38j Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:64 DL:128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.38j.1 Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:64 DL:128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320.

18.2.2.38j.1.1 Conformance requirement

See clause 18.2.2.4.1.

18.2.2.38j.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38j for the uplink payload 320 case.

18.2.2.38j.1.3 Method of test

See clause 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(64 kbps, 20 ms</u> <u>TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF0, TF0, TF0)
UL_TFC3	(TF3, TF2, TF0, TF0, TF0)
UL_TFC4	(TF4, TF3, TF0, TF0, TF0)
UL_TFC5	(TF5, TF4, TF1, TF0, TF0)
UL_TFC6	(TF0, TF0, TF0, TF1, TF0)
UL_TFC7	(TF1, TF0, TF0, TF1, TF0)
UL_TFC8	(TF2, TF1, TF0, TF1, TF0)
UL_TFC9	(TF3, TF2, TF0, TF1, TF0)
UL_TFC10	(TF4, TF3, TF0, TF1, TF0)
UL_TFC11	(TF5, TF4, TF1, TF1, TF0)
UL_TFC12	(TF0, TF0, TF0, TF2, TF0)
UL_TFC13	(TF1, TF0, TF0, TF2, TF0)
UL_TFC14	(TF2, TF1, TF0, TF2, TF0)
UL_TFC15	(TF3, TF2, TF0, TF2, TF0)
UL_TFC16	(TF4, TF3, TF0, TF2, TF0)
UL_TFC17	(TF5, TF4, TF1, TF2, TF0)
UL_TFC18	(TF0, TF0, TF0, TF4, TF0)
UL_TFC19	(TF1, TF0, TF0, TF4, TF0)
UL_TFC20	(TF2, TF1, TF0, TF4, TF0)
UL_TFC21	(TF3, TF2, TF0, TF4, TF0)
UL_TFC22	(TF4, TF3, TF0, TF4, TF0)
UL_TFC23	(TF5, TF4, TF1, TF4, TF0)
UL_TFC24	(TF0, TF0, TF0, TF0, TF1)
UL_TFC25	(TF1, TF0, TF0, TF0, TF1)
UL_TFC26	(TF2, TF1, TF0, TF0, TF1)
UL_TFC27	(TF3, TF2, TF0, TF0, TF1)
UL_TFC28	(TF4, TF3, TF0, TF0, TF1)
UL_TFC29	(TF5, TF4, TF1, TF0, TF1)
UL_TFC30	(TF0, TF0, TF0, TF1, TF1)
UL_TFC31	(TF1, TF0, TF0, TF1, TF1)
UL_TFC32	(TF2, TF1, TF0, TF1, TF1)
UL_TFC33	(TF3, TF2, TF0, TF1, TF1)
UL_TFC34	(TF4, TF3, TF0, TF1, TF1)
UL_TFC35	(TF5, TF4, TF1, TF1, TF1)
UL_TFC36	(TF0, TF0, TF0, TF2, TF1)
UL_TFC37	(TF1, TF0, TF0, TF2, TF1)
UL_TFC38	(TF2, TF1, TF0, TF2, TF1)
UL_TFC39	(TF3, TF2, TF0, TF2, TF1)
UL_TFC40	(TF4, TF3, TF0, TF2, TF1)
UL_TFC41	(TF5, TF4, TF1, TF2, TF1)
UL_TFC42	(TF0, TF0, TF0, TF4, TF1)
UL_TFC43	(TF1, TF0, TF0, TF4, TF1)
UL_TFC44	(TF2, TF1, TF0, TF4, TF1)
UL_TFC45	(TF3, TF2, TF0, TF4, TF1)
UL_TFC46	(TF4, TF3, TF0, TF4, TF1)
UL_TFC47	(TF5, TF4, TF1, TF4, TF1)

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (128 kbps, 20 ms TTI)	DCCH
TFS	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x53	1x60	1x336	1x148
	TF2, bits	1x42	1x63	N/A	2x336	N/A
	TF3, bits	1x55	1x84	N/A	4x336	N/A
	TF4, bits	1x75	1x103	N/A	8x336	N/A
	TF5, bits	1x81	N/A	N/A	N/A	N/A

[Downlink TFCS:](#)

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0,TF0,TF0,TF0,TF0)
DL_TFC1	(TF1,TF0,TF0,TF0,TF0)
DL_TFC2	(TF2,TF1,TF0,TF0,TF0)
DL_TFC3	(TF3,TF2,TF0,TF0,TF0)
DL_TFC4	(TF4,TF3,TF0,TF0,TF0)
DL_TFC5	(TF5,TF4,TF1,TF0,TF0)
DL_TFC6	(TF0,TF0,TF0,TF1,TF0)
DL_TFC7	(TF1,TF0,TF0,TF1,TF0)
DL_TFC8	(TF2,TF1,TF0,TF1,TF0)
DL_TFC9	(TF3,TF2,TF0,TF1,TF0)
DL_TFC10	(TF4,TF3,TF0,TF1,TF0)
DL_TFC11	(TF5,TF4,TF1,TF1,TF0)
DL_TFC12	(TF0,TF0,TF0,TF2,TF0)
DL_TFC13	(TF1,TF0,TF0,TF2,TF0)
DL_TFC14	(TF2,TF1,TF0,TF2,TF0)
DL_TFC15	(TF3,TF2,TF0,TF2,TF0)
DL_TFC16	(TF4,TF3,TF0,TF2,TF0)
DL_TFC17	(TF5,TF4,TF1,TF2,TF0)
DL_TFC18	(TF0,TF0,TF0,TF3,TF0)
DL_TFC19	(TF1,TF0,TF0,TF3,TF0)
DL_TFC20	(TF2,TF1,TF0,TF3,TF0)
DL_TFC21	(TF3,TF2,TF0,TF3,TF0)
DL_TFC22	(TF4,TF3,TF0,TF3,TF0)
DL_TFC23	(TF5,TF4,TF1,TF3,TF0)
DL_TFC24	(TF0,TF0,TF0,TF4,TF0)
DL_TFC25	(TF1,TF0,TF0,TF4,TF0)
DL_TFC26	(TF2,TF1,TF0,TF4,TF0)
DL_TFC27	(TF3,TF2,TF0,TF4,TF0)
DL_TFC28	(TF4,TF3,TF0,TF4,TF0)
DL_TFC29	(TF5,TF4,TF1,TF4,TF0)
DL_TFC30	(TF0,TF0,TF0,TF0,TF1)
DL_TFC31	(TF1,TF0,TF0,TF0,TF1)
DL_TFC32	(TF2,TF1,TF0,TF0,TF1)
DL_TFC33	(TF3,TF2,TF0,TF0,TF1)
DL_TFC34	(TF4,TF3,TF0,TF0,TF1)
DL_TFC35	(TF5,TF4,TF1,TF0,TF1)
DL_TFC36	(TF0,TF0,TF0,TF1,TF1)
DL_TFC37	(TF1,TF0,TF0,TF1,TF1)
DL_TFC38	(TF2,TF1,TF0,TF1,TF1)
DL_TFC39	(TF3,TF2,TF0,TF1,TF1)
DL_TFC40	(TF4,TF3,TF0,TF1,TF1)
DL_TFC41	(TF5,TF4,TF1,TF1,TF1)
DL_TFC42	(TF0,TF0,TF0,TF2,TF1)
DL_TFC43	(TF1,TF0,TF0,TF2,TF1)
DL_TFC44	(TF2,TF1,TF0,TF2,TF1)
DL_TFC45	(TF3,TF2,TF0,TF2,TF1)
DL_TFC46	(TF4,TF3,TF0,TF2,TF1)
DL_TFC47	(TF5,TF4,TF1,TF2,TF1)
DL_TFC48	(TF0,TF0,TF0,TF3,TF1)
DL_TFC49	(TF1,TF0,TF0,TF3,TF1)
DL_TFC50	(TF2,TF1,TF0,TF3,TF1)
DL_TFC51	(TF3,TF2,TF0,TF3,TF1)
DL_TFC52	(TF4,TF3,TF0,TF3,TF1)
DL_TFC53	(TF5,TF4,TF1,TF3,TF1)
DL_TFC54	(TF0,TF0,TF0,TF4,TF1)
DL_TFC55	(TF1,TF0,TF0,TF4,TF1)
DL_TFC56	(TF2,TF1,TF0,TF4,TF1)
DL_TFC57	(TF3,TF2,TF0,TF4,TF1)
DL_TFC58	(TF4,TF3,TF0,TF4,TF1)
DL_TFC59	(TF5,TF4,TF1,TF4,TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size</u> (note 2)	<u>Test data size</u> (note 2)
<u>1</u>	<u>DL TFC1,</u> <u>DL TFC31</u>	<u>UL TFC1,</u> <u>UL TFC25</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC25</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>2</u>	<u>DL TFC2,</u> <u>DL TFC32</u>	<u>UL TFC2,</u> <u>UL TFC26</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC26</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>3</u>	<u>DL TFC3,</u> <u>DL TFC33</u>	<u>UL TFC3,</u> <u>UL TFC27</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC27</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>4</u>	<u>DL TFC4,</u> <u>DL TFC34</u>	<u>UL TFC4,</u> <u>UL TFC28</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC28</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>5</u>	<u>DL TFC5,</u> <u>DL TFC35</u>	<u>UL TFC5,</u> <u>UL TFC29</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC29</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: No data</u>
<u>6</u>	<u>DL TFC6,</u> <u>DL TFC36</u>	<u>UL TFC6,</u> <u>UL TFC30</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC30</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>

<u>7</u>	<u>DL_TFC7,</u> <u>DL_TFC37</u>	<u>UL_TFC7,</u> <u>UL_TFC31</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC24,</u> <u>UL_TFC25,</u> <u>UL_TFC30,</u> <u>UL_TFC31</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>8</u>	<u>DL_TFC8,</u> <u>DL_TFC38</u>	<u>UL_TFC8,</u> <u>UL_TFC32</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC24,</u> <u>UL_TFC26,</u> <u>UL_TFC30,</u> <u>UL_TFC32</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>9</u>	<u>DL_TFC9,</u> <u>DL_TFC39</u>	<u>UL_TFC9,</u> <u>UL_TFC33</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC9,</u> <u>UL_TFC24,</u> <u>UL_TFC27,</u> <u>UL_TFC30,</u> <u>UL_TFC33</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>10</u>	<u>DL_TFC10,</u> <u>DL_TFC40</u>	<u>UL_TFC10,</u> <u>UL_TFC34</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC10,</u> <u>UL_TFC24,</u> <u>UL_TFC28,</u> <u>UL_TFC30,</u> <u>UL_TFC34</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>11</u>	<u>DL_TFC11,</u> <u>DL_TFC41</u>	<u>UL_TFC11,</u> <u>UL_TFC35</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC11,</u> <u>UL_TFC24,</u> <u>UL_TFC29,</u> <u>UL_TFC30,</u> <u>UL_TFC35</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>

12	DL_TFC12, DL_TFC42	UL_TFC12, UL_TFC36	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC24, UL_TFC36	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits	RB5: No data RB6: No data RB7: No data RB8: 632 bits
13	DL_TFC13, DL_TFC43	UL_TFC13, UL_TFC37	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC13, UL_TFC24, UL_TFC25, UL_TFC36, UL_TFC37	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 632 bits
14	DL_TFC14, DL_TFC44	UL_TFC14, UL_TFC38	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC14, UL_TFC24, UL_TFC26, UL_TFC36, UL_TFC38	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 632 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 632 bits
15	DL_TFC15, DL_TFC45	UL_TFC15, UL_TFC39	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC15, UL_TFC24, UL_TFC27, UL_TFC36, UL_TFC39	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 632 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 632 bits
16	DL_TFC16, DL_TFC46	UL_TFC16, UL_TFC40	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC16, UL_TFC24, UL_TFC28, UL_TFC36, UL_TFC40	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 632 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 632 bits

17	DL_TFC17, DL_TFC47	UL_TFC17, UL_TFC41	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC17, UL_TFC24, UL_TFC29, UL_TFC36, UL_TFC41	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits
18	DL_TFC18, DL_TFC48	UL_TFC18, UL_TFC42	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC24, UL_TFC42	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits	RB5: No data RB6: No data RB7: No data RB8: 1272 bits
19	DL_TFC19, DL_TFC49	UL_TFC19, UL_TFC43	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC19, UL_TFC24, UL_TFC25, UL_TFC42, UL_TFC43	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 1272 bits
20	DL_TFC20, DL_TFC50	UL_TFC20, UL_TFC44	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC20, UL_TFC24, UL_TFC26, UL_TFC42, UL_TFC44	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 1272 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 1272 bits
21	DL_TFC21, DL_TFC51	UL_TFC21, UL_TFC45	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC21, UL_TFC24, UL_TFC27, UL_TFC42, UL_TFC45	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 1272 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 1272 bits

<u>22</u>	<u>DL_TFC22,</u> <u>DL_TFC52</u>	<u>UL_TFC22,</u> <u>UL_TFC46</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC22,</u> <u>UL_TFC24,</u> <u>UL_TFC28,</u> <u>UL_TFC42,</u> <u>UL_TFC46</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 1272 bits</u>
<u>23</u>	<u>DL_TFC23,</u> <u>DL_TFC53</u>	<u>UL_TFC23,</u> <u>UL_TFC47</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC23,</u> <u>UL_TFC24,</u> <u>UL_TFC29,</u> <u>UL_TFC42,</u> <u>UL_TFC47</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>
<u>24</u>	<u>DL_TFC24,</u> <u>DL_TFC54</u>	<u>UL_TFC18,</u> <u>UL_TFC42</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC24,</u> <u>UL_TFC42</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 2552 bits</u>
<u>25</u>	<u>DL_TFC25,</u> <u>DL_TFC55</u>	<u>UL_TFC19,</u> <u>UL_TFC43</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC19,</u> <u>UL_TFC24,</u> <u>UL_TFC25,</u> <u>UL_TFC42,</u> <u>UL_TFC43</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 2552 bits</u>
<u>26</u>	<u>DL_TFC26,</u> <u>DL_TFC56</u>	<u>UL_TFC20,</u> <u>UL_TFC44</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC20,</u> <u>UL_TFC24,</u> <u>UL_TFC26,</u> <u>UL_TFC42,</u> <u>UL_TFC44</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: 2552 bits</u>

<u>27</u>	<u>DL_TFC27,</u> <u>DL_TFC57</u>	<u>UL_TFC21,</u> <u>UL_TFC45</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC21,</u> <u>UL_TFC24,</u> <u>UL_TFC27,</u> <u>UL_TFC42,</u> <u>UL_TFC45</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: 2552 bits</u>
<u>28</u>	<u>DL_TFC28,</u> <u>DL_TFC58</u>	<u>UL_TFC22,</u> <u>UL_TFC46</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC22,</u> <u>UL_TFC24,</u> <u>UL_TFC28,</u> <u>UL_TFC42,</u> <u>UL_TFC46</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 2552 bits</u>
<u>29</u>	<u>DL_TFC29,</u> <u>DL_TFC59</u>	<u>UL_TFC23,</u> <u>UL_TFC47</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC23,</u> <u>UL_TFC24,</u> <u>UL_TFC29,</u> <u>UL_TFC42,</u> <u>UL_TFC47</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 2552 bits</u>
<p><u>NOTE 1: UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6 and UL_TFC24 are part of minimum set of TFCIs.</u></p> <p><u>NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size parameter has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.38j.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2,3 and 4: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
 - for sub-test 5: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.

- for sub-test 6: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 7: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 8,9 and 10: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
- for sub-test 11: RLC SDUs on RB5, RB6 , RB7 and RB8 having the same content as sent by the SS.
- for sub-test 12: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 14,15 and 16: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
- for sub-test 17: RLC SDUs on RB5, RB6 , RB7 and RB8 having the same content as sent by the SS.
- for sub-test 18: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 19: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 20,21 and 22: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
- for sub-test 23: RLC SDUs on RB5, RB6 , RB7 and RB8 having the same content as sent by the SS.
- for sub-test 24: RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 25: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
- for sub-test 26,27 and 28: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB7.
- for sub-test 29: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.38j.2 Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL:(12.2 7.95 5.9 4.75) kbps / CS RAB + Interactive or background / UL:64 DL:128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128.

18.2.2.38j.2.1 Conformance requirement

See clause 18.2.2.4.1.

18.2.2.38j.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.38j for the uplink payload 128 case.

18.2.2.38j.2.3 Method of test

See clause 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(64 kbps, 20 ms</u> <u>TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x53</u>	<u>1x60</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x42</u>	<u>1x63</u>	<u>N/A</u>	<u>3x144</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>1x55</u>	<u>1x84</u>	<u>N/A</u>	<u>7x144</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>1x75</u>	<u>1x103</u>	<u>N/A</u>	<u>10x144</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF0, TF0, TF0)
UL_TFC3	(TF3, TF2, TF0, TF0, TF0)
UL_TFC4	(TF4, TF3, TF0, TF0, TF0)
UL_TFC5	(TF5, TF4, TF1, TF0, TF0)
UL_TFC6	(TF0, TF0, TF0, TF1, TF0)
UL_TFC7	(TF1, TF0, TF0, TF1, TF0)
UL_TFC8	(TF2, TF1, TF0, TF1, TF0)
UL_TFC9	(TF3, TF2, TF0, TF1, TF0)
UL_TFC10	(TF4, TF3, TF0, TF1, TF0)
UL_TFC11	(TF5, TF4, TF1, TF1, TF0)
UL_TFC12	(TF0, TF0, TF0, TF2, TF0)
UL_TFC13	(TF1, TF0, TF0, TF2, TF0)
UL_TFC14	(TF2, TF1, TF0, TF2, TF0)
UL_TFC15	(TF3, TF2, TF0, TF2, TF0)
UL_TFC16	(TF4, TF3, TF0, TF2, TF0)
UL_TFC17	(TF5, TF4, TF1, TF2, TF0)
UL_TFC18	(TF0, TF0, TF0, TF4, TF0)
UL_TFC19	(TF1, TF0, TF0, TF4, TF0)
UL_TFC20	(TF2, TF1, TF0, TF4, TF0)
UL_TFC21	(TF3, TF2, TF0, TF4, TF0)
UL_TFC22	(TF4, TF3, TF0, TF4, TF0)
UL_TFC23	(TF5, TF4, TF1, TF4, TF0)
UL_TFC24	(TF0, TF0, TF0, TF0, TF1)
UL_TFC25	(TF1, TF0, TF0, TF0, TF1)
UL_TFC26	(TF2, TF1, TF0, TF0, TF1)
UL_TFC27	(TF3, TF2, TF0, TF0, TF1)
UL_TFC28	(TF4, TF3, TF0, TF0, TF1)
UL_TFC29	(TF5, TF4, TF1, TF0, TF1)
UL_TFC30	(TF0, TF0, TF0, TF1, TF1)
UL_TFC31	(TF1, TF0, TF0, TF1, TF1)
UL_TFC32	(TF2, TF1, TF0, TF1, TF1)
UL_TFC33	(TF3, TF2, TF0, TF1, TF1)
UL_TFC34	(TF4, TF3, TF0, TF1, TF1)
UL_TFC35	(TF5, TF4, TF1, TF1, TF1)
UL_TFC36	(TF0, TF0, TF0, TF2, TF1)
UL_TFC37	(TF1, TF0, TF0, TF2, TF1)
UL_TFC38	(TF2, TF1, TF0, TF2, TF1)
UL_TFC39	(TF3, TF2, TF0, TF2, TF1)
UL_TFC40	(TF4, TF3, TF0, TF2, TF1)
UL_TFC41	(TF5, TF4, TF1, TF2, TF1)
UL_TFC42	(TF0, TF0, TF0, TF4, TF1)
UL_TFC43	(TF1, TF0, TF0, TF4, TF1)
UL_TFC44	(TF2, TF1, TF0, TF4, TF1)
UL_TFC45	(TF3, TF2, TF0, TF4, TF1)
UL_TFC46	(TF4, TF3, TF0, TF4, TF1)
UL_TFC47	(TF5, TF4, TF1, TF4, TF1)

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (128 kbps, 20 ms TTI)	DCCH
TFS	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x53	1x60	1x336	1x148
	TF2, bits	1x42	1x63	N/A	2x336	N/A
	TF3, bits	1x55	1x84	N/A	4x336	N/A
	TF4, bits	1x75	1x103	N/A	8x336	N/A
	TF5, bits	1x81	N/A	N/A	N/A	N/A

[Downlink TFCS:](#)

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0,TF0,TF0,TF0,TF0)
DL_TFC1	(TF1,TF0,TF0,TF0,TF0)
DL_TFC2	(TF2,TF1,TF0,TF0,TF0)
DL_TFC3	(TF3,TF2,TF0,TF0,TF0)
DL_TFC4	(TF4,TF3,TF0,TF0,TF0)
DL_TFC5	(TF5,TF4,TF1,TF0,TF0)
DL_TFC6	(TF0,TF0,TF0,TF1,TF0)
DL_TFC7	(TF1,TF0,TF0,TF1,TF0)
DL_TFC8	(TF2,TF1,TF0,TF1,TF0)
DL_TFC9	(TF3,TF2,TF0,TF1,TF0)
DL_TFC10	(TF4,TF3,TF0,TF1,TF0)
DL_TFC11	(TF5,TF4,TF1,TF1,TF0)
DL_TFC12	(TF0,TF0,TF0,TF2,TF0)
DL_TFC13	(TF1,TF0,TF0,TF2,TF0)
DL_TFC14	(TF2,TF1,TF0,TF2,TF0)
DL_TFC15	(TF3,TF2,TF0,TF2,TF0)
DL_TFC16	(TF4,TF3,TF0,TF2,TF0)
DL_TFC17	(TF5,TF4,TF1,TF2,TF0)
DL_TFC18	(TF0,TF0,TF0,TF3,TF0)
DL_TFC19	(TF1,TF0,TF0,TF3,TF0)
DL_TFC20	(TF2,TF1,TF0,TF3,TF0)
DL_TFC21	(TF3,TF2,TF0,TF3,TF0)
DL_TFC22	(TF4,TF3,TF0,TF3,TF0)
DL_TFC23	(TF5,TF4,TF1,TF3,TF0)
DL_TFC24	(TF0,TF0,TF0,TF4,TF0)
DL_TFC25	(TF1,TF0,TF0,TF4,TF0)
DL_TFC26	(TF2,TF1,TF0,TF4,TF0)
DL_TFC27	(TF3,TF2,TF0,TF4,TF0)
DL_TFC28	(TF4,TF3,TF0,TF4,TF0)
DL_TFC29	(TF5,TF4,TF1,TF4,TF0)
DL_TFC30	(TF0,TF0,TF0,TF0,TF1)
DL_TFC31	(TF1,TF0,TF0,TF0,TF1)
DL_TFC32	(TF2,TF1,TF0,TF0,TF1)
DL_TFC33	(TF3,TF2,TF0,TF0,TF1)
DL_TFC34	(TF4,TF3,TF0,TF0,TF1)
DL_TFC35	(TF5,TF4,TF1,TF0,TF1)
DL_TFC36	(TF0,TF0,TF0,TF1,TF1)
DL_TFC37	(TF1,TF0,TF0,TF1,TF1)
DL_TFC38	(TF2,TF1,TF0,TF1,TF1)
DL_TFC39	(TF3,TF2,TF0,TF1,TF1)
DL_TFC40	(TF4,TF3,TF0,TF1,TF1)
DL_TFC41	(TF5,TF4,TF1,TF1,TF1)
DL_TFC42	(TF0,TF0,TF0,TF2,TF1)
DL_TFC43	(TF1,TF0,TF0,TF2,TF1)
DL_TFC44	(TF2,TF1,TF0,TF2,TF1)
DL_TFC45	(TF3,TF2,TF0,TF2,TF1)
DL_TFC46	(TF4,TF3,TF0,TF2,TF1)
DL_TFC47	(TF5,TF4,TF1,TF2,TF1)
DL_TFC48	(TF0,TF0,TF0,TF3,TF1)
DL_TFC49	(TF1,TF0,TF0,TF3,TF1)
DL_TFC50	(TF2,TF1,TF0,TF3,TF1)
DL_TFC51	(TF3,TF2,TF0,TF3,TF1)
DL_TFC52	(TF4,TF3,TF0,TF3,TF1)
DL_TFC53	(TF5,TF4,TF1,TF3,TF1)
DL_TFC54	(TF0,TF0,TF0,TF4,TF1)
DL_TFC55	(TF1,TF0,TF0,TF4,TF1)
DL_TFC56	(TF2,TF1,TF0,TF4,TF1)
DL_TFC57	(TF3,TF2,TF0,TF4,TF1)
DL_TFC58	(TF4,TF3,TF0,TF4,TF1)
DL_TFC59	(TF5,TF4,TF1,TF4,TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> <u>(note 1)</u>	<u>UL RLC SDU size</u> <u>(note 2)</u>	<u>Test data size</u> <u>(note 2)</u>
<u>1</u>	<u>DL TFC1,</u> <u>DL TFC31</u>	<u>UL TFC1,</u> <u>UL TFC25</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC25</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>2</u>	<u>DL TFC2,</u> <u>DL TFC32</u>	<u>UL TFC2,</u> <u>UL TFC26</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC26</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>3</u>	<u>DL TFC3,</u> <u>DL TFC33</u>	<u>UL TFC3,</u> <u>UL TFC27</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC27</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>4</u>	<u>DL TFC4,</u> <u>DL TFC34</u>	<u>UL TFC4,</u> <u>UL TFC28</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC28</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>5</u>	<u>DL TFC5,</u> <u>DL TFC35</u>	<u>UL TFC5,</u> <u>UL TFC29</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC29</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: No data</u>
<u>6</u>	<u>DL TFC6,</u> <u>DL TFC36</u>	<u>UL TFC6,</u> <u>UL TFC30</u>	<u>DL TFC0, DL TFC30,</u> <u>UL TFC0, UL TFC24</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC24,</u> <u>UL TFC30</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>

<u>7</u>	<u>DL_TFC7,</u> <u>DL_TFC37</u>	<u>UL_TFC7,</u> <u>UL_TFC31</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC24,</u> <u>UL_TFC25,</u> <u>UL_TFC30,</u> <u>UL_TFC31</u>	<u>RB5: 39 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 39 bits</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>8</u>	<u>DL_TFC8,</u> <u>DL_TFC38</u>	<u>UL_TFC8,</u> <u>UL_TFC32</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC24,</u> <u>UL_TFC26,</u> <u>UL_TFC30,</u> <u>UL_TFC32</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 42 bits</u> <u>RB6: 53 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>9</u>	<u>DL_TFC9,</u> <u>DL_TFC39</u>	<u>UL_TFC9,</u> <u>UL_TFC33</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC9,</u> <u>UL_TFC24,</u> <u>UL_TFC27,</u> <u>UL_TFC30,</u> <u>UL_TFC33</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>10</u>	<u>DL_TFC10,</u> <u>DL_TFC40</u>	<u>UL_TFC10,</u> <u>UL_TFC34</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC10,</u> <u>UL_TFC24,</u> <u>UL_TFC28,</u> <u>UL_TFC30,</u> <u>UL_TFC34</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 312 bits</u>
<u>11</u>	<u>DL_TFC11,</u> <u>DL_TFC41</u>	<u>UL_TFC11,</u> <u>UL_TFC35</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC11,</u> <u>UL_TFC24,</u> <u>UL_TFC29,</u> <u>UL_TFC30,</u> <u>UL_TFC35</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 120 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 312 bits</u>

12	DL_TFC12, DL_TFC42	UL_TFC12, UL_TFC36	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC24, UL_TFC36	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 376 bits	RB5: No data RB6: No data RB7: No data RB8: 632 bits
13	DL_TFC13, DL_TFC43	UL_TFC13, UL_TFC37	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC13, UL_TFC24, UL_TFC25, UL_TFC36, UL_TFC37	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 376 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 632 bits
14	DL_TFC14, DL_TFC44	UL_TFC14, UL_TFC38	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC14, UL_TFC24, UL_TFC26, UL_TFC36, UL_TFC38	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 376 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 632 bits
15	DL_TFC15, DL_TFC45	UL_TFC15, UL_TFC39	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC15, UL_TFC24, UL_TFC27, UL_TFC36, UL_TFC39	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 376 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 632 bits
16	DL_TFC16, DL_TFC46	UL_TFC16, UL_TFC40	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC16, UL_TFC24, UL_TFC28, UL_TFC36, UL_TFC40	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 376 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 632 bits

17	DL_TFC17, DL_TFC47	UL_TFC17, UL_TFC41	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC12, UL_TFC17, UL_TFC24, UL_TFC29, UL_TFC36, UL_TFC41	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 376 bits	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 632 bits
18	DL_TFC18, DL_TFC48	UL_TFC18, UL_TFC42	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC24, UL_TFC42	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits	RB5: No data RB6: No data RB7: No data RB8: 1272 bits
19	DL_TFC19, DL_TFC49	UL_TFC19, UL_TFC43	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC19, UL_TFC24, UL_TFC25, UL_TFC42, UL_TFC43	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 1272 bits
20	DL_TFC20, DL_TFC50	UL_TFC20, UL_TFC44	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC20, UL_TFC24, UL_TFC26, UL_TFC42, UL_TFC44	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 1272 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 1272 bits
21	DL_TFC21, DL_TFC51	UL_TFC21, UL_TFC45	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC21, UL_TFC24, UL_TFC27, UL_TFC42, UL_TFC45	RB5: 55 bits RB6: 63 bits RB7: 60 bits RB8: 1272 bits	RB5: 55 bits RB6: 63 bits RB7: No data RB8: 1272 bits

22	DL_TFC22, DL_TFC52	UL_TFC22, UL_TFC46	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC22, UL_TFC24, UL_TFC28, UL_TFC42, UL_TFC46	RB5: 75 bits RB6: 84 bits RB7: 60 bits RB8: 1272 bits	RB5: 75 bits RB6: 84 bits RB7: No data RB8: 1272 bits
23	DL_TFC23, DL_TFC53	UL_TFC23, UL_TFC47	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC23, UL_TFC24, UL_TFC29, UL_TFC42, UL_TFC47	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits
24	DL_TFC24, DL_TFC54	UL_TFC18, UL_TFC42	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC24, UL_TFC42	RB5: 81 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits	RB5: No data RB6: No data RB7: No data RB8: 2552 bits
25	DL_TFC25, DL_TFC55	UL_TFC19, UL_TFC43	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC19, UL_TFC24, UL_TFC25, UL_TFC42, UL_TFC43	RB5: 39 bits RB6: 103 bits RB7: 60 bits RB8: 1272 bits	RB5: 39 bits RB6: No data RB7: No data RB8: 2552 bits
26	DL_TFC26, DL_TFC56	UL_TFC20, UL_TFC44	DL_TFC0, DL_TFC30, UL_TFC0, UL_TFC24	UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6, UL_TFC18, UL_TFC20, UL_TFC24, UL_TFC26, UL_TFC42, UL_TFC44	RB5: 42 bits RB6: 53 bits RB7: 60 bits RB8: 1272 bits	RB5: 42 bits RB6: 53 bits RB7: No data RB8: 2552 bits

<u>27</u>	<u>DL_TFC27,</u> <u>DL_TFC57</u>	<u>UL_TFC21,</u> <u>UL_TFC45</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC21,</u> <u>UL_TFC24,</u> <u>UL_TFC27,</u> <u>UL_TFC42,</u> <u>UL_TFC45</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 55 bits</u> <u>RB6: 63 bits</u> <u>RB7: No data</u> <u>RB8: 2552 bits</u>
<u>28</u>	<u>DL_TFC28,</u> <u>DL_TFC58</u>	<u>UL_TFC22,</u> <u>UL_TFC46</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC22,</u> <u>UL_TFC24,</u> <u>UL_TFC28,</u> <u>UL_TFC42,</u> <u>UL_TFC46</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 75 bits</u> <u>RB6: 84 bits</u> <u>RB7: No data</u> <u>RB8: 2552 bits</u>
<u>29</u>	<u>DL_TFC29,</u> <u>DL_TFC59</u>	<u>UL_TFC23,</u> <u>UL_TFC47</u>	<u>DL_TFC0, DL_TFC30,</u> <u>UL_TFC0, UL_TFC24</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC18,</u> <u>UL_TFC23,</u> <u>UL_TFC24,</u> <u>UL_TFC29,</u> <u>UL_TFC42,</u> <u>UL_TFC47</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 1272 bits</u>	<u>RB5: 81 bits</u> <u>RB6: 103 bits</u> <u>RB7: 60 bits</u> <u>RB8: 2552 bits</u>
<p><u>NOTE 1: UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3, UL_TFC4, UL_TFC5, UL_TFC6 and UL_TFC24 are part of minimum set of TFCIs.</u></p> <p><u>NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size parameter has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.38j.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2,3 and 4: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; and no data shall be received on RB7 and RB8.
 - for sub-test 5: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.

- for sub-test 6: RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 7: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
- for sub-test 8,9 and 10: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB7.
- for sub-test 11: RLC SDUs on RB5, RB6 , and RB7 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink.
- for sub-test 12: RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 14,15 and 16: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS; and no data shall be received on RB7.
- for sub-test 17: RLC SDUs on RB5, RB6 , and RB7 having the same content as sent by the SS; and RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS.
- for sub-test 18: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 19: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 20,21 and 22: RLC SDUs on RB5, RB6 and RB8 having the same content as sent by the SS; and no data shall be received on RB7.
- for sub-test 23: RLC SDUs on RB5, RB6 , RB7 and RB8 having the same content as sent by the SS.
- for sub-test 24: RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 25: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
- for sub-test 26,27 and 28: RLC SDUs on RB5 and RB6 having the same content as sent by the SS; RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB7.
- for sub-test 29: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.39 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:32 DL:64 kbps / PS RAB+ UL:3.4 DL: 3.4 kbps SRBs for DCCH

18.2.2.39.1 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:32 DL:64 kbps / PS RAB / Payload 320

18.2.2.39.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.39.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.39 for the uplink payload 320 case.

18.2.2.39.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(32 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>UL_TFC9</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC10</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC11</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>UL_TFC12</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC13</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC14</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>UL_TFC15</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC17</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(64 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>

Downlink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0, TF0, TF0, TF0, TF0)
DL_TFC1	(TF1, TF0, TF0, TF0, TF0)
DL_TFC2	(TF2, TF1, TF1, TF0, TF0)
DL_TFC3	(TF0, TF0, TF0, TF1, TF0)
DL_TFC4	(TF1, TF0, TF0, TF1, TF0)
DL_TFC5	(TF2, TF1, TF1, TF1, TF0)
DL_TFC6	(TF0, TF0, TF0, TF2, TF0)
DL_TFC7	(TF1, TF0, TF0, TF2, TF0)
DL_TFC8	(TF2, TF1, TF1, TF2, TF0)
DL_TFC9	(TF0, TF0, TF0, TF3, TF0)
DL_TFC10	(TF1, TF0, TF0, TF3, TF0)
DL_TFC11	(TF2, TF1, TF1, TF3, TF0)
DL_TFC12	(TF0, TF0, TF0, TF4, TF0)
DL_TFC13	(TF1, TF0, TF0, TF4, TF0)
DL_TFC14	(TF2, TF1, TF1, TF4, TF0)
DL_TFC15	(TF0, TF0, TF0, TF0, TF1)
DL_TFC16	(TF1, TF0, TF0, TF0, TF1)
DL_TFC17	(TF2, TF1, TF1, TF0, TF1)
DL_TFC18	(TF0, TF0, TF0, TF1, TF1)
DL_TFC19	(TF1, TF0, TF0, TF1, TF1)
DL_TFC20	(TF2, TF1, TF1, TF1, TF1)
DL_TFC21	(TF0, TF0, TF0, TF2, TF1)
DL_TFC22	(TF1, TF0, TF0, TF2, TF1)
DL_TFC23	(TF2, TF1, TF1, TF2, TF1)
DL_TFC24	(TF0, TF0, TF0, TF3, TF1)
DL_TFC25	(TF1, TF0, TF0, TF3, TF1)
DL_TFC26	(TF2, TF1, TF1, TF3, TF1)
DL_TFC27	(TF0, TF0, TF0, TF4, TF1)
DL_TFC28	(TF1, TF0, TF0, TF4, TF1)
DL_TFC29	(TF2, TF1, TF1, TF4, TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	DL_TFC1 , DL_TFC16	UL_TFC1 , UL_TFC10	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC9 , UL_TFC10	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL_TFC2 , DL_TFC17	UL_TFC2 , UL_TFC11	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC2 , UL_TFC9 , UL_TFC11	RB5: 81 RB6: 103 RB7: 60 RB8: 312	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL_TFC3 , DL_TFC18	UL_TFC3 , UL_TFC12	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC3 , UL_TFC9 , UL_TFC12	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL_TFC4 , DL_TFC19	UL_TFC4 , UL_TFC13	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC4 , UL_TFC9 , UL_TFC10 , UL_TFC12 , UL_TFC13	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL_TFC5 , DL_TFC20	UL_TFC5 , UL_TFC14	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC2 , UL_TFC3 , UL_TFC5 , UL_TFC9 , UL_TFC11 , UL_TFC12 , UL_TFC14	RB5: 81 RB6: 103 RB7: 60 RB8: 312	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL_TFC6 , DL_TFC21	UL_TFC6 , UL_TFC15	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC6 , UL_TFC9 , UL_TFC15	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: No data RB6: No data RB7: No data RB8: 632
7	DL_TFC7 , DL_TFC22	UL_TFC7 , UL_TFC16	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC6 , UL_TFC7 , UL_TFC9 , UL_TFC10 , UL_TFC15 , UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: 39 RB6: No data RB7: No data RB8: 632
8	DL_TFC8 , DL_TFC23	UL_TFC8 , UL_TFC17	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC2 , UL_TFC6 , UL_TFC8 , UL_TFC9 , UL_TFC11 , UL_TFC15 , UL_TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 632	RB5: 81 RB6: 103 RB7: 60 RB8: 632
9	DL_TFC9 , DL_TFC24	UL_TFC6 , UL_TFC15	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC6 , UL_TFC9 , UL_TFC15	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: No data RB6: No data RB7: No data RB8: 952
10	DL_TFC10 , DL_TFC25	UL_TFC7 , UL_TFC16	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC6 , UL_TFC7 , UL_TFC9 , UL_TFC10 , UL_TFC15 , UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: 39 RB6: No data RB7: No data RB8: 952

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
11	DL_TFC11 , DL_TFC26	UL_TFC8 , UL_TFC17	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC2 , UL_TFC6 , UL_TFC8 , UL_TFC9 , UL_TFC11 , UL_TFC15 , UL_TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 632	RB5: 81 RB6: 103 RB7: 60 RB8: 952
12	DL_TFC12 , DL_TFC27	UL_TFC6 , UL_TFC15	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC6 , UL_TFC9 , UL_TFC15	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: No data RB6: No data RB7: No data RB8: 1272
13	DL_TFC13 , DL_TFC28	UL_TFC7 , UL_TFC16	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC6 , UL_TFC7 , UL_TFC9 , UL_TFC10 , UL_TFC15 , UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: 39 RB6: No data RB7: No data RB8: 1272
14	DL_TFC14 , DL_TFC29	UL_TFC8 , UL_TFC17	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC2 , UL_TFC6 , UL_TFC8 , UL_TFC9 , UL_TFC11 , UL_TFC15 , UL_TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 632	RB5: 81 RB6: 103 RB7: 60 RB8: 1272
<p>NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB8: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB8 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit).</p>						

18.2.2.39.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. [At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.](#)
2. [At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.](#)
3. [At step 15a and step 15b the UE shall return](#)
 - [for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.](#)
 - [for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.](#)
 - [for sub-test 3: an RLC SDU on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6 and RB7.](#)
 - [for sub-test 4: an RLC SDU on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6 and RB7.](#)
 - [for sub-test 5: an RLC SDU on RB5, RB6, RB7 and RB8 having the same content as sent by SS.](#)
 - [for sub-test 6: an RLC SDU on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6 and RB7.](#)

- for sub-test 7: an RLC SDU on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 8: an RLC SDU on RB5, RB6, RB7 and RB8 having the same content as sent by SS.
- for sub-test 9: an RLC SDU on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 10: an RLC SDU on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 11: an RLC SDU on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 12: an RLC SDU on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: an RLC SDU on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 14: an RLC SDU on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.39.2 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:32 DL:64 kbps / PS RAB / Payload 128

18.2.2.39.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.39.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.39 for the uplink payload 128 case.

18.2.2.39.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (32 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>5x144</u>	<u>N/A</u>

Uplink TFCs:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>UL_TFC9</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC10</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC11</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>UL_TFC12</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC13</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC14</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>UL_TFC15</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC17</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(64 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>

Downlink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0, TF0, TF0, TF0, TF0)
DL_TFC1	(TF1, TF0, TF0, TF0, TF0)
DL_TFC2	(TF2, TF1, TF1, TF0, TF0)
DL_TFC3	(TF0, TF0, TF0, TF1, TF0)
DL_TFC4	(TF1, TF0, TF0, TF1, TF0)
DL_TFC5	(TF2, TF1, TF1, TF1, TF0)
DL_TFC6	(TF0, TF0, TF0, TF2, TF0)
DL_TFC7	(TF1, TF0, TF0, TF2, TF0)
DL_TFC8	(TF2, TF1, TF1, TF2, TF0)
DL_TFC9	(TF0, TF0, TF0, TF3, TF0)
DL_TFC10	(TF1, TF0, TF0, TF3, TF0)
DL_TFC11	(TF2, TF1, TF1, TF3, TF0)
DL_TFC12	(TF0, TF0, TF0, TF4, TF0)
DL_TFC13	(TF1, TF0, TF0, TF4, TF0)
DL_TFC14	(TF2, TF1, TF1, TF4, TF0)
DL_TFC15	(TF0, TF0, TF0, TF0, TF1)
DL_TFC16	(TF1, TF0, TF0, TF0, TF1)
DL_TFC17	(TF2, TF1, TF1, TF0, TF1)
DL_TFC18	(TF0, TF0, TF0, TF1, TF1)
DL_TFC19	(TF1, TF0, TF0, TF1, TF1)
DL_TFC20	(TF2, TF1, TF1, TF1, TF1)
DL_TFC21	(TF0, TF0, TF0, TF2, TF1)
DL_TFC22	(TF1, TF0, TF0, TF2, TF1)
DL_TFC23	(TF2, TF1, TF1, TF2, TF1)
DL_TFC24	(TF0, TF0, TF0, TF3, TF1)
DL_TFC25	(TF1, TF0, TF0, TF3, TF1)
DL_TFC26	(TF2, TF1, TF1, TF3, TF1)
DL_TFC27	(TF0, TF0, TF0, TF4, TF1)
DL_TFC28	(TF1, TF0, TF0, TF4, TF1)
DL_TFC29	(TF2, TF1, TF1, TF4, TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	DL_TFC1 , DL_TFC16	UL_TFC1 , UL_TFC10	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC9 , UL_TFC10	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL_TFC2 , DL_TFC17	UL_TFC2 , UL_TFC11	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC2 , UL_TFC9 , UL_TFC11	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL_TFC3 , DL_TFC18	UL_TFC3 , UL_TFC12	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC3 , UL_TFC9 , UL_TFC12	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL_TFC4 , DL_TFC19	UL_TFC4 , UL_TFC13	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC4 , UL_TFC9 , UL_TFC10 , UL_TFC12 , UL_TFC13	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL_TFC5 , DL_TFC20	UL_TFC5 , UL_TFC14	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC2 , UL_TFC3 , UL_TFC5 , UL_TFC9 , UL_TFC11 , UL_TFC12 , UL_TFC14	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL_TFC6 , DL_TFC21	UL_TFC6 , UL_TFC15	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC6 , UL_TFC9 , UL_TFC15	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: No data RB6: No data RB7: No data RB8: 632
7	DL_TFC7 , DL_TFC22	UL_TFC7 , UL_TFC16	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC6 , UL_TFC7 , UL_TFC9 , UL_TFC10 , UL_TFC15 , UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: 39 RB6: No data RB7: No data RB8: 632
8	DL_TFC8 , DL_TFC23	UL_TFC8 , UL_TFC17	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC2 , UL_TFC6 , UL_TFC8 , UL_TFC9 , UL_TFC11 , UL_TFC15 , UL_TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 632	RB5: 81 RB6: 103 RB7: 60 RB8: 632
9	DL_TFC9 , DL_TFC24	UL_TFC6 , UL_TFC15	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC6 , UL_TFC9 , UL_TFC15	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: No data RB6: No data RB7: No data RB8: 952
10	DL_TFC10 , DL_TFC25	UL_TFC7 , UL_TFC16	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC9	UL_TFC0 , UL_TFC1 , UL_TFC6 , UL_TFC7 , UL_TFC9 , UL_TFC10 , UL_TFC15 , UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: 39 RB6: No data RB7: No data RB8: 952

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
11	<u>DL_TFC11, DL_TFC26</u>	<u>UL_TFC8, UL_TFC17</u>	<u>DL_TFC0, DL_TFC15, UL_TFC0, UL_TFC9</u>	<u>UL_TFC0, UL_TFC2, UL_TFC6, UL_TFC8, UL_TFC9, UL_TFC11, UL_TFC15, UL_TFC17</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 632</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 952</u>
12	<u>DL_TFC12, DL_TFC27</u>	<u>UL_TFC6, UL_TFC15</u>	<u>DL_TFC0, DL_TFC15, UL_TFC0, UL_TFC9</u>	<u>UL_TFC0, UL_TFC6, UL_TFC9, UL_TFC15</u>	<u>RB5: 39 RB6: 103 RB7: 60 RB8: 632</u>	<u>RB5: No data RB6: No data RB7: No data RB8: 1272</u>
13	<u>DL_TFC13, DL_TFC28</u>	<u>UL_TFC7, UL_TFC16</u>	<u>DL_TFC0, DL_TFC15, UL_TFC0, UL_TFC9</u>	<u>UL_TFC0, UL_TFC1, UL_TFC6, UL_TFC7, UL_TFC9, UL_TFC10, UL_TFC15, UL_TFC16</u>	<u>RB5: 39 RB6: 103 RB7: 60 RB8: 632</u>	<u>RB5: 39 RB6: No data RB7: No data RB8: 1272</u>
14	<u>DL_TFC14, DL_TFC29</u>	<u>UL_TFC8, UL_TFC17</u>	<u>DL_TFC0, DL_TFC15, UL_TFC0, UL_TFC9</u>	<u>UL_TFC0, UL_TFC2, UL_TFC6, UL_TFC8, UL_TFC9, UL_TFC11, UL_TFC15, UL_TFC17</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 632</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 1272</u>
<p>NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. <u>RB8: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB8 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.39.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
 - for sub-test 3: RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
 - for sub-test 5: RLC SDUs on RB5, RB6, and RB7 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink.

- for sub-test 6: an RLC SDU on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 7: an RLC SDU on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 8: an RLC SDU on RB5, RB6, RB7 and RB8 having the same content as sent by SS.
- for sub-test 9: an RLC SDU on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 10: an RLC SDU on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 11: an RLC SDU on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 12: an RLC SDU on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: an RLC SDU on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 14: an RLC SDU on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.40 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB+ UL:3.4 DL: 3.4 kbps SRBs for DCCH

18.2.2.40.1 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB+ UL:3.4 DL: 3.4 kbps SRBs for DCCH / Payload 320, 1 CCTrCH

18.2.2.40.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.40.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.40 for uplink payload 320 and 1 CCTrCH configuration case.

18.2.2.40.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (64 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>UL_TFC9</u>	<u>(TF0, TF0, TF0, TF3, TF0)</u>
<u>UL_TFC10</u>	<u>(TF1, TF0, TF0, TF3, TF0)</u>
<u>UL_TFC11</u>	<u>(TF2, TF1, TF1, TF3, TF0)</u>
<u>UL_TFC12</u>	<u>(TF0, TF0, TF0, TF4, TF0)</u>
<u>UL_TFC13</u>	<u>(TF1, TF0, TF0, TF4, TF0)</u>
<u>UL_TFC14</u>	<u>(TF2, TF1, TF1, TF4, TF0)</u>
<u>UL_TFC15</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC17</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>UL_TFC18</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC19</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC20</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>UL_TFC21</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC22</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC23</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>
<u>UL_TFC24</u>	<u>(TF0, TF0, TF0, TF3, TF1)</u>
<u>UL_TFC25</u>	<u>(TF1, TF0, TF0, TF3, TF1)</u>
<u>UL_TFC26</u>	<u>(TF2, TF1, TF1, TF3, TF1)</u>
<u>UL_TFC27</u>	<u>(TF0, TF0, TF0, TF4, TF1)</u>
<u>UL_TFC28</u>	<u>(TF1, TF0, TF0, TF4, TF1)</u>
<u>UL_TFC29</u>	<u>(TF2, TF1, TF1, TF4, TF1)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(64 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>DL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>DL_TFC9</u>	<u>(TF0, TF0, TF0, TF3, TF0)</u>
<u>DL_TFC10</u>	<u>(TF1, TF0, TF0, TF3, TF0)</u>
<u>DL_TFC11</u>	<u>(TF2, TF1, TF1, TF3, TF0)</u>
<u>DL_TFC12</u>	<u>(TF0, TF0, TF0, TF4, TF0)</u>
<u>DL_TFC13</u>	<u>(TF1, TF0, TF0, TF4, TF0)</u>
<u>DL_TFC14</u>	<u>(TF2, TF1, TF1, TF4, TF0)</u>
<u>DL_TFC15</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC17</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>DL_TFC18</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC20</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>DL_TFC21</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC22</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC23</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>
<u>DL_TFC24</u>	<u>(TF0, TF0, TF0, TF3, TF1)</u>
<u>DL_TFC25</u>	<u>(TF1, TF0, TF0, TF3, TF1)</u>
<u>DL_TFC26</u>	<u>(TF2, TF1, TF1, TF3, TF1)</u>
<u>DL_TFC27</u>	<u>(TF0, TF0, TF0, TF4, TF1)</u>
<u>DL_TFC28</u>	<u>(TF1, TF0, TF0, TF4, TF1)</u>
<u>DL_TFC29</u>	<u>(TF2, TF1, TF1, TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
1	DL_TFC1 , DL_TFC16	UL_TFC1 , UL_TFC16	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC15 , UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL_TFC2 , DL_TFC17	UL_TFC2 , UL_TFC17	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC15 , UL_TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 312	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL_TFC3 , DL_TFC18	UL_TFC3 , UL_TFC18	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC15 , UL_TFC18	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL_TFC4 , DL_TFC19	UL_TFC4 , UL_TFC19	DL_TFC0 , DL_TFC15 , DUL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC4 , UL_TFC15 , UL_TFC16 , UL_TFC18 , UL_TFC19	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL_TFC5 , DL_TFC20	UL_TFC5 , UL_TFC20	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC5 , UL_TFC15 , UL_TFC17 , UL_TFC18 , UL_TFC20	RB5: 81 RB6: 103 RB7: 60 RB8: 312	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL_TFC6 , DL_TFC21	UL_TFC6 , UL_TFC21	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC15 , UL_TFC21	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: No data RB6: No data RB7: No data RB8: 632
7	DL_TFC7 , DL_TFC22	UL_TFC7 , UL_TFC22	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC7 , UL_TFC15 , UL_TFC16 , UL_TFC21 , UL_TFC22	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: 39 RB6: No data RB7: No data RB8: 632

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
8	DL_TFC8 , DL_TFC23	UL_TFC8 , UL_TFC23	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC8 , UL_TFC15 , UL_TFC17 , UL_TFC21 , UL_TFC23	RB5: 81 RB6: 103 RB7: 60 RB8: 632	RB5: 81 RB6: 103 RB7: 60 RB8: 632
9	DL_TFC9 , DL_TFC24	UL_TFC9 , UL_TFC24	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC15 , UL_TFC24	RB5: 39 RB6: 103 RB7: 60 RB8: 952	RB5: No data RB6: No data RB7: No data RB8: 952
10	DL_TFC10 , DL_TFC25	UL_TFC10 , UL_TFC25	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC10 , UL_TFC15 , UL_TFC16 , UL_TFC24 , UL_TFC25	RB5: 39 RB6: 103 RB7: 60 RB8: 952	RB5: 39 RB6: No data RB7: No data RB8: 952
11	DL_TFC11 , DL_TFC26	UL_TFC11 , UL_TFC26	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC2 , UL_TFC9 , UL_TFC11 , UL_TFC15 , UL_TFC17 , UL_TFC24 , UL_TFC26	RB5: 81 RB6: 103 RB7: 60 RB8: 952	RB5: 81 RB6: 103 RB7: 60 RB8: 952
12	DL_TFC12 , DL_TFC27	UL_TFC12 , UL_TFC27	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC12 , UL_TFC15 , UL_TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 1272
13	DL_TFC13 , DL_TFC28	UL_TFC13 , UL_TFC28	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC12 , UL_TFC13 , UL_TFC15 , UL_TFC16 , UL_TFC27 , UL_TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 1272
14	DL_TFC14 , DL_TFC29	UL_TFC14 , UL_TFC29	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC12 , UL_TFC14 , UL_TFC15 , UL_TFC17 , UL_TFC27 , UL_TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 1272

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
NOTE 1: UL TFC0, UL TFC1, UL TFC2, UL TFC3 and UL TFC15 are part of minimum set of TFCIs.						
NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB8 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the the payload size of the UL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit).						

18.2.2.40.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: RLC SDUs on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
 - for sub-test 3, 6, 9 and 12: RLC SDUs on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4, 7, 10 and 13: RLC SDUs on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 5, 8, 11 and 14: RLC SDUs on RB5, RB6, RB7 and RB8 having the same content as sent by SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.40.2 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB+ UL:3.4 DL: 3.4 kbps SRBs for DCCH / Payload 128, 2 CCTrCHs

18.2.2.40.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.40.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.40 for uplink payload 128 and 2 CCTrCHs configuration case.

18.2.2.40.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (64 kbps)</u>	<u>DCCH</u>
TFS	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>3x144</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>7x144</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>10x144</u>	<u>N/A</u>

Uplink TFCS (conversational + SRB):

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_Conv_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_Conv_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_Conv_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_Conv_TFC3</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_Conv_TFC4</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_Conv_TFC5</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>

Uplink TFCS (Interactive or background):

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_Inter_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_Inter_TFC1</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_Inter_TFC2</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL_Inter_TFC3</u>	<u>(TF0, TF0, TF0, TF3, TF0)</u>
<u>UL_Inter_TFC4</u>	<u>(TF0, TF0, TF0, TF4, TF0)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(64 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>DL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>DL_TFC9</u>	<u>(TF0, TF0, TF0, TF3, TF0)</u>
<u>DL_TFC10</u>	<u>(TF1, TF0, TF0, TF3, TF0)</u>
<u>DL_TFC11</u>	<u>(TF2, TF1, TF1, TF3, TF0)</u>
<u>DL_TFC12</u>	<u>(TF0, TF0, TF0, TF4, TF0)</u>
<u>DL_TFC13</u>	<u>(TF1, TF0, TF0, TF4, TF0)</u>
<u>DL_TFC14</u>	<u>(TF2, TF1, TF1, TF4, TF0)</u>
<u>DL_TFC15</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC17</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>DL_TFC18</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC20</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>DL_TFC21</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC22</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC23</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>
<u>DL_TFC24</u>	<u>(TF0, TF0, TF0, TF3, TF1)</u>
<u>DL_TFC25</u>	<u>(TF1, TF0, TF0, TF3, TF1)</u>
<u>DL_TFC26</u>	<u>(TF2, TF1, TF1, TF3, TF1)</u>
<u>DL_TFC27</u>	<u>(TF0, TF0, TF0, TF4, TF1)</u>
<u>DL_TFC28</u>	<u>(TF1, TF0, TF0, TF4, TF1)</u>
<u>DL_TFC29</u>	<u>(TF2, TF1, TF1, TF4, TF1)</u>

Sub-tests:

Sub-test	Downlink TFCs Under Test	Uplink TFCs Under test	Implicitely tested	Restricted UL TFCs (note 1)	UL RLC SDU size (bits) (note 2)	Test data size (bits) (note 2)
1	DL TFC1 , DL TFC16	UL Conv TFC1 , UL Conv TFC4 , UL Inter TFC0	DL TFC0 , DL TFC15 , UL Conv TFC0 , UL Conv TFC3	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC3 , UL Conv TFC4 , UL Inter TFC0 , UL Inter TFC1	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL TFC2 , DL TFC17	UL Conv TFC2 , UL Conv TFC5 , UL Inter TFC0	DL TFC0 , DL TFC15 , UL Conv TFC0 , UL Conv TFC3	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL TFC3 , DL TFC18	UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC1	DL TFC0 , DL TFC15 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC3 , UL Conv TFC4 , UL Inter TFC0 , UL Inter TFC1	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL TFC4 , DL TFC19	UL Conv TFC1 , UL Conv TFC4 , UL Inter TFC1	DL TFC0 , DL TFC15 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC3 , UL Conv TFC4 , UL Inter TFC0 , UL Inter TFC1	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL TFC5 , DL TFC20	UL Conv TFC2 , UL Conv TFC5 , UL Inter TFC1	DL TFC0 , DL TFC15 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL TFC6 , DL TFC21	UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC2	DL TFC0 , DL TFC15 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC3 , UL Conv TFC4 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC2	RB5: 39 RB6: 103 RB7: 60 RB8: 376	RB5: No data RB6: No data RB7: No data RB8: 632
7	DL TFC7 , DL TFC22	UL Conv TFC1 , UL Conv TFC4 , UL Inter TFC2	DL TFC0 , DL TFC15 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC3 , UL Conv TFC4 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC2	RB5: 39 RB6: 103 RB7: 60 RB8: 376	RB5: 39 RB6: No data RB7: No data RB8: 632
8	DL TFC8 , DL TFC23	UL Conv TFC2 , UL Conv TFC5 , UL Inter TFC2	DL TFC0 , DL TFC15 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC2	RB5: 81 RB6: 103 RB7: 60 RB8: 376	RB5: 81 RB6: 103 RB7: 60 RB8: 632

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
9	DL TFC9 , DL TFC24	UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC3	DL TFC0 , DL TFC15 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC3 , UL Conv TFC4 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC3	RB5: 39 RB6: 103 RB7: 60 RB8: 888	RB5: No data RB6: No data RB7: No data RB8: 952
10	DL TFC10 , DL TFC25	UL Conv TFC1 , UL Conv TFC4 , UL Inter TFC3	DL TFC0 , DL TFC15 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC3 , UL Conv TFC4 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC3	RB5: 39 RB6: 103 RB7: 60 RB8: 888	RB5: 39 RB6: No data RB7: No data RB8: 952
11	DL TFC11 , DL TFC26	UL Conv TFC2 , UL Conv TFC5 , UL Inter TFC3	DL TFC0 , DL TFC15 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC3	RB5: 81 RB6: 103 RB7: 60 RB8: 888	RB5: 81 RB6: 103 RB7: 60 RB8: 952
12	DL TFC12 , DL TFC27	UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC4	DL TFC0 , DL TFC15 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC3 , UL Conv TFC4 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 1272
13	DL TFC13 , DL TFC28	UL Conv TFC1 , UL Conv TFC4 , UL Inter TFC4	DL TFC0 , DL TFC15 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC3 , UL Conv TFC4 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 1272
14	DL TFC14 , DL TFC29	UL Conv TFC2 , UL Conv TFC5 , UL Inter TFC4	DL TFC0 , DL TFC15 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 1272
<p>NOTE 1: UL Conv TFC0, UL Conv TFC1, UL Conv TFC3, UL Conv TFC4, UL Inter TFC0, and UL Inter TFC1 are part of minimum set of TFCs.</p> <p>NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB8 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the the payload size of the UL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit).</p>						

18.2.2.40.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCs as specified for the actual sub-test.

3. At step 15a and step 15b the UE shall return

- for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
- for sub-test 2: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.
- for sub-test 3: RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 4: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
- for sub-test 5: RLC SDUs on RB5, RB6 , and RB7 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink.
- for sub-test 6: RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 7: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 8: RLC SDUs on RB5, RB6 , and RB7 having the same content as sent by the SS; and RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS.
- for sub-test 9: RLC SDUs on RB8 having the same content as the first 888 bits of the test data sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 10: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 888 bits of the test data sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 11: RLC SDUs on RB5, RB6 , and RB7 having the same content as sent by the SS; and RLC SDUs on RB8 having the same content as the first 888 bits of the test data sent by the SS.
- for sub-test 12: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 14: RLC SDUs on RB5, RB6 , RB7 and RB8 having the same content as sent by the SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.41 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH

18.2.2.41.1 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320, 1 CCTrCH

18.2.2.41.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.41.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108. clause 6.10.3.4.1.41 for uplink payload 320 and 1 CCTrCH configuration case.

18.2.2.41.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	TFI	RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (64 kbps, 20 ms TTI)	DCCH
TFS	TF0, bits	0x81	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x103	1x60	1x336	1x148
	TF2, bits	1x81	N/A	N/A	2x336	N/A
	TF3, bits	N/A	N/A	N/A	3x336	N/A
	TF4, bits	N/A	N/A	N/A	4x336	N/A

Uplink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF1, TF0, TF0)
UL_TFC3	(TF0, TF0, TF0, TF1, TF0)
UL_TFC4	(TF1, TF0, TF0, TF1, TF0)
UL_TFC5	(TF2, TF1, TF1, TF1, TF0)
UL_TFC6	(TF0, TF0, TF0, TF2, TF0)
UL_TFC7	(TF1, TF0, TF0, TF2, TF0)
UL_TFC8	(TF2, TF1, TF1, TF2, TF0)
UL_TFC9	(TF0, TF0, TF0, TF3, TF0)
UL_TFC10	(TF1, TF0, TF0, TF3, TF0)
UL_TFC11	(TF2, TF1, TF1, TF3, TF0)
UL_TFC12	(TF0, TF0, TF0, TF4, TF0)
UL_TFC13	(TF1, TF0, TF0, TF4, TF0)
UL_TFC14	(TF2, TF1, TF1, TF4, TF0)
UL_TFC15	(TF0, TF0, TF0, TF0, TF1)
UL_TFC16	(TF1, TF0, TF0, TF0, TF1)
UL_TFC17	(TF2, TF1, TF1, TF0, TF1)
UL_TFC18	(TF0, TF0, TF0, TF1, TF1)
UL_TFC19	(TF1, TF0, TF0, TF1, TF1)
UL_TFC20	(TF2, TF1, TF1, TF1, TF1)
UL_TFC21	(TF0, TF0, TF0, TF2, TF1)
UL_TFC22	(TF1, TF0, TF0, TF2, TF1)
UL_TFC23	(TF2, TF1, TF1, TF2, TF1)
UL_TFC24	(TF0, TF0, TF0, TF3, TF1)
UL_TFC25	(TF1, TF0, TF0, TF3, TF1)
UL_TFC26	(TF2, TF1, TF1, TF3, TF1)
UL_TFC27	(TF0, TF0, TF0, TF4, TF1)
UL_TFC28	(TF1, TF0, TF0, TF4, TF1)
UL_TFC29	(TF2, TF1, TF1, TF4, TF1)

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (128 kbps, 20 ms TTI)	DCCH
TFS	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x103	1x60	1x336	1x148
	TF2, bits	1x81	N/A	N/A	2x336	N/A
	TF3, bits	N/A	N/A	N/A	4x336	N/A
	TF4, bits	N/A	N/A	N/A	8x336	N/A

Downlink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0, TF0, TF0, TF0, TF0)
DL_TFC1	(TF1, TF0, TF0, TF0, TF0)
DL_TFC2	(TF2, TF1, TF1, TF0, TF0)
DL_TFC3	(TF0, TF0, TF0, TF1, TF0)
DL_TFC4	(TF1, TF0, TF0, TF1, TF0)
DL_TFC5	(TF2, TF1, TF1, TF1, TF0)
DL_TFC6	(TF0, TF0, TF0, TF2, TF0)
DL_TFC7	(TF1, TF0, TF0, TF2, TF0)
DL_TFC8	(TF2, TF1, TF1, TF2, TF0)
DL_TFC9	(TF0, TF0, TF0, TF3, TF0)
DL_TFC10	(TF1, TF0, TF0, TF3, TF0)
DL_TFC11	(TF2, TF1, TF1, TF3, TF0)
DL_TFC12	(TF0, TF0, TF0, TF4, TF0)
DL_TFC13	(TF1, TF0, TF0, TF4, TF0)
DL_TFC14	(TF2, TF1, TF1, TF4, TF0)
DL_TFC15	(TF0, TF0, TF0, TF0, TF1)
DL_TFC16	(TF1, TF0, TF0, TF0, TF1)
DL_TFC17	(TF2, TF1, TF1, TF0, TF1)
DL_TFC18	(TF0, TF0, TF0, TF1, TF1)
DL_TFC19	(TF1, TF0, TF0, TF1, TF1)
DL_TFC20	(TF2, TF1, TF1, TF1, TF1)
DL_TFC21	(TF0, TF0, TF0, TF2, TF1)
DL_TFC22	(TF1, TF0, TF0, TF2, TF1)
DL_TFC23	(TF2, TF1, TF1, TF2, TF1)
DL_TFC24	(TF0, TF0, TF0, TF3, TF1)
DL_TFC25	(TF1, TF0, TF0, TF3, TF1)
DL_TFC26	(TF2, TF1, TF1, TF3, TF1)
DL_TFC27	(TF0, TF0, TF0, TF4, TF1)
DL_TFC28	(TF1, TF0, TF0, TF4, TF1)
DL_TFC29	(TF2, TF1, TF1, TF4, TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
1	DL_TFC1 , DL_TFC16	UL_TFC1 , UL_TFC16	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC15 , UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL_TFC2 , DL_TFC17	UL_TFC2 , UL_TFC17	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC15 , UL_TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 312	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL_TFC3 , DL_TFC18	UL_TFC3 , UL_TFC18	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC15 , UL_TFC18	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL_TFC4 , DL_TFC19	UL_TFC4 , UL_TFC19	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC4 , UL_TFC15 , UL_TFC16 , UL_TFC18 , UL_TFC19	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL_TFC5 , DL_TFC20	UL_TFC5 , UL_TFC20	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC5 , UL_TFC15 , UL_TFC17 , UL_TFC18 , UL_TFC20	RB5: 81 RB6: 103 RB7: 60 RB8: 312	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL_TFC6 , DL_TFC21	UL_TFC6 , UL_TFC21	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC15 , UL_TFC21	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: No data RB6: No data RB7: No data RB8: 632
7	DL_TFC7 , DL_TFC22	UL_TFC7 , UL_TFC22	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC7 , UL_TFC15 , UL_TFC16 , UL_TFC21 , UL_TFC22	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: 39 RB6: No data RB7: No data RB8: 632

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
8	DL_TFC8 , DL_TFC23	UL_TFC8 , UL_TFC23	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC8 , UL_TFC15 , UL_TFC17 , UL_TFC21 , UL_TFC23	RB5: 81 RB6: 103 RB7: 60 RB8: 632	RB5: 81 RB6: 103 RB7: 60 RB8: 632
9	DL_TFC9 , DL_TFC24	UL_TFC9 , UL_TFC24	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC15 , UL_TFC24	RB5: 39 RB6: 103 RB7: 60 RB8: 952	RB5: No data RB6: No data RB7: No data RB8: 1272
10	DL_TFC10 , DL_TFC25	UL_TFC10 , UL_TFC25	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC10 , UL_TFC15 , UL_TFC16 , UL_TFC24 , UL_TFC25	RB5: 39 RB6: 103 RB7: 60 RB8: 952	RB5: 39 RB6: No data RB7: No data RB8: 1272
11	DL_TFC11 , DL_TFC26	UL_TFC11 , UL_TFC26	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC11 , UL_TFC15 , UL_TFC17 , UL_TFC24 , UL_TFC26	RB5: 81 RB6: 103 RB7: 60 RB8: 952	RB5: 81 RB6: 103 RB7: 60 RB8: 1272
12	DL_TFC12 , DL_TFC27	UL_TFC12 , UL_TFC27	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC12 , UL_TFC15 , UL_TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 2552
13	DL_TFC13 , DL_TFC28	UL_TFC13 , UL_TFC28	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC12 , UL_TFC13 , UL_TFC15 , UL_TFC16 , UL_TFC27 , UL_TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 2552
14	DL_TFC14 , DL_TFC29	UL_TFC14 , UL_TFC29	DL_TFC0 , DL_TFC15 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC12 , UL_TFC14 , UL_TFC15 , UL_TFC17 , UL_TFC27 , UL_TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 2552

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
NOTE 1: UL TFC0, UL TFC1, UL TFC2, UL TFC3 and UL TFC15 are part of minimum set of TFCIs.						
NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB8 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the the payload size of the UL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit).						

18.2.2.41.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: RLC SDUs on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
 - for sub-test 3: RLC SDUs on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4: RLC SDUs on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 5: RLC SDUs on RB5, RB6, RB7 and RB8 having the same content as sent by SS.
 - for sub-test 6: RLC SDUs on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 7: RLC SDUs on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 8: RLC SDUs on RB5, RB6, RB7 and RB8 having the same content as sent by SS.
 - for sub-test 9: RLC SDUs on RB8 having the content equal to the first 952 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 10: RLC SDUs on RB8 having the content equal to the first 952 bits of the test data sent by the SS in downlink; RLC SDUs on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 11: RLC SDUs on RB8 having the content equal to the first 952 bits of the test data sent by the SS in downlink; RLC SDUs on RB5, RB6 and RB7 having the same content as sent by SS.
 - for sub-test 12: RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 13: RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; RLC SDUs on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 14: RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; RLC SDUs on RB5, RB6 and RB7 having the same content as sent by SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.41.2 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128, 2 CCTrCHs

18.2.2.41.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.41.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.41 for uplink payload 128 and 2 CCTrCHs configuration case.

18.2.2.41.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(64 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>3x144</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>7x144</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>10x144</u>	<u>N/A</u>

Uplink TFCS (conversational + SRB):

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL Conv TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL Conv TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL Conv TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL Conv TFC3</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL Conv TFC4</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL Conv TFC5</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>

Uplink TFCS (Interactive or background):

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL Inter TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL Inter TFC1</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL Inter TFC2</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL Inter TFC3</u>	<u>(TF0, TF0, TF0, TF3, TF0)</u>
<u>UL Inter TFC4</u>	<u>(TF0, TF0, TF0, TF4, TF0)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(128 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>8x336</u>	<u>N/A</u>

Downlink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0, TF0, TF0, TF0, TF0)
DL_TFC1	(TF1, TF0, TF0, TF0, TF0)
DL_TFC2	(TF2, TF1, TF1, TF0, TF0)
DL_TFC3	(TF0, TF0, TF0, TF1, TF0)
DL_TFC4	(TF1, TF0, TF0, TF1, TF0)
DL_TFC5	(TF2, TF1, TF1, TF1, TF0)
DL_TFC6	(TF0, TF0, TF0, TF2, TF0)
DL_TFC7	(TF1, TF0, TF0, TF2, TF0)
DL_TFC8	(TF2, TF1, TF1, TF2, TF0)
DL_TFC9	(TF0, TF0, TF0, TF3, TF0)
DL_TFC10	(TF1, TF0, TF0, TF3, TF0)
DL_TFC11	(TF2, TF1, TF1, TF3, TF0)
DL_TFC12	(TF0, TF0, TF0, TF4, TF0)
DL_TFC13	(TF1, TF0, TF0, TF4, TF0)
DL_TFC14	(TF2, TF1, TF1, TF4, TF0)
DL_TFC15	(TF0, TF0, TF0, TF0, TF1)
DL_TFC16	(TF1, TF0, TF0, TF0, TF1)
DL_TFC17	(TF2, TF1, TF1, TF0, TF1)
DL_TFC18	(TF0, TF0, TF0, TF1, TF1)
DL_TFC19	(TF1, TF0, TF0, TF1, TF1)
DL_TFC20	(TF2, TF1, TF1, TF1, TF1)
DL_TFC21	(TF0, TF0, TF0, TF2, TF1)
DL_TFC22	(TF1, TF0, TF0, TF2, TF1)
DL_TFC23	(TF2, TF1, TF1, TF2, TF1)
DL_TFC24	(TF0, TF0, TF0, TF3, TF1)
DL_TFC25	(TF1, TF0, TF0, TF3, TF1)
DL_TFC26	(TF2, TF1, TF1, TF3, TF1)
DL_TFC27	(TF0, TF0, TF0, TF4, TF1)
DL_TFC28	(TF1, TF0, TF0, TF4, TF1)
DL_TFC29	(TF2, TF1, TF1, TF4, TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
1	DL_TFC1 , DL_TFC16	UL_Conv_TFC1 , UL_Conv_TFC4 , UL_Inter_TFC0	DL_TFC0 , DL_TFC15 , UL_Conv_TFC0 , UL_Conv_TFC3	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Inter_TFC0 , UL_Inter_TFC1	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL_TFC2 , DL_TFC17	UL_Conv_TFC2 , UL_Conv_TFC5 , UL_Inter_TFC0	DL_TFC0 , DL_TFC15 , UL_Conv_TFC0 , UL_Conv_TFC3	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC2 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Conv_TFC5 , UL_Inter_TFC0 , UL_Inter_TFC1	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL_TFC3 , DL_TFC18	UL_Conv_TFC0 , UL_Conv_TFC3 , UL_Inter_TFC1	DL_TFC0 , DL_TFC15 , UL_Inter_TFC0	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Inter_TFC0 , UL_Inter_TFC1	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL_TFC4 , DL_TFC19	UL_Conv_TFC1 , UL_Conv_TFC4 , UL_Inter_TFC1	DL_TFC0 , DL_TFC15 , UL_Conv_TFC0 , UL_Conv_TFC3 , UL_Inter_TFC0	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Inter_TFC0 , UL_Inter_TFC1	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL_TFC5 , DL_TFC20	UL_Conv_TFC2 , UL_Conv_TFC5 , UL_Inter_TFC1	DL_TFC0 , DL_TFC15 , UL_Conv_TFC0 , UL_Conv_TFC3 , UL_Inter_TFC0	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC2 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Conv_TFC5 , UL_Inter_TFC0 , UL_Inter_TFC1	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL_TFC6 , DL_TFC21	UL_Conv_TFC0 , UL_Conv_TFC3 , UL_Inter_TFC2	DL_TFC0 , DL_TFC15 , UL_Inter_TFC0	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Inter_TFC0 , UL_Inter_TFC1 , UL_Inter_TFC2	RB5: 39 RB6: 103 RB7: 60 RB8: 376	RB5: No data RB6: No data RB7: No data RB8: 632
7	DL_TFC7 , DL_TFC22	UL_Conv_TFC1 , UL_Conv_TFC4 , UL_Inter_TFC2	DL_TFC0 , DL_TFC15 , UL_Conv_TFC0 , UL_Conv_TFC3 , UL_Inter_TFC0	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Inter_TFC0 , UL_Inter_TFC1 , UL_Inter_TFC2	RB5: 39 RB6: 103 RB7: 60 RB8: 376	RB5: 39 RB6: No data RB7: No data RB8: 632
8	DL_TFC8 , DL_TFC23	UL_Conv_TFC2 , UL_Conv_TFC5 , UL_Inter_TFC2	DL_TFC0 , DL_TFC15 , UL_Conv_TFC0 , UL_Conv_TFC3 , UL_Inter_TFC0	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC2 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Conv_TFC5 , UL_Inter_TFC0 , UL_Inter_TFC1 , UL_Inter_TFC2	RB5: 81 RB6: 103 RB7: 60 RB8: 376	RB5: 81 RB6: 103 RB7: 60 RB8: 632

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
9	DL_TFC9 , DL_TFC24	UL_Conv_TFC0 , UL_Conv_TFC3 , UL_Inter_TFC3	DL_TFC0 , DL_TFC15 , UL_Inter_TFC0	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Inter_TFC0 , UL_Inter_TFC1 , UL_Inter_TFC3	RB5: 39 RB6: 103 RB7: 60 RB8: 888	RB5: No data RB6: No data RB7: No data RB8: 1272
10	DL_TFC10 ⁺ DL_TFC25	UL_Conv_TFC1 , UL_Conv_TFC4 , UL_Inter_TFC3	DL_TFC0 , DL_TFC15 , UL_Conv_TFC0 , UL_Conv_TFC3 , UL_Inter_TFC0	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Inter_TFC0 , UL_Inter_TFC1 , UL_Inter_TFC3	RB5: 39 RB6: 103 RB7: 60 RB8: 888	RB5: 39 RB6: No data RB7: No data RB8: 1272
11	DL_TFC11 ⁺ DL_TFC26	UL_Conv_TFC2 , UL_Conv_TFC5 , UL_Inter_TFC3	DL_TFC0 , DL_TFC15 , UL_Conv_TFC0 , UL_Conv_TFC3 , UL_Inter_TFC0	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC2 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Conv_TFC5 , UL_Inter_TFC0 , UL_Inter_TFC1 , UL_Inter_TFC3	RB5: 81 RB6: 103 RB7: 60 RB8: 888	RB5: 81 RB6: 103 RB7: 60 RB8: 1272
12	DL_TFC12 ⁺ DL_TFC27	UL_Conv_TFC0 , UL_Conv_TFC3 , UL_Inter_TFC4	DL_TFC0 , DL_TFC15 , UL_Inter_TFC0	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Inter_TFC0 , UL_Inter_TFC1 , UL_Inter_TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 2552
13	DL_TFC13 ⁺ DL_TFC28	UL_Conv_TFC1 , UL_Conv_TFC4 , UL_Inter_TFC4	DL_TFC0 , DL_TFC15 , UL_Conv_TFC0 , UL_Conv_TFC3 , UL_Inter_TFC0	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Inter_TFC0 , UL_Inter_TFC1 , UL_Inter_TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 2552
14	DL_TFC14 ⁺ DL_TFC29	UL_Conv_TFC2 , UL_Conv_TFC5 , UL_Inter_TFC4	DL_TFC0 , DL_TFC15 , UL_Conv_TFC0 , UL_Conv_TFC3 , UL_Inter_TFC0	UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC2 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Conv_TFC5 , UL_Inter_TFC0 , UL_Inter_TFC1 , UL_Inter_TFC4	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 2552
NOTE 1: UL_Conv_TFC0 , UL_Conv_TFC1 , UL_Conv_TFC3 , UL_Conv_TFC4 , UL_Inter_TFC0 , and UL_Inter_TFC1 are part of minimum set of TFCs.						
NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB8 : Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB8 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the the payload size of the UL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit).						

18.2.2.41.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.

2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.

3. At step 15a and step 15b the UE shall return

- for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
- for sub-test 2: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.
- for sub-test 3: RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 4: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
- for sub-test 5: RLC SDUs on RB5, RB6, and RB7 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink.
- for sub-test 6: RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 7: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 8: RLC SDUs on RB5, RB6, and RB7 having the same content as sent by the SS; and RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS.
- for sub-test 9: RLC SDUs on RB8 having the same content as the first 888 bits of the test data sent by the SS; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 10: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 888 bits of the test data sent by the SS; and no data shall be received on RB6 and RB7.
- for sub-test 11: RLC SDUs on RB5, RB6, and RB7 having the same content as sent by the SS; and RLC SDUs on RB8 having the same content as the first 888 bits of the test data sent by the SS.
- for sub-test 12: RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; RLC SDUs on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 14: RLC SDUs on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; RLC SDUs on RB5, RB6 and RB7 having the same content as sent by SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.42 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:256 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH

18.2.2.42.1 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:256 kbps / PS RAB / Payload 320, 10 ms TTI

18.2.2.42.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.42.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.42 for the uplink payload 320 and downlink 10 ms TTI case.

18.2.2.42.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	TFI	RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (64 kbps)	DCCH
TFS	TF0, bits	0x81	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x103	1x60	1x336	1x148
	TF2, bits	1x81	N/A	N/A	2x336	N/A
	TF3, bits	N/A	N/A	N/A	3x336	N/A
	TF4, bits	N/A	N/A	N/A	4x336	N/A

Uplink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF1, TF0, TF0)
UL_TFC3	(TF0, TF0, TF0, TF1, TF0)
UL_TFC4	(TF1, TF0, TF0, TF1, TF0)
UL_TFC5	(TF2, TF1, TF1, TF1, TF0)
UL_TFC6	(TF0, TF0, TF0, TF2, TF0)
UL_TFC7	(TF1, TF0, TF0, TF2, TF0)
UL_TFC8	(TF2, TF1, TF1, TF2, TF0)
UL_TFC9	(TF0, TF0, TF0, TF3, TF0)
UL_TFC10	(TF1, TF0, TF0, TF3, TF0)
UL_TFC11	(TF2, TF1, TF1, TF3, TF0)
UL_TFC12	(TF0, TF0, TF0, TF4, TF0)
UL_TFC13	(TF1, TF0, TF0, TF4, TF0)
UL_TFC14	(TF2, TF1, TF1, TF4, TF0)
UL_TFC15	(TF0, TF0, TF0, TF0, TF1)
UL_TFC16	(TF1, TF0, TF0, TF0, TF1)
UL_TFC17	(TF2, TF1, TF1, TF0, TF1)
UL_TFC18	(TF0, TF0, TF0, TF1, TF1)
UL_TFC19	(TF1, TF0, TF0, TF1, TF1)
UL_TFC20	(TF2, TF1, TF1, TF1, TF1)
UL_TFC21	(TF0, TF0, TF0, TF2, TF1)
UL_TFC22	(TF1, TF0, TF0, TF2, TF1)
UL_TFC23	(TF2, TF1, TF1, TF2, TF1)
UL_TFC24	(TF0, TF0, TF0, TF3, TF1)
UL_TFC25	(TF1, TF0, TF0, TF3, TF1)
UL_TFC26	(TF2, TF1, TF1, TF3, TF1)
UL_TFC27	(TF0, TF0, TF0, TF4, TF1)
UL_TFC28	(TF1, TF0, TF0, TF4, TF1)
UL_TFC29	(TF2, TF1, TF1, TF4, TF1)

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (256 kbps)	DCCH
TFS	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x103	1x60	1x336	1x148
	TF2, bits	1x81	N/A	N/A	2x336	N/A
	TF3, bits	N/A	N/A	N/A	4x336	N/A
	TF4, bits	N/A	N/A	N/A	8x336	N/A

Downlink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0, TF0, TF0, TF0, TF0)
DL_TFC1	(TF1, TF0, TF0, TF0, TF0)
DL_TFC2	(TF2, TF1, TF1, TF0, TF0)
DL_TFC3	(TF0, TF0, TF0, TF1, TF0)
DL_TFC4	(TF1, TF0, TF0, TF1, TF0)
DL_TFC5	(TF2, TF1, TF1, TF1, TF0)
DL_TFC6	(TF0, TF0, TF0, TF2, TF0)
DL_TFC7	(TF1, TF0, TF0, TF2, TF0)
DL_TFC8	(TF2, TF1, TF1, TF2, TF0)
DL_TFC9	(TF0, TF0, TF0, TF3, TF0)
DL_TFC10	(TF1, TF0, TF0, TF3, TF0)
DL_TFC11	(TF2, TF1, TF1, TF3, TF0)
DL_TFC12	(TF0, TF0, TF0, TF4, TF0)
DL_TFC13	(TF1, TF0, TF0, TF4, TF0)
DL_TFC14	(TF2, TF1, TF1, TF4, TF0)
DL_TFC15	(TF0, TF0, TF0, TF0, TF1)
DL_TFC16	(TF1, TF0, TF0, TF0, TF1)
DL_TFC17	(TF2, TF1, TF1, TF0, TF1)
DL_TFC18	(TF0, TF0, TF0, TF1, TF1)
DL_TFC19	(TF1, TF0, TF0, TF1, TF1)
DL_TFC20	(TF2, TF1, TF1, TF1, TF1)
DL_TFC21	(TF0, TF0, TF0, TF2, TF1)
DL_TFC22	(TF1, TF0, TF0, TF2, TF1)
DL_TFC23	(TF2, TF1, TF1, TF2, TF1)
DL_TFC24	(TF0, TF0, TF0, TF3, TF1)
DL_TFC25	(TF1, TF0, TF0, TF3, TF1)
DL_TFC26	(TF2, TF1, TF1, TF3, TF1)
DL_TFC27	(TF0, TF0, TF0, TF4, TF1)
DL_TFC28	(TF1, TF0, TF0, TF4, TF1)
DL_TFC29	(TF2, TF1, TF1, TF4, TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	DL TFC1 , DL TFC16	UL TFC1 , DL TFC16	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC15 , UL TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 152	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL TFC2 , DL TFC17	UL TFC2 , DL TFC17	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC15 , UL TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 152	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL TFC3 , DL TFC18	UL TFC3 , DL TFC18	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC3 , UL TFC15 , UL TFC18	RB5: 39 RB6: 103 RB7: 60 RB8: 152	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL TFC4 , DL TFC19	UL TFC4 , DL TFC19	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC3 , UL TFC4 , UL TFC15 , UL TFC16 , UL TFC18 , UL TFC19	RB5: 39 RB6: 103 RB7: 60 RB8: 152	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL TFC5 , DL TFC20	UL TFC5 , DL TFC20	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC3 , UL TFC5 , UL TFC15 , UL TFC17 , UL TFC18 , UL TFC20	RB5: 81 RB6: 103 RB7: 60 RB8: 152	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL TFC6 , DL TFC21	UL TFC6 , DL TFC21	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC6 , UL TFC15 , UL TFC21	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: No data RB6: No data RB7: No data RB8: 632
7	DL TFC7 , DL TFC22	UL TFC7 , DL TFC22	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC6 , UL TFC7 , UL TFC15 , UL TFC16 , UL TFC21 , UL TFC22	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: 39 RB6: No data RB7: No data RB8: 632
8	DL TFC8 , DL TFC23	UL TFC8 , DL TFC23	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC6 , UL TFC8 , UL TFC15 , UL TFC17 , UL TFC21 , UL TFC23	RB5: 81 RB6: 103 RB7: 60 RB8: 312	RB5: 81 RB6: 103 RB7: 60 RB8: 632
9	DL TFC9 , DL TFC24	UL TFC9 , DL TFC24	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC9 , UL TFC15 , UL TFC24	RB5: 39 RB6: 103 RB7: 60 RB8: 472	RB5: No data RB6: No data RB7: No data RB8: 1272
10	DL TFC10 , DL TFC25	UL TFC10 , DL TFC25	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC9 , UL TFC10 , UL TFC15 , UL TFC16 , UL TFC24 , UL TFC25	RB5: 39 RB6: 103 RB7: 60 RB8: 472	RB5: 39 RB6: No data RB7: No data RB8: 1272

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
11	<u>DL TFC11, DL TFC26</u>	<u>UL TFC11, DL TFC26</u>	<u>DL TFC0, DL TFC15, UL TFC0, UL TFC15</u>	<u>UL TFC0, UL TFC2, UL TFC9, UL TFC11, UL TFC15, UL TFC17, UL TFC24, UL TFC26</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 472</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 1272</u>
12	<u>DL TFC12, DL TFC27</u>	<u>UL TFC12, DL TFC27</u>	<u>DL TFC0, DL TFC15, UL TFC0, UL TFC15</u>	<u>UL TFC0, UL TFC12, UL TFC15, UL TFC27</u>	<u>RB5: 39 RB6: 103 RB7: 60 RB8: 632</u>	<u>RB5: No data RB6: No data RB7: No data RB8: 2552</u>
13	<u>DL TFC13, DL TFC28</u>	<u>UL TFC13, DL TFC28</u>	<u>DL TFC0, DL TFC15, UL TFC0, UL TFC15</u>	<u>UL TFC0, UL TFC1, UL TFC12, UL TFC13, UL TFC15, UL TFC16, UL TFC27, UL TFC28</u>	<u>RB5: 39 RB6: 103 RB7: 60 RB8: 632</u>	<u>RB5: 39 RB6: No data RB7: No data RB8: 2552</u>
14	<u>DL TFC14, DL TFC29</u>	<u>UL TFC14, DL TFC29</u>	<u>DL TFC0, DL TFC15, UL TFC0, UL TFC15</u>	<u>UL TFC0, UL TFC2, UL TFC12, UL TFC14, UL TFC15, UL TFC17, UL TFC27, UL TFC29</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 632</u>	<u>RB5: 81 RB6: 103 RB7: 60 RB8: 2552</u>
NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB8: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the uplink TTI for RB8 is 20 ms while the downlink TTI is 10 ms then, to achieve continuous data transmission in uplink the size of the uplink RLC SDU has been set such that it will be transmitted over each TTIs, i.e. UL RLC SDU SIZE has been set to the uplink TFS size under test divided by 2 minus 8 (the size of a 7 bit length indicator and expansion bit).						

18.2.2.42.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
 - for sub-test 3: the 2 RLC SDUs on RB8 having the first 152 bits equal to the content of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4: the 2 RLC SDUs on RB8 having the first 152 bits equal to the content of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 5: the 2 RLC SDUs on RB8 having the first 152 bits equal to the content of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.

- for sub-test 6: the 2 RLC SDUs on RB8 having the first 312 bits equal to the content of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 7: the 2 RLC SDUs on RB8 having the first 312 bits equal to the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 8: the 2 RLC SDUs on RB8 having the first 312 bits equal to the content of the test data sent by the SS in downlink; and an RLC SDU on RB5, RB6, and RB7 having the same content as sent by SS.
- for sub-test 9: the 2 RLC SDUs on RB8 having the content equal to the first 472 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 10: the 2 RLC SDUs on RB8 having the content equal to the first 472 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 11: the 2 RLC SDUs on RB8 having the content equal to the first 472 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 12: the 2 RLC SDUs on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: the 2 RLC SDUs on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 14: the 2 RLC SDUs on RB8 having the content equal to the first 632 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.42.2 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:256 kbps / PS RAB / Payload 128, 20 ms TTI

18.2.2.42.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.42.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.42 for the uplink payload 128 and downlink 20 ms TTI case.

18.2.2.42.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (64 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>3x144</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>7x144</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>10x144</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>UL_TFC9</u>	<u>(TF0, TF0, TF0, TF3, TF0)</u>
<u>UL_TFC10</u>	<u>(TF1, TF0, TF0, TF3, TF0)</u>
<u>UL_TFC11</u>	<u>(TF2, TF1, TF1, TF3, TF0)</u>
<u>UL_TFC12</u>	<u>(TF0, TF0, TF0, TF4, TF0)</u>
<u>UL_TFC13</u>	<u>(TF1, TF0, TF0, TF4, TF0)</u>
<u>UL_TFC14</u>	<u>(TF2, TF1, TF1, TF4, TF0)</u>
<u>UL_TFC15</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC17</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>UL_TFC18</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC19</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC20</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>UL_TFC21</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC22</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC23</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>
<u>UL_TFC24</u>	<u>(TF0, TF0, TF0, TF3, TF1)</u>
<u>UL_TFC25</u>	<u>(TF1, TF0, TF0, TF3, TF1)</u>
<u>UL_TFC26</u>	<u>(TF2, TF1, TF1, TF3, TF1)</u>
<u>UL_TFC27</u>	<u>(TF0, TF0, TF0, TF4, TF1)</u>
<u>UL_TFC28</u>	<u>(TF1, TF0, TF0, TF4, TF1)</u>
<u>UL_TFC29</u>	<u>(TF2, TF1, TF1, TF4, TF1)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(256 kbps, 20 ms)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>8x336</u>	<u>N/A</u>
	<u>TF5, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>12x336</u>	<u>N/A</u>
	<u>TF6, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>16x336</u>	<u>N/A</u>

Downlink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0, TF0, TF0, TF0, TF0)
DL_TFC1	(TF1, TF0, TF0, TF0, TF0)
DL_TFC2	(TF2, TF1, TF1, TF0, TF0)
DL_TFC3	(TF0, TF0, TF0, TF1, TF0)
DL_TFC4	(TF1, TF0, TF0, TF1, TF0)
DL_TFC5	(TF2, TF1, TF1, TF1, TF0)
DL_TFC6	(TF0, TF0, TF0, TF2, TF0)
DL_TFC7	(TF1, TF0, TF0, TF2, TF0)
DL_TFC8	(TF2, TF1, TF1, TF2, TF0)
DL_TFC9	(TF0, TF0, TF0, TF3, TF0)
DL_TFC10	(TF1, TF0, TF0, TF3, TF0)
DL_TFC11	(TF2, TF1, TF1, TF3, TF0)
DL_TFC12	(TF0, TF0, TF0, TF4, TF0)
DL_TFC13	(TF1, TF0, TF0, TF4, TF0)
DL_TFC14	(TF2, TF1, TF1, TF4, TF0)
DL_TFC15	(TF0, TF0, TF0, TF5, TF0)
DL_TFC16	(TF1, TF0, TF0, TF5, TF0)
DL_TFC17	(TF2, TF1, TF1, TF5, TF0)
DL_TFC18	(TF0, TF0, TF0, TF6, TF0)
DL_TFC19	(TF1, TF0, TF0, TF6, TF0)
DL_TFC20	(TF2, TF1, TF1, TF6, TF0)
DL_TFC21	(TF0, TF0, TF0, TF0, TF1)
DL_TFC22	(TF1, TF0, TF0, TF0, TF1)
DL_TFC23	(TF2, TF1, TF1, TF0, TF1)
DL_TFC24	(TF0, TF0, TF0, TF1, TF1)
DL_TFC25	(TF1, TF0, TF0, TF1, TF1)
DL_TFC26	(TF2, TF1, TF1, TF1, TF1)
DL_TFC27	(TF0, TF0, TF0, TF2, TF1)
DL_TFC28	(TF1, TF0, TF0, TF2, TF1)
DL_TFC29	(TF2, TF1, TF1, TF2, TF1)
DL_TFC30	(TF0, TF0, TF0, TF3, TF1)
DL_TFC31	(TF1, TF0, TF0, TF3, TF1)
DL_TFC32	(TF2, TF1, TF1, TF3, TF1)
DL_TFC33	(TF0, TF0, TF0, TF4, TF1)
DL_TFC34	(TF1, TF0, TF0, TF4, TF1)
DL_TFC35	(TF2, TF1, TF1, TF4, TF1)
DL_TFC36	(TF0, TF0, TF0, TF5, TF1)
DL_TFC37	(TF1, TF0, TF0, TF5, TF1)
DL_TFC38	(TF2, TF1, TF1, TF5, TF1)
DL_TFC39	(TF0, TF0, TF0, TF6, TF1)
DL_TFC40	(TF1, TF0, TF0, TF6, TF1)
DL_TFC41	(TF2, TF1, TF1, TF6, TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	DL TFC1 , DL TFC22	UL TFC1 , UL TFC16	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC15 , UL TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL TFC2 , DL TFC23	UL TFC2 , UL TFC17	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC15 , UL TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL TFC3 , DL TFC24	UL TFC3 , UL TFC18	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC3 , UL TFC15 , UL TFC18	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL TFC4 , DL TFC25	UL TFC4 , UL TFC19	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC3 , UL TFC4 , UL TFC15 , UL TFC16 , UL TFC18 , UL TFC19	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL TFC5 , DL TFC26	UL TFC5 , UL TFC20	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC3 , UL TFC5 , UL TFC15 , UL TFC17 , UL TFC18 , UL TFC20	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL TFC6 , DL TFC27	UL TFC6 , UL TFC21	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC6 , UL TFC15 , UL TFC21	RB5: 39 RB6: 103 RB7: 60 RB8: 376	RB5: No data RB6: No data RB7: No data RB8: 632
7	DL TFC7 , DL TFC28	UL TFC7 , UL TFC22	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC6 , UL TFC7 , UL TFC15 , UL TFC16 , UL TFC21 , UL TFC22	RB5: 39 RB6: 103 RB7: 60 RB8: 376	RB5: 39 RB6: No data RB7: No data RB8: 632
8	DL TFC8 , DL TFC29	UL TFC8 , UL TFC23	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC6 , UL TFC8 , UL TFC15 , UL TFC17 , UL TFC21 , UL TFC23	RB5: 81 RB6: 103 RB7: 60 RB8: 376	RB5: 81 RB6: 103 RB7: 60 RB8: 632
9	DL TFC9 , DL TFC30	UL TFC9 , UL TFC24	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC9 , UL TFC15 , UL TFC24	RB5: 39 RB6: 103 RB7: 60 RB8: 888	RB5: No data RB6: No data RB7: No data RB8: 1272
10	DL TFC10 , DL TFC31	UL TFC10 , UL TFC25	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC9 , UL TFC10 , UL TFC15 , UL TFC16 , UL TFC24 , UL TFC25	RB5: 39 RB6: 103 RB7: 60 RB8: 888	RB5: 39 RB6: No data RB7: No data RB8: 1272

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
11	DL TFC11 , DL TFC32	UL TFC11 , UL TFC26	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC9 , UL TFC11 , UL TFC15 , UL TFC17 , UL TFC24 , UL TFC26	RB5: 81 RB6: 103 RB7: 60 RB8: 888	RB5: 81 RB6: 103 RB7: 60 RB8: 1272
12	DL TFC12 , DL TFC33	UL TFC12 , UL TFC27	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 2552
13	DL TFC13 , DL TFC34	UL TFC13 , UL TFC28	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 2552
14	DL TFC14 , DL TFC35	UL TFC14 , UL TFC29	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 2552
15	DL TFC15 , DL TFC36	UL TFC12 , UL TFC27	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 3832
16	DL TFC16 , DL TFC37	UL TFC13 , UL TFC28	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 3832
17	DL TFC17 , DL TFC38	UL TFC14 , UL TFC29	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 3832
18	DL TFC18 , DL TFC39	UL TFC12 , UL TFC27	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 5112
19	DL TFC19 , DL TFC40	UL TFC13 , UL TFC28	DL TFC0 , DL TFC21 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 5112

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
<u>20</u>	<u>DL_TFC20,</u> <u>DL_TFC41</u>	<u>UL_TFC14,</u> <u>UL_TFC29</u>	<u>DL_TFC0,</u> <u>DL_TFC21,</u> <u>UL_TFC0,</u> <u>UL_TFC15</u>	<u>UL_TFC0,</u> <u>UL_TFC2,</u> <u>UL_TFC12,</u> <u>UL_TFC14,</u> <u>UL_TFC15,</u> <u>UL_TFC17,</u> <u>UL_TFC27,</u> <u>UL_TFC29</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 1272</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 5112</u>
<p><u>NOTE:</u> See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. <u>RB8: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB8 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.42.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.
 - for sub-test 3: RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
 - for sub-test 5: RLC SDUs on RB5, RB6, and RB7 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 120 bits of the test data sent by the SS in downlink.
 - for sub-test 6: RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 7: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS; and no data shall be received on RB6 and RB7.
 - for sub-test 8: RLC SDUs on RB5, RB6, and RB7 having the same content as sent by the SS; and RLC SDUs on RB8 having the same content as the first 376 bits of the test data sent by the SS.
 - for sub-test 9: RLC SDUs on RB8 having the same content as the first 888 bits of the test data sent by the SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 10: RLC SDUs on RB5 having the same content as sent by the SS; RLC SDUs on RB8 having the same content as the first 888 bits of the test data sent by the SS; and no data shall be received on RB6 and RB7.
 - for sub-test 11: RLC SDUs on RB5, RB6, and RB7 having the same content as sent by the SS; and RLC SDUs on RB8 having the same content as the first 888 bits of the test data sent by the SS.

- for sub-test 12: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 14: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 15: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 16: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 17: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 18: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 19: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 20: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.43 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:384 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH

18.2.2.43.1 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:384 kbps / PS RAB / Payload 320, 10 ms TTI, 1 CCTrCH

18.2.2.43.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.43.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.43 for the uplink payload 320, downlink 10 ms TTI, and 1 CCTrCH configuration case.

18.2.2.43.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (64 kbps, 20 ms TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>

Uplink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF1, TF0, TF0)
UL_TFC3	(TF0, TF0, TF0, TF1, TF0)
UL_TFC4	(TF1, TF0, TF0, TF1, TF0)
UL_TFC5	(TF2, TF1, TF1, TF1, TF0)
UL_TFC6	(TF0, TF0, TF0, TF2, TF0)
UL_TFC7	(TF1, TF0, TF0, TF2, TF0)
UL_TFC8	(TF2, TF1, TF1, TF2, TF0)
UL_TFC9	(TF0, TF0, TF0, TF3, TF0)
UL_TFC10	(TF1, TF0, TF0, TF3, TF0)
UL_TFC11	(TF2, TF1, TF1, TF3, TF0)
UL_TFC12	(TF0, TF0, TF0, TF4, TF0)
UL_TFC13	(TF1, TF0, TF0, TF4, TF0)
UL_TFC14	(TF2, TF1, TF1, TF4, TF0)
UL_TFC15	(TF0, TF0, TF0, TF0, TF1)
UL_TFC16	(TF1, TF0, TF0, TF0, TF1)
UL_TFC17	(TF2, TF1, TF1, TF0, TF1)
UL_TFC18	(TF0, TF0, TF0, TF1, TF1)
UL_TFC19	(TF1, TF0, TF0, TF1, TF1)
UL_TFC20	(TF2, TF1, TF1, TF1, TF1)
UL_TFC21	(TF0, TF0, TF0, TF2, TF1)
UL_TFC22	(TF1, TF0, TF0, TF2, TF1)
UL_TFC23	(TF2, TF1, TF1, TF2, TF1)
UL_TFC24	(TF0, TF0, TF0, TF3, TF1)
UL_TFC25	(TF1, TF0, TF0, TF3, TF1)
UL_TFC26	(TF2, TF1, TF1, TF3, TF1)
UL_TFC27	(TF0, TF0, TF0, TF4, TF1)
UL_TFC28	(TF1, TF0, TF0, TF4, TF1)
UL_TFC29	(TF2, TF1, TF1, TF4, TF1)

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (384 kbps, 10 ms)	DCCH
TFS	TF0, bits	1x0	0x103	0x60	0x336	0x148
	TF1, bits	1x39	1x103	1x60	1x336	1x148
	TF2, bits	1x81	N/A	N/A	2x336	N/A
	TF3, bits	N/A	N/A	N/A	4x336	N/A
	TF4, bits	N/A	N/A	N/A	8x336	N/A
	TF5, bits	N/A	N/A	N/A	12x336	N/A

Downlink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0, TF0, TF0, TF0, TF0)
DL_TFC1	(TF1, TF0, TF0, TF0, TF0)
DL_TFC2	(TF2, TF1, TF1, TF0, TF0)
DL_TFC3	(TF0, TF0, TF0, TF1, TF0)
DL_TFC4	(TF1, TF0, TF0, TF1, TF0)
DL_TFC5	(TF2, TF1, TF1, TF1, TF0)
DL_TFC6	(TF0, TF0, TF0, TF2, TF0)
DL_TFC7	(TF1, TF0, TF0, TF2, TF0)
DL_TFC8	(TF2, TF1, TF1, TF2, TF0)
DL_TFC9	(TF0, TF0, TF0, TF3, TF0)
DL_TFC10	(TF1, TF0, TF0, TF3, TF0)
DL_TFC11	(TF2, TF1, TF1, TF3, TF0)
DL_TFC12	(TF0, TF0, TF0, TF4, TF0)
DL_TFC13	(TF1, TF0, TF0, TF4, TF0)
DL_TFC14	(TF2, TF1, TF1, TF4, TF0)
DL_TFC15	(TF0, TF0, TF0, TF5, TF0)
DL_TFC16	(TF1, TF0, TF0, TF5, TF0)
DL_TFC17	(TF2, TF1, TF1, TF5, TF0)
DL_TFC18	(TF0, TF0, TF0, TF0, TF1)
DL_TFC19	(TF1, TF0, TF0, TF0, TF1)
DL_TFC20	(TF2, TF1, TF1, TF0, TF1)
DL_TFC21	(TF0, TF0, TF0, TF1, TF1)
DL_TFC22	(TF1, TF0, TF0, TF1, TF1)
DL_TFC23	(TF2, TF1, TF1, TF1, TF1)
DL_TFC24	(TF0, TF0, TF0, TF2, TF1)
DL_TFC25	(TF1, TF0, TF0, TF2, TF1)
DL_TFC26	(TF2, TF1, TF1, TF2, TF1)
DL_TFC27	(TF0, TF0, TF0, TF3, TF1)
DL_TFC28	(TF1, TF0, TF0, TF3, TF1)
DL_TFC29	(TF2, TF1, TF1, TF3, TF1)
DL_TFC30	(TF0, TF0, TF0, TF4, TF1)
DL_TFC31	(TF1, TF0, TF0, TF4, TF1)
DL_TFC32	(TF2, TF1, TF1, TF4, TF1)
DL_TFC33	(TF0, TF0, TF0, TF5, TF1)
DL_TFC34	(TF1, TF0, TF0, TF5, TF1)
DL_TFC35	(TF2, TF1, TF1, TF5, TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 3)	<u>UL RLC SDU size (bits)</u> (note 1)	<u>Test data size (bits)</u> (note 1)
1	DL_TFC1 , DL_TFC19	UL_TFC1 , UL_TFC16	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC15 , UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 152	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL_TFC2 , DL_TFC20	UL_TFC2 , UL_TFC17	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC15 , UL_TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 152	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL_TFC3 , DL_TFC21	UL_TFC3 , UL_TFC18	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC15 , UL_TFC18	RB5: 39 RB6: 103 RB7: 60 RB8: 152	RB5: No data RB6: No data RB7: No data RB8: 312
4	DL_TFC4 , DL_TFC22	UL_TFC4 , UL_TFC19	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC4 , UL_TFC15 , UL_TFC16 , UL_TFC18 , UL_TFC19	RB5: 39 RB6: 103 RB7: 60 RB8: 152	RB5: 39 RB6: No data RB7: No data RB8: 312
5	DL_TFC5 , DL_TFC23	UL_TFC5 , UL_TFC20	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC5 , UL_TFC17 , UL_TFC18 , UL_TFC15 , UL_TFC20	RB5: 81 RB6: 103 RB7: 60 RB8: 152	RB5: 81 RB6: 103 RB7: 60 RB8: 312
6	DL_TFC6 , DL_TFC24	UL_TFC6 , UL_TFC21	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC15 , UL_TFC21	RB5: 39 RB6: 103 RB7: 60 RB8: 312 (note 2)	RB5: No data RB6: No data RB7: No data RB8: 632
7	DL_TFC7 , DL_TFC25	UL_TFC7 , UL_TFC22	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC7 , UL_TFC15 , UL_TFC16 , UL_TFC21 , UL_TFC22	RB5: 39 RB6: 103 RB7: 60 RB8: 312 (note 2)	RB5: 39 RB6: No data RB7: No data RB8: 632

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u> (note 3)	<u>UL RLC SDU size (bits)</u> (note 1)	<u>Test data size (bits)</u> (note 1)
8	DL_TFC8 , DL_TFC26	UL_TFC8 , UL_TFC23	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC8 , UL_TFC15 , UL_TFC17 , UL_TFC21 , UL_TFC23	RB5: 81 RB6: 103 RB7: 60 RB8: 312 (note 2)	RB5: 81 RB6: 103 RB7: 60 RB8: 632
9	DL_TFC9 , DL_TFC27	UL_TFC9 , UL_TFC24	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC15 , UL_TFC24	RB5: 39 RB6: 103 RB7: 60 RB8: 472	RB5: No data RB6: No data RB7: No data RB8: 1272
10	DL_TFC10 , DL_TFC28	UL_TFC10 , UL_TFC25	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC10 , UL_TFC15 , UL_TFC16 , UL_TFC24 , UL_TFC25	RB5: 39 RB6: 103 RB7: 60 RB8: 472	RB5: 39 RB6: No data RB7: No data RB8: 1272
11	DL_TFC11 , DL_TFC29	UL_TFC11 , UL_TFC26	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC9 , UL_TFC11 , UL_TFC15 , UL_TFC17 , UL_TFC24 , UL_TFC26	RB5: 81 RB6: 103 RB7: 60 RB8: 472	RB5: 81 RB6: 103 RB7: 60 RB8: 1272
12	DL_TFC12 , DL_TFC30	UL_TFC12 , UL_TFC27	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC12 , UL_TFC15 , UL_TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 632 (note 2)	RB5: No data RB6: No data RB7: No data RB8: 2552
13	DL_TFC13 , DL_TFC31	UL_TFC13 , UL_TFC28	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC12 , UL_TFC13 , UL_TFC15 , UL_TFC16 , UL_TFC27 , UL_TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 632 (note 2)	RB5: 39 RB6: No data RB7: No data RB8: 2552
14	DL_TFC14 , DL_TFC32	UL_TFC14 , UL_TFC29	DL_TFC0 , DL_TFC18 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC12 , UL_TFC14 , UL_TFC15 , UL_TFC17 , UL_TFC27 , UL_TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 632 (note 2)	RB5: 81 RB6: 103 RB7: 60 RB8: 2552

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u> (note 3)	<u>UL RLC SDU size (bits)</u> (note 1)	<u>Test data size (bits)</u> (note 1)
15	<u>DL TFC15,</u> <u>DL TFC33</u>	<u>UL TFC12,</u> <u>UL TFC27</u>	<u>DL TFC0,</u> <u>DL TFC18,</u> <u>UL TFC0,</u> <u>UL TFC15</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC27</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 632</u> (note 2)	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 3832</u>
16	<u>DL TFC16,</u> <u>DL TFC34</u>	<u>UL TFC13,</u> <u>UL TFC28</u>	<u>DL TFC0,</u> <u>DL TFC18,</u> <u>UL TFC0,</u> <u>UL TFC15</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC12,</u> <u>UL TFC13,</u> <u>UL TFC15,</u> <u>UL TFC16,</u> <u>UL TFC27,</u> <u>UL TFC28</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 632</u> (note 2)	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 3832</u>
17	<u>DL TFC17,</u> <u>DL TFC35</u>	<u>UL TFC14,</u> <u>UL TFC29</u>	<u>DL TFC0,</u> <u>DL TFC18,</u> <u>UL TFC0,</u> <u>UL TFC15</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC2,</u> <u>UL TFC3,</u> <u>UL TFC12,</u> <u>UL TFC14,</u> <u>UL TFC15,</u> <u>UL TFC17,</u> <u>UL TFC27,</u> <u>UL TFC29</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 632</u> (note 2)	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 3832</u>
<p><u>NOTE 1: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB8: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit).</u></p> <p><u>NOTE 2: RB8: For sub-tests to adopt to the difference in downlink TTI (10 ms) and uplink TTI (20ms) the UL RLC SDU size has been chosen such that 2 SDUs will be returned per uplink TTI. I.e. the UL RLC SDU size is set equal to half the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</u></p> <p><u>NOTE 3: UL TFC0, UL TFC1, UL TFC2, UL TFC3 and UL TFC15 are part of minimum set of TFCIs.</u></p>						

18.2.2.43.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.
 - for sub-test 3: RLC SDUs on RB8 having 2 SDUs equal to the content of the first 152 bits of test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4: RLC SDUs on RB5 having the same content as sent by the SS; RB8 having 2 SDUs equal to the content of the first 152 bits of test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
 - for sub-test 5: RLC SDUs on RB5, RB6, and RB7 having the same content as sent by the SS; and RB8 having 2 SDUs equal to the content of the first 152 bits of test data sent by the SS in downlink.

- for sub-test 6: RLC SDUs on RB8 having 2 SDUs equal to the content of the first 312 bits of test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 7: RLC SDUs on RB5 having the same content as sent by the SS; RB8 having 2 SDUs equal to the content of the first 312 bits of test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
- for sub-test 8: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and RB8 having 2 SDUs equal to the content of the first 312 bits of test data sent by the SS in downlink.
- for sub-test 9: RLC SDUs on RB8 having 2 SDUs equal to the content of the first 472 bits of test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 10: RLC SDUs on RB5 having the same content as sent by the SS; RB8 having 2 SDUs equal to the content of the first 472 bits of test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
- for sub-test 11: RLC SDUs on RB5, RB6, and RB7 having the same content as sent by the SS; and RB8 having 2 SDUs equal to the content of the first 472 bits of test data sent by the SS in downlink.
- for sub-test 12: RLC SDUs on RB8 having 2 SDUs equal to the content of the first 632 bits of test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: RLC SDUs on RB5 having the same content as sent by the SS; RB8 having 2 SDUs equal to the content of the first 632 bits of test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
- for sub-test 14: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and RB8 having 2 SDUs equal to the content of the first 632 bits of test data sent by the SS in downlink.
- for sub-test 15: RB8 having 2 SDUs equal to the content of the first 632 bits of test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 16: RLC SDUs on RB5 having the same content as sent by the SS; RB8 having 2 SDUs equal to the content of the first 632 bits of test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
- for sub-test 17: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the the SS; and RB8 having 2 SDUs equal to the content of the first 632 bits of test data sent by the SS in downlink.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.43.2 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:64 DL:384 kbps / PS RAB / Payload 128, 20 ms TTI, 2 CCTrCHs

18.2.2.43.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.43.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.43 for the uplink payload 128, downlink 20 ms TTI, and 2 CCTrCH configuration case.

18.2.2.43.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
4	<u>DL Conv T FC1,</u> <u>DL Inter TF C1,</u> <u>DL Conv T FC4</u>	<u>UL Conv T FC1,</u> <u>UL Inter T FC1,</u> <u>UL Conv T FC4</u>	<u>DL Conv TFC0,</u> <u>DL Conv TFC3,</u> <u>DL Inter TFC0,</u> <u>UL Conv TFC0,</u> <u>UL Conv TFC3,</u> <u>UL Inter TFC0</u>	<u>UL Conv TFC0,</u> <u>UL Conv TFC1,</u> <u>UL Conv TFC2,</u> <u>UL Conv TFC3,</u> <u>UL Conv TFC4,</u> <u>UL Conv TFC5,</u> <u>UL Inter TFC0,</u> <u>UL Inter TFC1</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 120</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 312</u>
5	<u>DL Conv T FC2,</u> <u>DL Inter TF C1,</u> <u>DL Conv T FC5</u>	<u>UL Conv T FC2,</u> <u>UL Inter T FC1,</u> <u>UL Conv T FC5</u>	<u>DL Conv TFC0,</u> <u>DL Conv TFC3,</u> <u>DL Inter TFC0,</u> <u>UL Conv TFC0,</u> <u>UL Conv TFC3,</u> <u>UL Inter TFC0</u>	<u>UL Conv TFC0,</u> <u>UL Conv TFC1,</u> <u>UL Conv TFC2,</u> <u>UL Conv TFC3,</u> <u>UL Conv TFC4,</u> <u>UL Conv TFC5,</u> <u>UL Inter TFC0,</u> <u>UL Inter TFC1</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 120</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 312</u>
6	<u>DL Conv T FC0,</u> <u>DL Inter TF C2,</u> <u>DL Conv T FC3</u>	<u>UL Conv T FC0,</u> <u>UL Inter T FC2,</u> <u>UL Conv T FC3</u>	<u>DL Inter TFC0,</u> <u>UL Inter TFC0</u>	<u>UL Conv TFC0,</u> <u>UL Conv TFC1,</u> <u>UL Conv TFC2,</u> <u>UL Conv TFC3,</u> <u>UL Conv TFC4,</u> <u>UL Conv TFC5,</u> <u>UL Inter TFC0,</u> <u>UL Inter TFC1,</u> <u>UL Inter TFC2</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 376</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 632</u>
7	<u>DL Conv T FC1,</u> <u>DL Inter TF C2,</u> <u>DL Conv T FC4</u>	<u>UL Conv T FC1,</u> <u>UL Inter T FC2,</u> <u>UL Conv T FC4</u>	<u>DL Conv TFC0,</u> <u>DL Conv TFC3,</u> <u>DL Inter TFC0,</u> <u>UL Conv TFC0,</u> <u>UL Conv TFC3,</u> <u>UL Inter TFC0</u>	<u>UL Conv TFC0,</u> <u>UL Conv TFC1,</u> <u>UL Conv TFC2,</u> <u>UL Conv TFC3,</u> <u>UL Conv TFC4,</u> <u>UL Conv TFC5,</u> <u>UL Inter TFC0,</u> <u>UL Inter TFC1,</u> <u>UL Inter TFC2</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 376</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 632</u>
8	<u>DL Conv T FC2,</u> <u>DL Inter TF C2,</u> <u>DL Conv T FC5</u>	<u>UL Conv T FC2,</u> <u>UL Inter T FC2,</u> <u>UL Conv T FC5</u>	<u>DL Conv TFC0,</u> <u>DL Conv TFC3,</u> <u>DL Inter TFC0,</u> <u>UL Conv TFC0,</u> <u>UL Conv TFC3,</u> <u>UL Inter TFC0</u>	<u>UL Conv TFC0,</u> <u>UL Conv TFC1,</u> <u>UL Conv TFC2,</u> <u>UL Conv TFC3,</u> <u>UL Conv TFC4,</u> <u>UL Conv TFC5,</u> <u>UL Inter TFC0,</u> <u>UL Inter TFC1,</u> <u>UL Inter TFC2</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 376</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 632</u>
9	<u>DL Conv T FC0,</u> <u>DL Inter TF C3,</u> <u>DL Conv T FC3</u>	<u>UL Conv T FC0,</u> <u>UL Inter T FC3,</u> <u>UL Conv T FC3</u>	<u>DL Inter TFC0,</u> <u>UL Inter TFC0</u>	<u>UL Conv TFC0,</u> <u>UL Conv TFC1,</u> <u>UL Conv TFC2,</u> <u>UL Conv TFC3,</u> <u>UL Conv TFC4,</u> <u>UL Conv TFC5,</u> <u>UL Inter TFC0,</u> <u>UL Inter TFC1,</u> <u>UL Inter TFC3</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 888</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 1272</u>
10	<u>DL Conv T FC1,</u> <u>DL Inter TF C3,</u> <u>DL Conv T FC4</u>	<u>UL Conv T FC1,</u> <u>UL Inter T FC3,</u> <u>UL Conv T FC4</u>	<u>DL Conv TFC0,</u> <u>DL Conv TFC3,</u> <u>DL Inter TFC0,</u> <u>UL Conv TFC0,</u> <u>UL Conv TFC3,</u> <u>UL Inter TFC0</u>	<u>UL Conv TFC0,</u> <u>UL Conv TFC1,</u> <u>UL Conv TFC2,</u> <u>UL Conv TFC3,</u> <u>UL Conv TFC4,</u> <u>UL Conv TFC5,</u> <u>UL Inter TFC0,</u> <u>UL Inter TFC1,</u> <u>UL Inter TFC3</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 888</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 1272</u>

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
11	DL Conv T FC2 , DL Inter TF C3 , DL Conv T FC5	UL Conv T FC2 , UL Inter T FC3 , UL Conv T FC5	DL Conv TFC0 , DL Conv TFC3 , DL Inter TFC0 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC3	RB5: 81 RB6: 103 RB7: 60 RB8: 888	RB5: 81 RB6: 103 RB7: 60 RB8: 1272
12	DL Conv T FC0 , DL Inter TF C4 , DL Conv T FC3	UL Conv T FC0 , UL Inter T FC4 , UL Conv T FC3	DL Inter TFC0 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 2552
13	DL Conv T FC1 , DL Inter TF C4 , DL Conv T FC4	UL Conv T FC1 , UL Inter T FC4 , UL Conv T FC4	DL Conv TFC0 , DL Conv TFC3 , DL Inter TFC0 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 2552
14	DL Conv T FC2 , DL Inter TF C4 , DL Conv T FC5	UL Conv T FC2 , UL Inter T FC4 , UL Conv T FC5	DL Conv TFC0 , DL Conv TFC3 , DL Inter TFC0 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 2552
15	DL Conv T FC0 , DL Inter TF C5 , DL Conv T FC3	UL Conv T FC0 , UL Inter T FC4 , UL Conv T FC3	DL Inter TFC0 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 3832
16	DL Conv T FC1 , DL Inter TF C5 , DL Conv T FC4	UL Conv T FC1 , UL Inter T FC4 , UL Conv T FC4	DL Conv TFC0 , DL Conv TFC3 , DL Inter TFC0 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 3832
17	DL Conv T FC2 , DL Inter TF C5 , DL Conv T FC5	UL Conv T FC2 , UL Inter T FC4 , UL Conv T FC5	DL Conv TFC0 , DL Conv TFC3 , DL Inter TFC0 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 3832

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
18	DL Conv T FC0 , DL Inter TF C6 , DL Conv T FC3	UL Conv T FC0 , UL Inter T FC4 , UL Conv T FC3	DL Inter TFC0 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 5112
19	DL Conv T FC1 , DL Inter TF C6 , DL Conv T FC4	UL Conv T FC1 , UL Inter T FC4 , UL Conv T FC4	DL Conv TFC0 , DL Conv TFC3 , DL Inter TFC0 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 5112
20	DL Conv T FC2 , DL Inter TF C6 , DL Conv T FC5	UL Conv T FC2 , UL Inter T FC4 , UL Conv T FC5	DL Conv TFC0 , DL Conv TFC3 , DL Inter TFC0 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 5112
21	DL Conv T FC0 , DL Inter TF C7 , DL Conv T FC3	UL Conv T FC0 , UL Inter T FC4 , UL Conv T FC3	DL Inter TFC0 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 6392
22	DL Conv T FC1 , DL Inter TF C7 , DL Conv T FC4	UL Conv T FC1 , UL Inter T FC4 , UL Conv T FC4	DL Conv TFC0 , DL Conv TFC3 , DL Inter TFC0 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 6392
23	DL Conv T FC2 , DL Inter TF C7 , DL Conv T FC5	UL Conv T FC2 , UL Inter T FC4 , UL Conv T FC5	DL Conv TFC0 , DL Conv TFC3 , DL Inter TFC0 , UL Conv TFC0 , UL Conv TFC3 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 6392
24	DL Conv T FC0 , DL Inter TF C8 , DL Conv T FC3	UL Conv T FC0 , UL Inter T FC4 , UL Conv T FC3	DL Inter TFC0 , UL Inter TFC0	UL Conv TFC0 , UL Conv TFC1 , UL Conv TFC2 , UL Conv TFC3 , UL Conv TFC4 , UL Conv TFC5 , UL Inter TFC0 , UL Inter TFC1 , UL Inter TFC4	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 7672

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
<u>25</u>	<u>DL Conv T FC1,</u> <u>DL Inter TF C8,</u> <u>DL Conv T FC4</u>	<u>UL Conv T FC1,</u> <u>UL Inter T FC4,</u> <u>UL Conv T FC4</u>	<u>DL Conv TFC0,</u> <u>DL Conv TFC3,</u> <u>DL Inter TFC0,</u> <u>UL Conv TFC0,</u> <u>UL Conv TFC3,</u> <u>UL Inter TFC0</u>	<u>UL Conv TFC0,</u> <u>UL Conv TFC1,</u> <u>UL Conv TFC2,</u> <u>UL Conv TFC3,</u> <u>UL Conv TFC4,</u> <u>UL Conv TFC5,</u> <u>UL Inter TFC0,</u> <u>UL Inter TFC1,</u> <u>UL Inter TFC4</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 1272</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 7672</u>
<u>26</u>	<u>DL Conv T FC2,</u> <u>DL Inter TF C8,</u> <u>DL Conv T FC5</u>	<u>UL Conv T FC2,</u> <u>UL Inter T FC4,</u> <u>UL Conv T FC5</u>	<u>DL Conv TFC0,</u> <u>DL Conv TFC3,</u> <u>DL Inter TFC0,</u> <u>UL Conv TFC0,</u> <u>UL Conv TFC3,</u> <u>UL Inter TFC0</u>	<u>UL Conv TFC0,</u> <u>UL Conv TFC1,</u> <u>UL Conv TFC2,</u> <u>UL Conv TFC3,</u> <u>UL Conv TFC4,</u> <u>UL Conv TFC5,</u> <u>UL Inter TFC0,</u> <u>UL Inter TFC1,</u> <u>UL Inter TFC4</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 1272</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 7672</u>
<u>NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB8: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB8 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit).</u>						

18.2.2.43.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
 - for sub-test 3: an RLC SDU on RB8 having the content of the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4: an RLC SDU on RB5 having the content equal to the content of the test data sent by the SS in downlink; an RLC SDU on RB8 having the content of the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
 - for sub-test 5: an RLC SDU on RB5, RB6, and RB7 having the same content as sent by SS; and an RLC SDU on RB8 having the content of the first 120 bits of the test data sent by the SS in downlink.
 - for sub-test 6: an RLC SDU on RB8 having the content of the first 376 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 7: an RLC SDU on RB5 having the content equal to the content of the test data sent by the SS in downlink; an RLC SDU on RB8 having the content of the first 376 bits of the test data sent by the SS in downlink; and no data shall be received on RB6 and RB7.
 - for sub-test 8: an RLC SDU on RB5, RB6, and RB7 having the same content as sent by SS; and an RLC SDU on RB8 having the content of the first 376 bits of the test data sent by the SS in downlink.

- for sub-test 9: an RLC SDU on RB8 having the content equal to the first 888 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 10: an RLC SDU on RB8 having the content equal to the first 888 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 11: an RLC SDU on RB8 having the content equal to the first 888 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 12: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 13: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 14: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 15: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 16: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 17: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 18: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 19: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 20: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 21: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 22: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 23: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 24: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 25: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 26: an RLC SDU on RB8 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.44 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:128 DL:2048 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH

18.2.2.44.1 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:128 DL:2048 kbps / PS RAB / Payload 320, 10 ms TTI

18.2.2.44.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.44.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.44 for the uplink payload 320 and downlink 10 ms TTI case.

18.2.2.44.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (128 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>8x336</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF1, TF0, TF0)
UL_TFC3	(TF0, TF0, TF0, TF1, TF0)
UL_TFC4	(TF1, TF0, TF0, TF1, TF0)
UL_TFC5	(TF2, TF1, TF1, TF1, TF0)
UL_TFC6	(TF0, TF0, TF0, TF2, TF0)
UL_TFC7	(TF1, TF0, TF0, TF2, TF0)
UL_TFC8	(TF2, TF1, TF1, TF2, TF0)
UL_TFC9	(TF0, TF0, TF0, TF3, TF0)
UL_TFC10	(TF1, TF0, TF0, TF3, TF0)
UL_TFC11	(TF2, TF1, TF1, TF3, TF0)
UL_TFC12	(TF0, TF0, TF0, TF4, TF0)
UL_TFC13	(TF1, TF0, TF0, TF4, TF0)
UL_TFC14	(TF2, TF1, TF1, TF4, TF0)
UL_TFC15	(TF0, TF0, TF0, TF0, TF1)
UL_TFC16	(TF1, TF0, TF0, TF0, TF1)
UL_TFC17	(TF2, TF1, TF1, TF0, TF1)
UL_TFC18	(TF0, TF0, TF0, TF1, TF1)
UL_TFC19	(TF1, TF0, TF0, TF1, TF1)
UL_TFC20	(TF2, TF1, TF1, TF1, TF1)
UL_TFC21	(TF0, TF0, TF0, TF2, TF1)
UL_TFC22	(TF1, TF0, TF0, TF2, TF1)
UL_TFC23	(TF2, TF1, TF1, TF2, TF1)
UL_TFC24	(TF0, TF0, TF0, TF3, TF1)
UL_TFC25	(TF1, TF0, TF0, TF3, TF1)
UL_TFC26	(TF2, TF1, TF1, TF3, TF1)
UL_TFC27	(TF0, TF0, TF0, TF4, TF1)
UL_TFC28	(TF1, TF0, TF0, TF4, TF1)
UL_TFC29	(TF2, TF1, TF1, TF4, TF1)

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(2048 kbps)</u>	<u>DCCH</u>
TFS	TF0, bits	1x0	0x103	0x60	0x656	0x148
	TF1, bits	1x39	1x103	1x60	1x656	1x148
	TF2, bits	1x81	N/A	N/A	2x656	N/A
	TF3, bits	N/A	N/A	N/A	4x656	N/A
	TF4, bits	N/A	N/A	N/A	8x656	N/A
	TF5, bits	N/A	N/A	N/A	12x656	N/A
	TF6, bits	N/A	N/A	N/A	16x656	N/A
	TF7, bits	N/A	N/A	N/A	20x656	N/A
	TF8, bits	N/A	N/A	N/A	24x656	N/A
	TF9, bits	N/A	N/A	N/A	28x656	N/A
TF10, bits	N/A	N/A	N/A	31x656	N/A	

Downlink TFCs:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
DL_TFC0	(TF0, TF0, TF0, TF0, TF0)
DL_TFC1	(TF1, TF0, TF0, TF0, TF0)
DL_TFC2	(TF2, TF1, TF1, TF0, TF0)
DL_TFC3	(TF0, TF0, TF0, TF1, TF0)
DL_TFC4	(TF1, TF0, TF0, TF1, TF0)
DL_TFC5	(TF2, TF1, TF1, TF1, TF0)
DL_TFC6	(TF0, TF0, TF0, TF2, TF0)
DL_TFC7	(TF1, TF0, TF0, TF2, TF0)
DL_TFC8	(TF2, TF1, TF1, TF2, TF0)
DL_TFC9	(TF0, TF0, TF0, TF3, TF0)
DL_TFC10	(TF1, TF0, TF0, TF3, TF0)

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC11</u>	<u>(TF2, TF1, TF1, TF3, TF0)</u>
<u>DL_TFC12</u>	<u>(TF0, TF0, TF0, TF4, TF0)</u>
<u>DL_TFC13</u>	<u>(TF1, TF0, TF0, TF4, TF0)</u>
<u>DL_TFC14</u>	<u>(TF2, TF1, TF1, TF4, TF0)</u>
<u>DL_TFC15</u>	<u>(TF0, TF0, TF0, TF5, TF0)</u>
<u>DL_TFC16</u>	<u>(TF1, TF0, TF0, TF5, TF0)</u>
<u>DL_TFC17</u>	<u>(TF2, TF1, TF1, TF5, TF0)</u>
<u>DL_TFC18</u>	<u>(TF0, TF0, TF0, TF6, TF0)</u>
<u>DL_TFC19</u>	<u>(TF1, TF0, TF0, TF6, TF0)</u>
<u>DL_TFC20</u>	<u>(TF2, TF1, TF1, TF6, TF0)</u>
<u>DL_TFC21</u>	<u>(TF0, TF0, TF0, TF7, TF0)</u>
<u>DL_TFC22</u>	<u>(TF1, TF0, TF0, TF7, TF0)</u>
<u>DL_TFC23</u>	<u>(TF2, TF1, TF1, TF7, TF0)</u>
<u>DL_TFC24</u>	<u>(TF0, TF0, TF0, TF8, TF0)</u>
<u>DL_TFC25</u>	<u>(TF1, TF0, TF0, TF8, TF0)</u>
<u>DL_TFC26</u>	<u>(TF2, TF1, TF1, TF8, TF0)</u>
<u>DL_TFC27</u>	<u>(TF0, TF0, TF0, TF9, TF0)</u>
<u>DL_TFC28</u>	<u>(TF1, TF0, TF0, TF9, TF0)</u>
<u>DL_TFC29</u>	<u>(TF2, TF1, TF1, TF9, TF0)</u>
<u>DL_TFC30</u>	<u>(TF0, TF0, TF0, TF10, TF0)</u>
<u>DL_TFC31</u>	<u>(TF1, TF0, TF0, TF10, TF0)</u>
<u>DL_TFC32</u>	<u>(TF2, TF1, TF1, TF10, TF0)</u>
<u>DL_TFC33</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC34</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC35</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>DL_TFC36</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC37</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC38</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>DL_TFC39</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC40</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC41</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>
<u>DL_TFC42</u>	<u>(TF0, TF0, TF0, TF3, TF1)</u>
<u>DL_TFC43</u>	<u>(TF1, TF0, TF0, TF3, TF1)</u>
<u>DL_TFC44</u>	<u>(TF2, TF1, TF1, TF3, TF1)</u>
<u>DL_TFC45</u>	<u>(TF0, TF0, TF0, TF4, TF1)</u>
<u>DL_TFC46</u>	<u>(TF1, TF0, TF0, TF4, TF1)</u>
<u>DL_TFC47</u>	<u>(TF2, TF1, TF1, TF4, TF1)</u>
<u>DL_TFC48</u>	<u>(TF0, TF0, TF0, TF5, TF1)</u>
<u>DL_TFC49</u>	<u>(TF1, TF0, TF0, TF5, TF1)</u>
<u>DL_TFC50</u>	<u>(TF2, TF1, TF1, TF5, TF1)</u>
<u>DL_TFC51</u>	<u>(TF0, TF0, TF0, TF6, TF1)</u>
<u>DL_TFC52</u>	<u>(TF1, TF0, TF0, TF6, TF1)</u>
<u>DL_TFC53</u>	<u>(TF2, TF1, TF1, TF6, TF1)</u>
<u>DL_TFC54</u>	<u>(TF0, TF0, TF0, TF7, TF1)</u>
<u>DL_TFC55</u>	<u>(TF1, TF0, TF0, TF7, TF1)</u>
<u>DL_TFC56</u>	<u>(TF2, TF1, TF1, TF7, TF1)</u>
<u>DL_TFC57</u>	<u>(TF0, TF0, TF0, TF8, TF1)</u>
<u>DL_TFC58</u>	<u>(TF1, TF0, TF0, TF8, TF1)</u>
<u>DL_TFC59</u>	<u>(TF2, TF1, TF1, TF8, TF1)</u>
<u>DL_TFC60</u>	<u>(TF0, TF0, TF0, TF9, TF1)</u>
<u>DL_TFC61</u>	<u>(TF1, TF0, TF0, TF9, TF1)</u>
<u>DL_TFC62</u>	<u>(TF2, TF1, TF1, TF9, TF1)</u>
<u>DL_TFC63</u>	<u>(TF0, TF0, TF0, TF10, TF1)</u>
<u>DL_TFC64</u>	<u>(TF1, TF0, TF0, TF10, TF1)</u>
<u>DL_TFC65</u>	<u>(TF2, TF1, TF1, TF10, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
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<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	DL_TFC1 , DL_TFC34	UL_TFC1 , UL_TFC16	DL_TFC0 , DL_TFC33 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC15 , UL_TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 152	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL_TFC2 , DL_TFC35	UL_TFC2 , UL_TFC17	DL_TFC0 , DL_TFC33 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC2 , UL_TFC15 , UL_TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 152	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL_TFC3 , DL_TFC36	UL_TFC3 , UL_TFC18	DL_TFC0 , DL_TFC33 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC3 , UL_TFC15 , UL_TFC18	RB5: 39 RB6: 103 RB7: 60 RB8: 152	RB5: No data RB6: No data RB7: No data RB8: 632
4	DL_TFC4 , DL_TFC37	UL_TFC4 , UL_TFC19	DL_TFC0 , DL_TFC33 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC4 , UL_TFC15 , UL_TFC16 , UL_TFC18 , UL_TFC19	RB5: 39 RB6: 103 RB7: 60 RB8: 152	RB5: 39 RB6: No data RB7: No data RB8: 632
5	DL_TFC5 , DL_TFC38	UL_TFC5 , UL_TFC20	DL_TFC0 , DL_TFC33 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC2 , UL_TFC3 , UL_TFC5 , UL_TFC15 , UL_TFC17 , UL_TFC18 , UL_TFC20	RB5: 81 RB6: 103 RB7: 60 RB8: 152	RB5: 81 RB6: 103 RB7: 60 RB8: 632
6	DL_TFC6 , DL_TFC39	UL_TFC6 , UL_TFC21	DL_TFC0 , DL_TFC33 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC6 , UL_TFC15 , UL_TFC21	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: No data RB6: No data RB7: No data RB8: 1272
7	DL_TFC7 , DL_TFC40	UL_TFC7 , UL_TFC22	DL_TFC0 , DL_TFC33 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC6 , UL_TFC7 , UL_TFC15 , UL_TFC16 , UL_TFC21 , UL_TFC22	RB5: 39 RB6: 103 RB7: 60 RB8: 312	RB5: 39 RB6: No data RB7: No data RB8: 1272
8	DL_TFC8 , DL_TFC41	UL_TFC8 , UL_TFC23	DL_TFC0 , DL_TFC33 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC2 , UL_TFC6 , UL_TFC8 , UL_TFC15 , UL_TFC17 , UL_TFC21 , UL_TFC23	RB5: 81 RB6: 103 RB7: 60 RB8: 312	RB5: 81 RB6: 103 RB7: 60 RB8: 1272
9	DL_TFC9 , DL_TFC42	UL_TFC9 , UL_TFC24	DL_TFC0 , DL_TFC33 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC9 , UL_TFC15 , UL_TFC24	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: No data RB6: No data RB7: No data RB8: 2552
10	DL_TFC10 , DL_TFC43	UL_TFC10 , UL_TFC25	DL_TFC0 , DL_TFC33 , UL_TFC0 , UL_TFC15	UL_TFC0 , UL_TFC1 , UL_TFC9 , UL_TFC10 , UL_TFC15 , UL_TFC16 , UL_TFC24 , UL_TFC25	RB5: 39 RB6: 103 RB7: 60 RB8: 632	RB5: 39 RB6: No data RB7: No data RB8: 2552

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
11	DL TFC11 , DL TFC44	UL TFC11 , UL TFC26	DL TFC0 , DL TFC33 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC9 , UL TFC11 , UL TFC15 , UL TFC17 , UL TFC24 , UL TFC26	RB5: 81 RB6: 103 RB7: 60 RB8: 632	RB5: 81 RB6: 103 RB7: 60 RB8: 2552
12	DL TFC12 , DL TFC45	UL TFC12 , UL TFC27	DL TFC0 , DL TFC33 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 5112
13	DL TFC13 , DL TFC46	UL TFC13 , UL TFC28	DL TFC0 , DL TFC33 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 5112
14	DL TFC14 , DL TFC47	UL TFC14 , UL TFC29	DL TFC0 , DL TFC33 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 5112
15	DL TFC15 , DL TFC48	UL TFC12 , UL TFC27	DL TFC0 , DL TFC33 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 7672
16	DL TFC16 , DL TFC49	UL TFC13 , UL TFC28	DL TFC0 , DL TFC33 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 7672
17	DL TFC17 , DL TFC50	UL TFC14 , UL TFC29	DL TFC0 , DL TFC33 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 7672
18	DL TFC18 , DL TFC51	UL TFC12 , UL TFC27	DL TFC0 , DL TFC33 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 10232
19	DL TFC19 , DL TFC52	UL TFC13 , UL TFC28	DL TFC0 , DL TFC33 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 10232

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
20	DL TFC20, DL TFC53	UL TFC14, UL TFC29	DL TFC0, DL TFC33, UL TFC0, UL TFC15	UL TFC0, UL TFC2, UL TFC12, UL TFC14, UL TFC15, UL TFC17, UL TFC27, UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 10232
21	DL TFC21, DL TFC54	UL TFC12, UL TFC27	DL TFC0, DL TFC33, UL TFC0, UL TFC15	UL TFC0, UL TFC12, UL TFC15, UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 12792
22	DL TFC22, DL TFC55	UL TFC13, UL TFC28	DL TFC0, DL TFC33, UL TFC0, UL TFC15	UL TFC0, UL TFC1, UL TFC12, UL TFC13, UL TFC15, UL TFC16, UL TFC27, UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 12792
23	DL TFC23, DL TFC56	UL TFC14, UL TFC29	DL TFC0, DL TFC33, UL TFC0, UL TFC15	UL TFC0, UL TFC2, UL TFC12, UL TFC14, UL TFC15, UL TFC17, UL TFC27, UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 12792
24	DL TFC24, DL TFC57	UL TFC12, UL TFC27	DL TFC0, DL TFC33, UL TFC0, UL TFC15	UL TFC0, UL TFC12, UL TFC15, UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 15352
25	DL TFC25, DL TFC58	UL TFC13, UL TFC28	DL TFC0, DL TFC33, UL TFC0, UL TFC15	UL TFC0, UL TFC1, UL TFC12, UL TFC13, UL TFC15, UL TFC16, UL TFC27, UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 15352
26	DL TFC26, DL TFC59	UL TFC14, UL TFC29	DL TFC0, DL TFC33, UL TFC0, UL TFC15	UL TFC0, UL TFC2, UL TFC12, UL TFC14, UL TFC15, UL TFC17, UL TFC27, UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 1272	RB5: 81 RB6: 103 RB7: 60 RB8: 15352
27	DL TFC27, DL TFC60	UL TFC12, UL TFC27	DL TFC0, DL TFC33, UL TFC0, UL TFC15	UL TFC0, UL TFC12, UL TFC15, UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: No data RB6: No data RB7: No data RB8: 17912
28	DL TFC28, DL TFC61	UL TFC13, UL TFC28	DL TFC0, DL TFC33, UL TFC0, UL TFC15	UL TFC0, UL TFC1, UL TFC12, UL TFC13, UL TFC15, UL TFC16, UL TFC27, UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 1272	RB5: 39 RB6: No data RB7: No data RB8: 17912

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
<u>29</u>	<u>DL TFC29,</u> <u>DL TFC62</u>	<u>UL TFC14,</u> <u>UL TFC29</u>	<u>DL TFC0,</u> <u>DL TFC33,</u> <u>UL TFC0,</u> <u>UL TFC15</u>	<u>UL TFC0,</u> <u>UL TFC2,</u> <u>UL TFC12,</u> <u>UL TFC14,</u> <u>UL TFC15,</u> <u>UL TFC17,</u> <u>UL TFC27,</u> <u>UL TFC29</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 1272</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 17912</u>
<u>30</u>	<u>DL TFC30,</u> <u>DL TFC63</u>	<u>UL TFC12,</u> <u>UL TFC27</u>	<u>DL TFC0,</u> <u>DL TFC33,</u> <u>UL TFC0,</u> <u>UL TFC15</u>	<u>UL TFC0,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC27</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 1272</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 20472</u>
<u>31</u>	<u>DL TFC31,</u> <u>DL TFC64</u>	<u>UL TFC13,</u> <u>UL TFC28</u>	<u>DL TFC0,</u> <u>DL TFC33,</u> <u>UL TFC0,</u> <u>UL TFC15</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC12,</u> <u>UL TFC13,</u> <u>UL TFC15,</u> <u>UL TFC16,</u> <u>UL TFC27,</u> <u>UL TFC28</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 1272</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 20472</u>
<u>32</u>	<u>DL TFC32,</u> <u>DL TFC65</u>	<u>UL TFC14,</u> <u>UL TFC29</u>	<u>DL TFC0,</u> <u>DL TFC33,</u> <u>UL TFC0,</u> <u>UL TFC15</u>	<u>UL TFC0,</u> <u>UL TFC2,</u> <u>UL TFC12,</u> <u>UL TFC14,</u> <u>UL TFC15,</u> <u>UL TFC17,</u> <u>UL TFC27,</u> <u>UL TFC29</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 1272</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 20472</u>
<u>NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB8: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the uplink TTI for RB8 is 20 ms while the downlink TTI is 10 ms then, to achieve continous data transmission in uplink the size of the uplink RLC SDU has been set such that it will be transmitted over each TTI, ie the uplink TFS size minus 8 bits (size of 7 bit length indicator and expansion bit) divided by 2 (20ms/10ms).</u>						

18.2.2.44.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
 - for sub-test 3: 2 RLC SDUs on RB8 equal to the content of the first 152 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4: 2 RLC SDUs on RB8 equal to the content of the first 152 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 5: 2 RLC SDUs on RB8 equal to the content of the first 152 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.

- for sub-test 26: 2 RLC SDUs on RB8 equal to the content of the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 27: 2 RLC SDUs on RB8 equal to the content of the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 28: 2 RLC SDUs on RB8 equal to the content of the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 29: 2 RLC SDUs on RB8 equal to the content of the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 30: 2 RLC SDUs on RB8 equal to the content of the first 1272 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 31: 2 RLC SDUs on RB8 equal to the content of the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 32: 2 RLC SDUs on RB8 equal to the content of the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.44.2 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Interactive or background / UL:128 DL:2048 kbps / PS RAB / Payload 128, 20 ms TTI

18.2.2.44.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.44.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.44 for the uplink payload 128, and the downlink 20 ms TTI case.

18.2.2.44.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (RAB subflow #1)</u>	<u>RB6 (RAB subflow #2)</u>	<u>RB7 (RAB subflow #3)</u>	<u>RB8 (128 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>7x144</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>14x144</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>20x144</u>	<u>N/A</u>

Uplink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
UL_TFC0	(TF0, TF0, TF0, TF0, TF0)
UL_TFC1	(TF1, TF0, TF0, TF0, TF0)
UL_TFC2	(TF2, TF1, TF1, TF0, TF0)
UL_TFC3	(TF0, TF0, TF0, TF1, TF0)
UL_TFC4	(TF1, TF0, TF0, TF1, TF0)
UL_TFC5	(TF2, TF1, TF1, TF1, TF0)
UL_TFC6	(TF0, TF0, TF0, TF2, TF0)
UL_TFC7	(TF1, TF0, TF0, TF2, TF0)
UL_TFC8	(TF2, TF1, TF1, TF2, TF0)
UL_TFC9	(TF0, TF0, TF0, TF3, TF0)
UL_TFC10	(TF1, TF0, TF0, TF3, TF0)
UL_TFC11	(TF2, TF1, TF1, TF3, TF0)
UL_TFC12	(TF0, TF0, TF0, TF4, TF0)
UL_TFC13	(TF1, TF0, TF0, TF4, TF0)
UL_TFC14	(TF2, TF1, TF1, TF4, TF0)
UL_TFC15	(TF0, TF0, TF0, TF0, TF1)
UL_TFC16	(TF1, TF0, TF0, TF0, TF1)
UL_TFC17	(TF2, TF1, TF1, TF0, TF1)
UL_TFC18	(TF0, TF0, TF0, TF1, TF1)
UL_TFC19	(TF1, TF0, TF0, TF1, TF1)
UL_TFC20	(TF2, TF1, TF1, TF1, TF1)
UL_TFC21	(TF0, TF0, TF0, TF2, TF1)
UL_TFC22	(TF1, TF0, TF0, TF2, TF1)
UL_TFC23	(TF2, TF1, TF1, TF2, TF1)
UL_TFC24	(TF0, TF0, TF0, TF3, TF1)
UL_TFC25	(TF1, TF0, TF0, TF3, TF1)
UL_TFC26	(TF2, TF1, TF1, TF3, TF1)
UL_TFC27	(TF0, TF0, TF0, TF4, TF1)
UL_TFC28	(TF1, TF0, TF0, TF4, TF1)
UL_TFC29	(TF2, TF1, TF1, TF4, TF1)

Downlink TFS:

		RB5 (RAB subflow #1)	RB6 (RAB subflow #2)	RB7 (RAB subflow #3)	RB8 (2048 kbps)	DCCH
TFS	TF0, bits	1x0	0x103	0x60	0x656	0x148
	TF1, bits	1x39	1x103	1x60	1x656	1x148
	TF2, bits	1x81	N/A	N/A	2x656	N/A
	TF3, bits	N/A	N/A	N/A	4x656	N/A
	TF4, bits	N/A	N/A	N/A	8x656	N/A
	TF5, bits	N/A	N/A	N/A	12x656	N/A
	TF6, bits	N/A	N/A	N/A	16x656	N/A
	TF7, bits	N/A	N/A	N/A	20x656	N/A
	TF8, bits	N/A	N/A	N/A	24x656	N/A
	TF9, bits	N/A	N/A	N/A	28x656	N/A
	TF10, bits	N/A	N/A	N/A	32x656	N/A
	TF11, bits	N/A	N/A	N/A	36x656	N/A
	TF12, bits	N/A	N/A	N/A	40x656	N/A
	TF13, bits	N/A	N/A	N/A	44x656	N/A
	TF14, bits	N/A	N/A	N/A	48x656	N/A
	TF15, bits	N/A	N/A	N/A	52x656	N/A
	TF16, bits	N/A	N/A	N/A	56x656	N/A
	TF17, bits	N/A	N/A	N/A	60x656	N/A
TF18, bits	N/A	N/A	N/A	64x656	N/A	

Downlink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0, TF0, TF0, TF0, TF0)
DL_TFC1	(TF1, TF0, TF0, TF0, TF0)
DL_TFC2	(TF2, TF1, TF1, TF0, TF0)
DL_TFC3	(TF0, TF0, TF0, TF1, TF0)
DL_TFC4	(TF1, TF0, TF0, TF1, TF0)
DL_TFC5	(TF2, TF1, TF1, TF1, TF0)
DL_TFC6	(TF0, TF0, TF0, TF2, TF0)
DL_TFC7	(TF1, TF0, TF0, TF2, TF0)
DL_TFC8	(TF2, TF1, TF1, TF2, TF0)
DL_TFC9	(TF0, TF0, TF0, TF3, TF0)
DL_TFC10	(TF1, TF0, TF0, TF3, TF0)
DL_TFC11	(TF2, TF1, TF1, TF3, TF0)
DL_TFC12	(TF0, TF0, TF0, TF4, TF0)
DL_TFC13	(TF1, TF0, TF0, TF4, TF0)
DL_TFC14	(TF2, TF1, TF1, TF4, TF0)
DL_TFC15	(TF0, TF0, TF0, TF5, TF0)
DL_TFC16	(TF1, TF0, TF0, TF5, TF0)
DL_TFC17	(TF2, TF1, TF1, TF5, TF0)
DL_TFC18	(TF0, TF0, TF0, TF6, TF0)
DL_TFC19	(TF1, TF0, TF0, TF6, TF0)
DL_TFC20	(TF2, TF1, TF1, TF6, TF0)
DL_TFC21	(TF0, TF0, TF0, TF7, TF0)
DL_TFC22	(TF1, TF0, TF0, TF7, TF0)
DL_TFC23	(TF2, TF1, TF1, TF7, TF0)
DL_TFC24	(TF0, TF0, TF0, TF8, TF0)
DL_TFC25	(TF1, TF0, TF0, TF8, TF0)
DL_TFC26	(TF2, TF1, TF1, TF8, TF0)
DL_TFC27	(TF0, TF0, TF0, TF9, TF0)
DL_TFC28	(TF1, TF0, TF0, TF9, TF0)
DL_TFC29	(TF2, TF1, TF1, TF9, TF0)
DL_TFC30	(TF0, TF0, TF0, TF10, TF0)
DL_TFC31	(TF1, TF0, TF0, TF10, TF0)
DL_TFC32	(TF2, TF1, TF1, TF10, TF0)
DL_TFC33	(TF0, TF0, TF0, TF11, TF0)
DL_TFC34	(TF1, TF0, TF0, TF11, TF0)
DL_TFC35	(TF2, TF1, TF1, TF11, TF0)
DL_TFC36	(TF0, TF0, TF0, TF12, TF0)
DL_TFC37	(TF1, TF0, TF0, TF12, TF0)
DL_TFC38	(TF2, TF1, TF1, TF12, TF0)
DL_TFC39	(TF0, TF0, TF0, TF13, TF0)
DL_TFC40	(TF1, TF0, TF0, TF13, TF0)
DL_TFC41	(TF2, TF1, TF1, TF13, TF0)
DL_TFC42	(TF0, TF0, TF0, TF14, TF0)
DL_TFC43	(TF1, TF0, TF0, TF14, TF0)
DL_TFC44	(TF2, TF1, TF1, TF14, TF0)
DL_TFC45	(TF0, TF0, TF0, TF15, TF0)
DL_TFC46	(TF1, TF0, TF0, TF15, TF0)
DL_TFC47	(TF2, TF1, TF1, TF15, TF0)
DL_TFC48	(TF0, TF0, TF0, TF16, TF0)
DL_TFC49	(TF1, TF0, TF0, TF16, TF0)
DL_TFC50	(TF2, TF1, TF1, TF16, TF0)
DL_TFC51	(TF0, TF0, TF0, TF17, TF0)
DL_TFC52	(TF1, TF0, TF0, TF17, TF0)
DL_TFC53	(TF2, TF1, TF1, TF17, TF0)
DL_TFC54	(TF0, TF0, TF0, TF18, TF0)
DL_TFC55	(TF1, TF0, TF0, TF18, TF0)
DL_TFC56	(TF2, TF1, TF1, TF18, TF0)
DL_TFC57	(TF0, TF0, TF0, TF0, TF1)
DL_TFC58	(TF1, TF0, TF0, TF0, TF1)
DL_TFC59	(TF2, TF1, TF1, TF0, TF1)
DL_TFC60	(TF0, TF0, TF0, TF1, TF1)

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC61</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC61</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>DL_TFC63</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC64</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>DL_TFC65</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>
<u>DL_TFC66</u>	<u>(TF0, TF0, TF0, TF3, TF1)</u>
<u>DL_TFC67</u>	<u>(TF1, TF0, TF0, TF3, TF1)</u>
<u>DL_TFC68</u>	<u>(TF2, TF1, TF1, TF3, TF1)</u>
<u>DL_TFC69</u>	<u>(TF0, TF0, TF0, TF4, TF1)</u>
<u>DL_TFC70</u>	<u>(TF1, TF0, TF0, TF4, TF1)</u>
<u>DL_TFC71</u>	<u>(TF2, TF1, TF1, TF4, TF1)</u>
<u>DL_TFC72</u>	<u>(TF0, TF0, TF0, TF5, TF1)</u>
<u>DL_TFC73</u>	<u>(TF1, TF0, TF0, TF5, TF1)</u>
<u>DL_TFC74</u>	<u>(TF2, TF1, TF1, TF5, TF1)</u>
<u>DL_TFC75</u>	<u>(TF0, TF0, TF0, TF6, TF1)</u>
<u>DL_TFC76</u>	<u>(TF1, TF0, TF0, TF6, TF1)</u>
<u>DL_TFC77</u>	<u>(TF2, TF1, TF1, TF6, TF1)</u>
<u>DL_TFC78</u>	<u>(TF0, TF0, TF0, TF7, TF1)</u>
<u>DL_TFC79</u>	<u>(TF1, TF0, TF0, TF7, TF1)</u>
<u>DL_TFC80</u>	<u>(TF2, TF1, TF1, TF7, TF1)</u>
<u>DL_TFC81</u>	<u>(TF0, TF0, TF0, TF8, TF1)</u>
<u>DL_TFC82</u>	<u>(TF1, TF0, TF0, TF8, TF1)</u>
<u>DL_TFC83</u>	<u>(TF2, TF1, TF1, TF8, TF1)</u>
<u>DL_TFC84</u>	<u>(TF0, TF0, TF0, TF9, TF1)</u>
<u>DL_TFC85</u>	<u>(TF1, TF0, TF0, TF9, TF1)</u>
<u>DL_TFC86</u>	<u>(TF2, TF1, TF1, TF9, TF1)</u>
<u>DL_TFC87</u>	<u>(TF0, TF0, TF0, TF10, TF1)</u>
<u>DL_TFC88</u>	<u>(TF1, TF0, TF0, TF10, TF1)</u>
<u>DL_TFC89</u>	<u>(TF2, TF1, TF1, TF10, TF1)</u>
<u>DL_TFC90</u>	<u>(TF0, TF0, TF0, TF11, TF1)</u>
<u>DL_TFC91</u>	<u>(TF1, TF0, TF0, TF11, TF1)</u>
<u>DL_TFC92</u>	<u>(TF2, TF1, TF1, TF11, TF1)</u>
<u>DL_TFC93</u>	<u>(TF0, TF0, TF0, TF12, TF1)</u>
<u>DL_TFC94</u>	<u>(TF1, TF0, TF0, TF12, TF1)</u>
<u>DL_TFC95</u>	<u>(TF2, TF1, TF1, TF12, TF1)</u>
<u>DL_TFC96</u>	<u>(TF0, TF0, TF0, TF13, TF1)</u>
<u>DL_TFC97</u>	<u>(TF1, TF0, TF0, TF13, TF1)</u>
<u>DL_TFC98</u>	<u>(TF2, TF1, TF1, TF13, TF1)</u>
<u>DL_TFC99</u>	<u>(TF0, TF0, TF0, TF14, TF1)</u>
<u>DL_TFC100</u>	<u>(TF1, TF0, TF0, TF14, TF1)</u>
<u>DL_TFC101</u>	<u>(TF2, TF1, TF1, TF14, TF1)</u>
<u>DL_TFC102</u>	<u>(TF0, TF0, TF0, TF15, TF1)</u>
<u>DL_TFC103</u>	<u>(TF1, TF0, TF0, TF15, TF1)</u>
<u>DL_TFC104</u>	<u>(TF2, TF1, TF1, TF15, TF1)</u>
<u>DL_TFC105</u>	<u>(TF0, TF0, TF0, TF16, TF1)</u>
<u>DL_TFC106</u>	<u>(TF1, TF0, TF0, TF16, TF1)</u>
<u>DL_TFC107</u>	<u>(TF2, TF1, TF1, TF16, TF1)</u>
<u>DL_TFC108</u>	<u>(TF0, TF0, TF0, TF17, TF1)</u>
<u>DL_TFC109</u>	<u>(TF1, TF0, TF0, TF17, TF1)</u>
<u>DL_TFC110</u>	<u>(TF2, TF1, TF1, TF17, TF1)</u>
<u>DL_TFC111</u>	<u>(TF0, TF0, TF0, TF18, TF1)</u>
<u>DL_TFC112</u>	<u>(TF1, TF0, TF0, TF18, TF1)</u>
<u>DL_TFC113</u>	<u>(TF2, TF1, TF1, TF18, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	DL TFC1 , DL TFC58	UL TFC1 , UL TFC16	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC15 , UL TFC16	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL TFC2 , DL TFC59	UL TFC2 , UL TFC17	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC15 , UL TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: 81 RB6: 103 RB7: 60 RB8: No data
3	DL TFC3 , DL TFC60	UL TFC3 , UL TFC18	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC3 , UL TFC15 , UL TFC18	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: No data RB6: No data RB7: No data RB8: 632
4	DL TFC4 , DL TFC61	UL TFC4 , UL TFC19	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC3 , UL TFC4 , UL TFC15 , UL TFC16 , UL TFC18 , UL TFC19	RB5: 39 RB6: 103 RB7: 60 RB8: 120	RB5: 39 RB6: No data RB7: No data RB8: 632
5	DL TFC5 , DL TFC62	UL TFC5 , UL TFC20	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC3 , UL TFC5 , UL TFC15 , UL TFC17 , UL TFC18 , UL TFC20	RB5: 81 RB6: 103 RB7: 60 RB8: 120	RB5: 81 RB6: 103 RB7: 60 RB8: 632
6	DL TFC6 , DL TFC63	UL TFC6 , UL TFC21	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC6 , UL TFC15 , UL TFC21	RB5: 39 RB6: 103 RB7: 60 RB8: 888	RB5: No data RB6: No data RB7: No data RB8: 1272
7	DL TFC7 , DL TFC64	UL TFC7 , UL TFC22	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC6 , UL TFC7 , UL TFC15 , UL TFC16 , UL TFC21 , UL TFC22	RB5: 39 RB6: 103 RB7: 60 RB8: 888	RB5: 39 RB6: No data RB7: No data RB8: 1272
8	DL TFC8 , DL TFC65	UL TFC8 , UL TFC23	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC6 , UL TFC8 , UL TFC15 , UL TFC17 , UL TFC21 , UL TFC23	RB5: 81 RB6: 103 RB7: 60 RB8: 888	RB5: 81 RB6: 103 RB7: 60 RB8: 1272
9	DL TFC9 , DL TFC66	UL TFC9 , UL TFC24	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC9 , UL TFC15 , UL TFC24	RB5: 39 RB6: 103 RB7: 60 RB8: 1784	RB5: No data RB6: No data RB7: No data RB8: 2552
10	DL TFC10 , DL TFC67	UL TFC10 , UL TFC25	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC9 , UL TFC10 , UL TFC15 , UL TFC16 , UL TFC24 , UL TFC25	RB5: 39 RB6: 103 RB7: 60 RB8: 1784	RB5: 39 RB6: No data RB7: No data RB8: 2552

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
11	DL TFC11 , DL TFC68	UL TFC11 , UL TFC26	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC9 , UL TFC11 , UL TFC15 , UL TFC17 , UL TFC24 , UL TFC26	RB5: 81 RB6: 103 RB7: 60 RB8: 1784	RB5: 81 RB6: 103 RB7: 60 RB8: 2552
12	DL TFC12 , DL TFC69	UL TFC12 , UL TFC27	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 5112
13	DL TFC13 , DL TFC70	UL TFC13 , UL TFC28	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 5112
14	DL TFC14 , DL TFC71	UL TFC14 , UL TFC29	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 5112
15	DL TFC15 , DL TFC72	UL TFC12 , UL TFC27	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 7672
16	DL TFC16 , DL TFC73	UL TFC13 , UL TFC28	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 7672
17	DL TFC17 , DL TFC74	UL TFC14 , UL TFC29	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 7672
18	DL TFC18 , DL TFC75	UL TFC12 , UL TFC27	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 10232
19	DL TFC19 , DL TFC76	UL TFC13 , UL TFC28	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 10232

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
20	DL TFC20, DL TFC77	UL TFC14, UL TFC29	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC2, UL TFC12, UL TFC14, UL TFC15, UL TFC17, UL TFC27, UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 10232
21	DL TFC21, DL TFC78	UL TFC12, UL TFC27	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC12, UL TFC15, UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 12792
22	DL TFC22, DL TFC79	UL TFC13, UL TFC28	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC1, UL TFC12, UL TFC13, UL TFC15, UL TFC16, UL TFC27, UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 12792
23	DL TFC23, DL TFC80	UL TFC14, UL TFC29	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC2, UL TFC12, UL TFC14, UL TFC15, UL TFC17, UL TFC27, UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 12792
24	DL TFC24, DL TFC81	UL TFC12, UL TFC27	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC12, UL TFC15, UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 15352
25	DL TFC25, DL TFC82	UL TFC13, UL TFC28	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC1, UL TFC12, UL TFC13, UL TFC15, UL TFC16, UL TFC27, UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 15352
26	DL TFC26, DL TFC83	UL TFC14, UL TFC29	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC2, UL TFC12, UL TFC14, UL TFC15, UL TFC17, UL TFC27, UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 15352
27	DL TFC27, DL TFC84	UL TFC12, UL TFC27	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC12, UL TFC15, UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 17912
28	DL TFC28, DL TFC85	UL TFC13, UL TFC28	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC1, UL TFC12, UL TFC13, UL TFC15, UL TFC16, UL TFC27, UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 17912

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
29	DL TFC29, DL TFC86	UL TFC14, UL TFC29	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC2, UL TFC12, UL TFC14, UL TFC15, UL TFC17, UL TFC27, UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 17912
30	DL TFC30, DL TFC87	UL TFC12, UL TFC27	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC12, UL TFC15, UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 20472
31	DL TFC31, DL TFC88	UL TFC13, UL TFC28	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC1, UL TFC12, UL TFC13, UL TFC15, UL TFC16, UL TFC27, UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 20472
32	DL TFC32, DL TFC89	UL TFC14, UL TFC29	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC2, UL TFC12, UL TFC14, UL TFC15, UL TFC17, UL TFC27, UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 20472
33	DL TFC33, DL TFC90	UL TFC12, UL TFC27	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC12, UL TFC15, UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 23032
34	DL TFC34, DL TFC91	UL TFC13, UL TFC28	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC1, UL TFC12, UL TFC13, UL TFC15, UL TFC16, UL TFC27, UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 23032
35	DL TFC35, DL TFC92	UL TFC14, UL TFC29	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC2, UL TFC12, UL TFC14, UL TFC15, UL TFC17, UL TFC27, UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 23032
36	DL TFC36, DL TFC93	UL TFC12, UL TFC27	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC12, UL TFC15, UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 25592
37	DL TFC37, DL TFC94	UL TFC13, UL TFC28	DL TFC0, DL TFC57, UL TFC0, UL TFC15	UL TFC0, UL TFC1, UL TFC12, UL TFC13, UL TFC15, UL TFC16, UL TFC27, UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 25592

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
38	DL TFC38 , DL TFC95	UL TFC14 , UL TFC29	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 25592
39	DL TFC39 , DL TFC96	UL TFC12 , UL TFC27	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 28152
40	DL TFC40 , DL TFC97	UL TFC13 , UL TFC28	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 28152
41	DL TFC41 , DL TFC98	UL TFC14 , UL TFC29	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 28152
42	DL TFC42 , DL TFC99	UL TFC12 , UL TFC27	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 30712
43	DL TFC43 , DL TFC100	UL TFC13 , UL TFC28	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 30712
44	DL TFC44 , DL TFC101	UL TFC14 , UL TFC29	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 30712
45	DL TFC45 , DL TFC102	UL TFC12 , UL TFC27	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 33272
46	DL TFC46 , DL TFC103	UL TFC13 , UL TFC28	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 33272

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
47	DL TFC47 , DL TFC104	UL TFC14 , UL TFC29	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 33272
48	DL TFC48 , DL TFC105	UL TFC12 , UL TFC27	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 35832
49	DL TFC49 , DL TFC106	UL TFC13 , UL TFC28	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 35832
50	DL TFC50 , DL TFC107	UL TFC14 , UL TFC29	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 35832
51	DL TFC51 , DL TFC108	UL TFC12 , UL TFC27	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 38392
52	DL TFC52 , DL TFC109	UL TFC13 , UL TFC28	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 38392
53	DL TFC53 , DL TFC110	UL TFC14 , UL TFC29	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 2552	RB5: 81 RB6: 103 RB7: 60 RB8: 38392
54	DL TFC54 , DL TFC111	UL TFC12 , UL TFC27	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC12 , UL TFC15 , UL TFC27	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: No data RB6: No data RB7: No data RB8: 40952
55	DL TFC55 , DL TFC112	UL TFC13 , UL TFC28	DL TFC0 , DL TFC57 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 2552	RB5: 39 RB6: No data RB7: No data RB8: 40952

<u>Sub-test</u>	<u>Downlink TFCs under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
56	<u>DL TFC56,</u> <u>DL TFC113</u>	<u>UL TFC14,</u> <u>UL TFC29</u>	<u>DL TFC0,</u> <u>DL TFC57,</u> <u>UL TFC0,</u> <u>UL TFC15</u>	<u>UL TFC0,</u> <u>UL TFC2,</u> <u>UL TFC12,</u> <u>UL TFC14,</u> <u>UL TFC15,</u> <u>UL TFC17,</u> <u>UL TFC27,</u> <u>UL TFC29</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 2552</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 40952</u>
<p><u>NOTE:</u> See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. <u>RB8: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB8 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.44.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15 the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
 - for sub-test 3: an RLC SDU on RB8 having the content equal to the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4: an RLC SDU on RB8 having the content equal to the first 120 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 5: an RLC SDU on RB8 having the content equal to the first 120 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
 - for sub-test 6: an RLC SDU on RB8 having the content equal to the first 888 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 7: an RLC SDU on RB8 having the content equal to the first 888 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 8: an RLC SDU on RB8 having the content equal to the first 888 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
 - for sub-test 9: an RLC SDU on RB8 having the content equal to the first 1784 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 10: an RLC SDU on RB8 having the content equal to the first 1784 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 11: an RLC SDU on RB8 having the content equal to the first 1784 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.

- for sub-test 52: an RLC SDU on RB8 having the content equal to the first 2552 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 53: an RLC SDU on RB8 having the content equal to the first 2552 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.
- for sub-test 54: an RLC SDU on RB8 having the content equal to the first 2552 bits of the test data sent by the SS in downlink; and no data shall be received on RB5, RB6 and RB7.
- for sub-test 55: an RLC SDU on RB8 having the content equal to the first 2552 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
- for sub-test 56: an RLC SDU on RB8 having the content equal to the first 2552 bits of the test data sent by the SS in downlink; an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.45 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Streaming / unknown / UL:57.6 DL:57.6 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH

18.2.2.45.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.45.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.45.

18.2.2.45.3 Method of test

See 18.2.1.2 for test procedure.

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Streaming / unknown / UL:57.6 DL:57.6 kbps / CS RAB (RB8):

<u>Uplink RLC</u>	
<u>TM RLC</u>	
<u>Transmission RLC discard CHOICE SDU</u>	
<u>Discard Mode</u>	
<u>Timer based</u>	
<u>no explicit</u>	<u>100ms</u>
<u>Timer discard</u>	
<u>Segmentation indication</u>	<u>FALSE</u>
<u>Downlink RLC</u>	
<u>TM RLC</u>	
<u>Segmentation indication</u>	<u>FALSE</u>
<u>NOTE: Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTI.</u>	

Uplink TFS:

	<u>TFI</u>	<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(57.6 kbps)</u>	<u>DCCH</u>
TFS	<u>TF0, bits</u>	0x81	0x103	0x60	0x576	0x148
	<u>TF1, bits</u>	1x39	1x103	1x60	1x576	1x148
	<u>TF2, bits</u>	1x81	N/A	N/A	2x576	N/A
	<u>TF3, bits</u>	N/A	N/A	N/A	3x576	N/A
	<u>TF4, bits</u>	N/A	N/A	N/A	4x576	N/A

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF1, TF2, TF0)</u>
<u>UL_TFC9</u>	<u>(TF0, TF0, TF0, TF3, TF0)</u>
<u>UL_TFC10</u>	<u>(TF1, TF0, TF0, TF3, TF0)</u>
<u>UL_TFC11</u>	<u>(TF2, TF1, TF1, TF3, TF0)</u>
<u>UL_TFC12</u>	<u>(TF0, TF0, TF0, TF4, TF0)</u>
<u>UL_TFC13</u>	<u>(TF1, TF0, TF0, TF4, TF0)</u>
<u>UL_TFC14</u>	<u>(TF2, TF1, TF1, TF4, TF0)</u>
<u>UL_TFC15</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC17</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>UL_TFC18</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC19</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC20</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>
<u>UL_TFC21</u>	<u>(TF0, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC22</u>	<u>(TF1, TF0, TF0, TF2, TF1)</u>
<u>UL_TFC23</u>	<u>(TF2, TF1, TF1, TF2, TF1)</u>
<u>UL_TFC24</u>	<u>(TF0, TF0, TF0, TF3, TF1)</u>
<u>UL_TFC25</u>	<u>(TF1, TF0, TF0, TF3, TF1)</u>
<u>UL_TFC26</u>	<u>(TF2, TF1, TF1, TF3, TF1)</u>
<u>UL_TFC27</u>	<u>(TF0, TF0, TF0, TF4, TF1)</u>
<u>UL_TFC28</u>	<u>(TF1, TF0, TF0, TF4, TF1)</u>
<u>UL_TFC29</u>	<u>(TF2, TF1, TF1, TF4, TF1)</u>

Downlink TFS:

		<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(57.6 kbps)</u>	<u>DCCH</u>
TFS	<u>TF0, bits</u>	1x0	0x103	0x60	0x576	0x148
	<u>TF1, bits</u>	1x39	1x103	1x60	1x576	1x148
	<u>TF2, bits</u>	1x81	N/A	N/A	2x576	N/A
	<u>TF3, bits</u>	N/A	N/A	N/A	3x576	N/A
	<u>TF4, bits</u>	N/A	N/A	N/A	4x576	N/A

Downlink TFCS:

TFCI	(RB5, RB6, RB7, RB8, DCCH)
DL_TFC0	(TF0, TF0, TF0, TF0, TF0)
DL_TFC1	(TF1, TF0, TF0, TF0, TF0)
DL_TFC2	(TF2, TF1, TF1, TF0, TF0)
DL_TFC3	(TF0, TF0, TF0, TF1, TF0)
DL_TFC4	(TF1, TF0, TF0, TF1, TF0)
DL_TFC5	(TF2, TF1, TF1, TF1, TF0)
DL_TFC6	(TF0, TF0, TF0, TF2, TF0)
DL_TFC7	(TF1, TF0, TF0, TF2, TF0)
DL_TFC8	(TF2, TF1, TF1, TF2, TF0)
DL_TFC9	(TF0, TF0, TF0, TF3, TF0)
DL_TFC10	(TF1, TF0, TF0, TF3, TF0)
DL_TFC11	(TF2, TF1, TF1, TF3, TF0)
DL_TFC12	(TF0, TF0, TF0, TF4, TF0)
DL_TFC13	(TF1, TF0, TF0, TF4, TF0)
DL_TFC14	(TF2, TF1, TF1, TF4, TF0)
DL_TFC15	(TF0, TF0, TF0, TF0, TF1)
DL_TFC16	(TF1, TF0, TF0, TF0, TF1)
DL_TFC17	(TF2, TF1, TF1, TF0, TF1)
DL_TFC18	(TF0, TF0, TF0, TF1, TF1)
DL_TFC19	(TF1, TF0, TF0, TF1, TF1)
DL_TFC20	(TF2, TF1, TF1, TF1, TF1)
DL_TFC21	(TF0, TF0, TF0, TF2, TF1)
DL_TFC22	(TF1, TF0, TF0, TF2, TF1)
DL_TFC23	(TF2, TF1, TF1, TF2, TF1)
DL_TFC24	(TF0, TF0, TF0, TF3, TF1)
DL_TFC25	(TF1, TF0, TF0, TF3, TF1)
DL_TFC26	(TF2, TF1, TF1, TF3, TF1)
DL_TFC27	(TF0, TF0, TF0, TF4, TF1)
DL_TFC28	(TF1, TF0, TF0, TF4, TF1)
DL_TFC29	(TF2, TF1, TF1, TF4, TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
1	<u>DL_TFC1,</u> <u>DL_TFC16</u>	<u>UL_TFC1,</u> <u>DL_TFC16</u>	<u>DL_TFC0,</u> <u>DL_TFC15,</u> <u>UL_TFC0,</u> <u>UL_TFC15</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC15,</u> <u>UL_TFC16</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 576</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
2	<u>DL_TFC2,</u> <u>DL_TFC17</u>	<u>UL_TFC2,</u> <u>DL_TFC17</u>	<u>DL_TFC0,</u> <u>DL_TFC15,</u> <u>UL_TFC0,</u> <u>UL_TFC15</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC15,</u> <u>UL_TFC17</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 576</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: No data</u>
3	<u>DL_TFC3,</u> <u>DL_TFC18</u>	<u>UL_TFC3,U</u> <u>L_TFC18</u>	<u>DL_TFC0,</u> <u>DL_TFC15,</u> <u>UL_TFC0,</u> <u>UL_TFC15</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC15,</u> <u>UL_TFC18</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 576</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 576</u>
4	<u>DL_TFC4,</u> <u>DL_TFC19</u>	<u>UL_TFC4,</u> <u>DL_TFC19</u>	<u>DL_TFC0,</u> <u>DL_TFC15,</u> <u>UL_TFC0,</u> <u>UL_TFC15</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3</u> <u>UL_TFC4,</u> <u>UL_TFC15,</u> <u>UL_TFC16,</u> <u>UL_TFC18,</u> <u>UL_TFC19</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 576</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 576</u>
5	<u>DL_TFC5,</u> <u>DL_TFC20</u>	<u>UL_TFC5,</u> <u>DL_TFC20</u>	<u>DL_TFC0,</u> <u>DL_TFC15,</u> <u>UL_TFC0,</u> <u>UL_TFC15</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC5,</u> <u>UL_TFC15,</u> <u>UL_TFC17,</u> <u>UL_TFC18,</u> <u>UL_TFC20</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 576</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 576</u>
6	<u>DL_TFC6,</u> <u>DL_TFC21</u>	<u>UL_TFC6,</u> <u>DL_TFC21</u>	<u>DL_TFC0,</u> <u>DL_TFC15,</u> <u>UL_TFC0,</u> <u>UL_TFC15</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC15,</u> <u>UL_TFC21</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 576</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 1152</u>
7	<u>DL_TFC7,</u> <u>DL_TFC22</u>	<u>UL_TFC7,</u> <u>DL_TFC22</u>	<u>DL_TFC0,</u> <u>DL_TFC15,</u> <u>UL_TFC0,</u> <u>UL_TFC15</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC15,</u> <u>UL_TFC16,</u> <u>UL_TFC21,</u> <u>UL_TFC22</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 576</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 2x576</u>

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
8	DL TFC8 , DL TFC23	UL TFC8 , DL TFC23	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC2 , UL TFC3 , UL TFC6 , UL TFC8 , UL TFC15 , UL TFC17 , UL TFC21 , UL TFC23	RB5: 81 RB6: 103 RB7: 60 RB8: 576	RB5: 81 RB6: 103 RB7: 60 RB8: 2x576
9	DL TFC9 , DL TFC24	UL TFC9 , DL TFC24	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC2 , UL TFC3 , UL TFC9 , UL TFC15 , UL TFC24	RB5: 39 RB6: 103 RB7: 60 RB8: 576	RB5: No data RB6: No data RB7: No data RB8: 3x576
10	DL TFC10 , DL TFC25	UL TFC10 , UL TFC25	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC2 , UL TFC3 , UL TFC9 , UL TFC10 , UL TFC15 , UL TFC16 , UL TFC24 , UL TFC25	RB5: 39 RB6: 103 RB7: 60 RB8: 576	RB5: 39 RB6: No data RB7: No data RB8: 3x576
11	DL TFC11 , DL TFC26	UL TFC11 , DL TFC26	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC2 , UL TFC3 , UL TFC9 , UL TFC11 , UL TFC15 , UL TFC17 , UL TFC24 , UL TFC26	RB5: 81 RB6: 103 RB7: 60 RB8: 576	RB5: 81 RB6: 103 RB7: 60 RB8: 3x576
12	DL TFC12 , DL TFC27	UL TFC12 , DL TFC27	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC2 , UL TFC3 , UL TFC12 , UL TFC15 , UL TFC26	RB5: 39 RB6: 103 RB7: 60 RB8: 576	RB5: No data RB6: No data RB7: No data RB8: 4x576
13	DL TFC13 , DL TFC28	UL TFC13 , DL TFC28	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC2 , UL TFC3 , UL TFC12 , UL TFC13 , UL TFC15 , UL TFC16 , UL TFC27 , UL TFC28	RB5: 39 RB6: 103 RB7: 60 RB8: 576	RB5: 39 RB6: No data RB7: No data RB8: 4x576
14	DL TFC14 , DL TFC29	UL TFC14 , DL TFC29	DL TFC0 , DL TFC15 , UL TFC0 , UL TFC15	UL TFC0 , UL TFC1 , UL TFC2 , UL TFC3 , UL TFC12 , UL TFC14 , UL TFC15 , UL TFC17 , UL TFC27 , UL TFC29	RB5: 81 RB6: 103 RB7: 60 RB8: 576	RB5: 81 RB6: 103 RB7: 60 RB8: 4x576

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
NOTE 1: UL TFC0, UL TFC1, UL TFC2, UL TFC3 and UL TFC15 are part of minimum set of TFCs.						
NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.						

18.2.2.45.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and 15b the UE transmitted transport format shall be within the set of restricted TFCs as specified in the actual sub test.
3. At step 15 the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
 - for sub-test 3, 6, 9 and 12: an RLC SDU on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4, 7, 10 and 13: an RLC SDU on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6 and RB7.
 - for sub-test 5, 8, 11 and 14: an RLC SDU on RB5, RB6, RB7 and RB8 having the same content as sent by SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.46 Void

18.2.2.47 Void

18.2.2.48 Void

18.2.2.49 Conversational / speech / UL:12.2 DL:12.2 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH

18.2.2.49.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.49.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.49.

18.2.2.49.3 Method of test

See 18.2.1.2 for test procedure.

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Conversational / unknown / UL:64 DL:64 kbps / CS RAB (RB8):

<u>Uplink RLC</u> <u>TM RLC</u> <u>Transmission RLC discard</u> <u>CHOICE SDU</u> <u>Discard Mode</u> <u>Timer based</u> <u>no explicit</u> <u>Timer_discard</u> <u>Segmentation indication</u>	 <u>100ms</u> <u>FALSE</u>
<u>Downlink RLC</u> <u>TM RLC</u> <u>Segmentation indication</u>	 <u>FALSE</u>
<u>NOTE:</u> <u>Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTI .</u>	

Uplink TFS:

	<u>TFI</u>	<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(64 kbps,</u> <u>20 ms TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x81</u>	<u>0x103</u>	<u>0x60</u>	<u>0x640</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>2x640</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>UL_TFC8</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>UL_TFC9</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC10</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>UL_TFC11</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>

Downlink TFS:

	<u>RB5</u> <u>(RAB subflow #1)</u>	<u>RB6</u> <u>(RAB subflow #2)</u>	<u>RB7</u> <u>(RAB subflow #3)</u>	<u>RB8</u> <u>(64 kbps,</u> <u>20 ms TTI)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>1x0</u>	<u>0x103</u>	<u>0x60</u>	<u>0x640</u>
	<u>TF1, bits</u>	<u>1x39</u>	<u>1x103</u>	<u>1x60</u>	<u>2x640</u>
	<u>TF2, bits</u>	<u>1x81</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, RB8, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF1, TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC5</u>	<u>(TF2, TF1, TF1, TF1, TF0)</u>
<u>DL_TFC6</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC7</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC8</u>	<u>(TF2, TF1, TF1, TF0, TF1)</u>
<u>DL_TFC9</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC10</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC11</u>	<u>(TF2, TF1, TF1, TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCS Under Test</u>	<u>Uplink TFCS Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u> <small>(note 1)</small>	<u>UL RLC SDU size (bits)</u> <small>(note 2)</small>	<u>Test data size (bits)</u> <small>(note 2)</small>
<u>1</u>	<u>DL_TFC1,</u> <u>DL_TFC7</u>	<u>UL_TFC1,</u> <u>UL_TFC7</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC7</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 640</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: No data</u>
<u>2</u>	<u>DL_TFC2,</u> <u>DL_TFC8</u>	<u>UL_TFC2,</u> <u>UL_TFC8</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC8</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 640</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: No data</u>
<u>3</u>	<u>DL_TFC3,</u> <u>DL_TFC9</u>	<u>UL_TFC3,</u> <u>UL_TFC9</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC9</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 640</u>	<u>RB5: No data</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 2x640</u>
<u>4</u>	<u>DL_TFC4,</u> <u>DL_TFC10</u>	<u>UL_TFC4,</u> <u>UL_TFC10</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC9,</u> <u>UL_TFC10</u>	<u>RB5: 39</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 640</u>	<u>RB5: 39</u> <u>RB6: No data</u> <u>RB7: No data</u> <u>RB8: 2x640</u>
<u>5</u>	<u>DL_TFC5,</u> <u>DL_TFC11</u>	<u>UL_TFC5,</u> <u>UL_TFC11</u>	<u>DL_TFC0,</u> <u>DL_TFC6,</u> <u>UL_TFC0,</u> <u>UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC9,</u> <u>UL_TFC11</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 640</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 2x640</u>

NOTE 1: UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3 and UL_TFC6 are part of minimum set of TFCIs.

NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.

18.2.2.49.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2: RLC SDUs on RB5, RB6 and RB7 having the same content as sent by the SS; and no data shall be received on RB8.
 - for sub-test 3: RLC SDUs on RB8 having the same content as sent by the SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 4: RLC SDUs on RB5 and RB8 having the same content as sent by the SS; and no data shall be received on RB6 and RB7.
 - for sub-test 5: RLC SDUs on RB5, RB6, RB7 and RB8 having the same content as sent by the SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.49a Conversational / speech / UL:(12.2 7.95 5.9 4.75) DL(12.2 7.95 5.9 4.75) kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB+ UL:3.4 DL: 3.4 kbps SRBs for DCCH

18.2.2.49a.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.49a.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.49a.

18.2.2.49a.3 Method of test

See 18.2.1.2 for test procedure.

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Conversational / unknown / UL:64 DL:64 kbps / CS RAB (RB8):

<u>Uplink RLC</u> <u>TM RLC</u> <u>Segmentation indication</u> <u>Transmission RLC discard</u> <u>CHOICE SDU</u> <u>Discard Mode</u> <u>Timer based</u> <u>no explicit</u> <u>Timer discard</u>	<u>FALSE</u> <u>100ms</u>
<u>Downlink RLC</u> <u>TM RLC</u> <u>Segmentation indication</u>	<u>FALSE</u>
<u>NOTE: Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTL.</u>	

Uplink TFS:

	<u>TFI</u>	<u>RB5</u> (RAB subflow #1)	<u>RB6</u> (RAB subflow #2)	<u>RB7</u> (RAB subflow #3)	<u>RB8</u> (64 kbps)	<u>DCCH</u>
TFS	<u>TF0, bits</u>	0x81	0x103	0x60	0x640	0x148
	<u>TF1, bits</u>	1x39	1x53	1x60	2x640	1x148
	<u>TF2, bits</u>	1x42	1x63	N/A	N/A	N/A
	<u>TF3, bits</u>	1x55	1x84	N/A	N/A	N/A
	<u>TF4, bits</u>	1x75	1x103	N/A	N/A	N/A
	<u>TF5, bits</u>	1x81	N/A	N/A	N/A	N/A

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, RB7, 64 kbps RAB, DCCH)</u>
<u>UL_TFC0</u>	(TF0, TF0, TF0, TF0, TF0)
<u>UL_TFC1</u>	(TF1, TF0, TF0, TF0, TF0)
<u>UL_TFC2</u>	(TF2, TF1, TF0, TF0, TF0)
<u>UL_TFC3</u>	(TF3, TF2, TF0, TF0, TF0)
<u>UL_TFC4</u>	(TF4, TF3, TF0, TF0, TF0)
<u>UL_TFC5</u>	(TF5, TF4, TF1, TF0, TF0)
<u>UL_TFC6</u>	(TF0, TF0, TF0, TF1, TF0)
<u>UL_TFC7</u>	(TF1, TF0, TF0, TF1, TF0)
<u>UL_TFC8</u>	(TF2, TF1, TF0, TF1, TF0)
<u>UL_TFC9</u>	(TF3, TF2, TF0, TF1, TF0)
<u>UL_TFC10</u>	(TF4, TF3, TF0, TF1, TF0)
<u>UL_TFC11</u>	(TF5, TF4, TF1, TF1, TF0)
<u>UL_TFC12</u>	(TF0, TF0, TF0, TF0, TF1)
<u>UL_TFC13</u>	(TF1, TF0, TF0, TF0, TF1)
<u>UL_TFC14</u>	(TF2, TF1, TF0, TF0, TF1)
<u>UL_TFC15</u>	(TF3, TF2, TF0, TF0, TF1)
<u>UL_TFC16</u>	(TF4, TF3, TF0, TF0, TF1)
<u>UL_TFC17</u>	(TF5, TF4, TF1, TF0, TF1)
<u>UL_TFC18</u>	(TF0, TF0, TF0, TF1, TF1)
<u>UL_TFC19</u>	(TF1, TF0, TF0, TF1, TF1)
<u>UL_TFC20</u>	(TF2, TF1, TF0, TF1, TF1)
<u>UL_TFC21</u>	(TF3, TF2, TF0, TF1, TF1)
<u>UL_TFC22</u>	(TF4, TF3, TF0, TF1, TF1)
<u>UL_TFC23</u>	(TF5, TF4, TF1, TF1, TF1)

Downlink TFS:

	<u>TFI</u>	<u>RB5</u> (RAB subflow #1)	<u>RB6</u> (RAB subflow #2)	<u>RB7</u> (RAB subflow #3)	<u>RB8</u> (64 kbps)	<u>DCCH</u>
TFS	<u>TF0, bits</u>	1x0	0x103	0x60	0x640	0x148
	<u>TF1, bits</u>	1x39	1x53	1x60	2x640	1x148
	<u>TF2, bits</u>	1x42	1x63	N/A	N/A	N/A
	<u>TF3, bits</u>	1x55	1x84	N/A	N/A	N/A
	<u>TF4, bits</u>	1x75	1x103	N/A	N/A	N/A
	<u>TF5, bits</u>	1x81	N/A	N/A	N/A	N/A

Downlink TFCS:

<u>TFCI</u>	<u>(RB2, RB3, RB4, 64 kbps RAB, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF1, TF0, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF3, TF2, TF0, TF0, TF0)</u>
<u>DL_TFC4</u>	<u>(TF4, TF3, TF0, TF0, TF0)</u>
<u>DL_TFC5</u>	<u>(TF5, TF4, TF1, TF0, TF0)</u>
<u>DL_TFC6</u>	<u>(TF0, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF0, TF0, TF1, TF0)</u>
<u>DL_TFC8</u>	<u>(TF2, TF1, TF0, TF1, TF0)</u>
<u>DL_TFC9</u>	<u>(TF3, TF2, TF0, TF1, TF0)</u>
<u>DL_TFC10</u>	<u>(TF4, TF3, TF0, TF1, TF0)</u>
<u>DL_TFC11</u>	<u>(TF5, TF4, TF1, TF1, TF0)</u>
<u>DL_TFC12</u>	<u>(TF0, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC13</u>	<u>(TF1, TF0, TF0, TF0, TF1)</u>
<u>DL_TFC14</u>	<u>(TF2, TF1, TF0, TF0, TF1)</u>
<u>DL_TFC15</u>	<u>(TF3, TF2, TF0, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF4, TF3, TF0, TF0, TF1)</u>
<u>DL_TFC17</u>	<u>(TF5, TF4, TF1, TF0, TF1)</u>
<u>DL_TFC18</u>	<u>(TF0, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF0, TF0, TF1, TF1)</u>
<u>DL_TFC20</u>	<u>(TF2, TF1, TF0, TF1, TF1)</u>
<u>DL_TFC21</u>	<u>(TF3, TF2, TF0, TF1, TF1)</u>
<u>DL_TFC22</u>	<u>(TF4, TF3, TF0, TF1, TF1)</u>
<u>DL_TFC23</u>	<u>(TF5, TF4, TF1, TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	DL TFC1 , DL TFC13	UL TFC1 , UL TFC13	DL TFC0 , DL TFC12 , UL TFC0 , UL TFC12	UL TFC0 , UL TFC1 , UL TFC12 , UL TFC13	RB5: 39 RB6: 103 RB7: 60 RB8: 640	RB5: 39 RB6: No data RB7: No data RB8: No data
2	DL TFC2 , DL TFC14	UL TFC2 , UL TFC14	DL TFC0 , DL TFC12 , UL TFC0 , UL TFC12	UL TFC0 , UL TFC2 , UL TFC12 , UL TFC14	RB5: 42 RB6: 53 RB7: 60 RB8: 640	RB5: 42 RB6: 53 RB7: No data RB8: No data
3	DL TFC3 , DL TFC15	UL TFC3 , UL TFC15	DL TFC0 , DL TFC12 , UL TFC0 , UL TFC12	UL TFC0 , UL TFC3 , UL TFC12 , UL TFC15	RB5: 55 RB6: 63 RB7: 60 RB8: 640	RB5: 55 RB6: 63 RB7: No data RB8: No data
4	DL TFC4 , DL TFC16	UL TFC4 , UL TFC16	DL TFC0 , DL TFC12 , UL TFC0 , UL TFC12	UL TFC0 , UL TFC4 , UL TFC12 , UL TFC16	RB5: 75 RB6: 84 RB7: 60 RB8: 640	RB5: 75 RB6: 84 RB7: No data RB8: No data
5	DL TFC5 , DL TFC17	UL TFC5 , UL TFC17	DL TFC0 , DL TFC12 , UL TFC0 , UL TFC12	UL TFC0 , UL TFC5 , UL TFC12 , UL TFC17	RB5: 81 RB6: 103 RB7: 60 RB8: 640	RB5: 81 RB6: 103 RB7: 60 RB8: No data
6	DL TFC6 , DL TFC18	UL TFC6 , UL TFC18	DL TFC0 , DL TFC12 , UL TFC0 , UL TFC12	UL TFC0 , UL TFC6 , UL TFC12 , UL TFC18	RB5:81 RB6:103 RB7: 60 RB8: 1280	RB5: No data RB6: No data RB7: No data RB8: 1280
7	DL TFC7 , DL TFC19	UL TFC7 , UL TFC19	DL TFC0 , DL TFC12 , UL TFC0 , UL TFC12	UL TFC0 , UL TFC1 , UL TFC6 , UL TFC7 , UL TFC12 , UL TFC13 , UL TFC18 , UL TFC19	RB5: 39 RB6: 103 RB7: 60 RB8: 1280	RB5: 39 RB6: No data RB7: No data RB8: 1280
8	DL TFC8 , DL TFC20	UL TFC8 , UL TFC20	DL TFC0 , DL TFC12 , UL TFC0 , UL TFC12	UL TFC0 , UL TFC2 , UL TFC6 , UL TFC8 , UL TFC12 , UL TFC14 , UL TFC18 , UL TFC20	RB5: 42 RB6: 53 RB7: 60 RB8: 1280	RB5: 42 RB6: 53 RB7: No data RB8: 1280
9	DL TFC9 , DL TFC21	UL TFC9 , UL TFC21	DL TFC0 , DL TFC12 , UL TFC0 , UL TFC12	UL TFC0 , UL TFC3 , UL TFC6 , UL TFC9 , UL TFC12 , UL TFC15 , UL TFC18 , UL TFC21	RB5: 55 RB6: 63 RB7: 60 RB8: 1280	RB5: 55 RB6: 63 RB7: No data RB8: 1280
10	DL TFC10 ⁺ DL TFC22	UL TFC10 ⁺ UL TFC22	DL TFC0 , DL TFC12 , UL TFC0 , UL TFC12	UL TFC0 , UL TFC4 , UL TFC6 , UL TFC10 , UL TFC12 , UL TFC16 , UL TFC18 , UL TFC22	RB5: 75 RB6: 84 RB7: 60 RB8: 1280	RB5: 75 RB6: 84 RB7: No data RB8: 1280

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
11	<u>DL TFC11</u> <u>DL TFC23</u>	<u>UL TFC11</u> <u>UL TFC23</u>	<u>DL TFC0,</u> <u>DL TFC12,</u> <u>UL TFC0,</u> <u>UL TFC12</u>	<u>UL TFC0,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC11,</u> <u>UL TFC12,</u> <u>UL TFC17,</u> <u>UL TFC18,</u> <u>UL TFC23</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 1280</u>	<u>RB5: 81</u> <u>RB6: 103</u> <u>RB7: 60</u> <u>RB8: 1280</u>
<p><u>NOTE:</u> See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. As the TTI for RB8 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the uplink TB size.</p>						

18.2.2.49a.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6, RB7 and RB8.
 - for sub-test 2, 3, 4: an RLC SDU on RB5, RB6 having the same content as sent by SS; and no data shall be received on RB7 and RB8.
 - for sub-test 5: an RLC SDU on RB5, RB6 and RB7 having the same content as sent by SS; and no data shall be received on RB8.
 - for sub-test 6: an RLC SDU on RB8 having the same content as sent by SS; and no data shall be received on RB5, RB6 and RB7.
 - for sub-test 7: an RLC SDU on RB5 and RB8 having the same content as sent by SS; and no data shall be received on RB6, RB7.
 - for sub-test 8, 9, 10: an RLC SDU on RB5, RB6 and RB8 having the same content as sent by SS; and no data shall be received on RB7.
 - for sub-test 11: an RLC SDU on RB5, RB6, RB7 and RB8 having the same content as sent by SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.50 Conversational / unknown / UL:64 DL:64 kbps / CS RAB + Conversational / unknown / UL:64 DL:64 kbps / CS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH

18.2.2.50.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.50.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.50.

18.2.2.50. 3 Method of test

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Conversational / unknown / UL:64 DL:64 kbps / CS RAB (RB5 and RB6):

	RB5 (64 kbps)	RB6 (64 kbps)
Uplink RLC		
<u>TM RLC</u>		
<u>Segmentation indication</u>	<u>FALSE</u>	<u>FALSE</u>
<u>Transmission RLC discard</u>		
<u>CHOICE SDU</u>		
<u>Discard Mode</u>		
<u>Timer based</u>		
<u>no explicit</u>		
<u>Timer discard</u>	<u>100ms</u>	<u>100ms</u>
Downlink RLC		
<u>TM RLC</u>		
<u>Segmentation indication</u>	<u>FALSE</u>	<u>FALSE</u>
NOTE: <u>Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTI.</u>		

See 18.2.1.2 for test procedure.

Uplink TFS:

	TFI	RB5 (64 kbps)	RB6 (64 kbps)	DCCH
TFS	<u>TF0, bits</u>	<u>0x640</u>	<u>0x640</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>2x640</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Uplink TFCS:

TFCI	(RB5, RB6, DCCH)
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF1, TF0)</u>
<u>UL_TFC3</u>	<u>(TF1, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF0, TF0, TF1)</u>
<u>UL_TFC5</u>	<u>(TF1, TF0, TF1)</u>
<u>UL_TFC6</u>	<u>(TF0, TF1, TF1)</u>
<u>UL_TFC7</u>	<u>(TF1, TF1, TF1)</u>

Downlink TFS:

	TFI	RB5 (64 kbps)	RB6 (64 kbps)	DCCH
TFS	<u>TF0, bits</u>	<u>0x640</u>	<u>0x640</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>2x640</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>N/A</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC3</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC5</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC6</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC7</u>	<u>(TF1, TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	<u>DL_TFC1,</u> <u>DL_TFC5</u>	<u>UL_TFC1,</u> <u>DL_TFC5</u>	<u>DL_TFC0,</u> <u>DL_TFC4,</u> <u>UL_TFC0,</u> <u>UL_TFC4</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC4,</u> <u>UL_TFC5</u>	<u>RB5: 640</u> <u>RB6: 640</u>	<u>RB5: 2x640</u> <u>RB6: No data</u>
2	<u>DL_TFC2,</u> <u>DL_TFC6</u>	<u>UL_TFC2,</u> <u>DL_TFC6</u>	<u>DL_TFC0,</u> <u>DL_TFC4,</u> <u>UL_TFC0,</u> <u>UL_TFC4</u>	<u>UL_TFC0,</u> <u>UL_TFC2,</u> <u>UL_TFC4,</u> <u>UL_TFC6</u>	<u>RB5: 640</u> <u>RB6: 640</u>	<u>RB5: No data</u> <u>RB6: 2x640</u>
3	<u>DL_TFC3,</u> <u>DL_TFC7</u>	<u>UL_TFC3,</u> <u>DL_TFC7</u>	<u>DL_TFC0,</u> <u>DL_TFC4,</u> <u>UL_TFC0,</u> <u>UL_TFC4</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7</u>	<u>RB5: 640</u> <u>RB6: 640</u>	<u>RB5: 2x640</u> <u>RB6: 2x640</u>

NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.
As the TTI for RB5 and RB6 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the uplink TB size.

18.2.2.50. 4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual subtest.
3. At step 15 the UE shall return
 - for sub-test 1: two RLC SDUs on RB5 having the same content as sent by SS; and no data shall be received on RB6.
 - for sub-test 2: two RLC SDUs on RB6 having the same content as sent by SS; and no data shall be received on RB5.
 - for sub-test 3: two RLC SDUs on RB5 and RB6 having the same content as sent by SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.51 Conversational / unknown / UL:64 DL:64 kbps / CS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH

18.2.2.51.1 Conversational / unknown / UL:64 DL:64 kbps / CS RAB / + Interactive or background / UL:64 DL:64 kbps / PS RAB / Payload 320

18.2.2.51.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.51.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.51 for the uplink payload 320 interactive or background case.

18.2.2.51.1.3 Method of test

See 18.2.1.2 for test procedure.

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Conversational / unknown / UL:64 DL:64 kbps / CS RAB (RB5):

	<u>RB5 (Conv. 64 kbps)</u>
<u>Uplink RLC</u> <u>TM RLC</u> <u>Segmentation indication</u> <u>Transmission RLC discard</u> <u>CHOICE SDU</u> <u>Discard Mode</u> <u>Timer based</u> <u>no explicit</u> <u>Timer discard</u>	<u>FALSE</u> <u>100ms</u>
<u>Downlink RLC</u> <u>TM RLC</u> <u>Segmentation indication</u>	<u>FALSE</u>
<u>NOTE: Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTI .</u>	

Uplink TFS:

	<u>TFI</u>	<u>RB5 (Conv. 64 kbps)</u>	<u>RB6 (I/B 64 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x640</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>UL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>UL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>UL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>UL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>UL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>UL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>UL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>UL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>UL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>UL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>UL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>UL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>UL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5 (Conv. 64 kbps)</u>	<u>RB6 (I/E 64 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x640</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>

Downlink TFCs:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>DL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>DL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>DL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>DL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>DL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>DL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>DL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>DL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	DL_TFC1 , DL_TFC11	UL_TFC1 , UL_TFC11	DL_TFC0 , DL_TFC10 , UL_TFC0 , UL_TFC10	UL_TFC0 , UL_TFC1 , UL_TFC5 , UL_TFC10 , UL_TFC11	RB5: 640 RB6: 312	RB5: No data RB6: 312
2	DL_TFC2 , DL_TFC12	UL_TFC2 , UL_TFC12	DL_TFC0 , DL_TFC10 , UL_TFC0 , UL_TFC10	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC5 , UL_TFC10 , UL_TFC12	RB5: 640 RB6: 632	RB5: No data RB6: 632
3	DL_TFC3 , DL_TFC13	UL_TFC3 , UL_TFC13	DL_TFC0 , DL_TFC10 , UL_TFC0 , UL_TFC10	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC5 , UL_TFC10 , UL_TFC13	RB5: 640 RB6: 952	RB5: No data RB6: 952
4	DL_TFC4 , DL_TFC14	UL_TFC4 , UL_TFC14	DL_TFC0 , DL_TFC10 , UL_TFC0 , UL_TFC10	UL_TFC0 , UL_TFC1 , UL_TFC4 , UL_TFC5 , UL_TFC10 , UL_TFC14	RB5: 640 RB6: 1272	RB5: No data RB6: 1272
5	DL_TFC5 , DL_TFC15	UL_TFC5 , UL_TFC15	DL_TFC0 , DL_TFC10 , UL_TFC0 , UL_TFC10	UL_TFC0 , UL_TFC1 , UL_TFC5 , UL_TFC10 , UL_TFC15	RB5: 640 RB6: 312	RB5: 2x640 RB6: No data
6	DL_TFC6 , DL_TFC16	UL_TFC6 , UL_TFC16	DL_TFC0 , DL_TFC10 , UL_TFC0 , UL_TFC10	UL_TFC0 , UL_TFC1 , UL_TFC5 , UL_TFC6 , UL_TFC10 , UL_TFC11 , UL_TFC15 , UL_TFC16	RB5: 640 RB6: 312	RB5: 2x640 RB6: 312
7	DL_TFC7 , DL_TFC17	UL_TFC7 , UL_TFC17	DL_TFC0 , DL_TFC10 , UL_TFC0 , UL_TFC10	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC5 , UL_TFC7 , UL_TFC10 , UL_TFC12 , UL_TFC15 , UL_TFC17	RB5: 640 RB6: 632	RB5: 2x640 RB6: 632
8	DL_TFC8 , DL_TFC18	UL_TFC8 , UL_TFC18	DL_TFC0 , DL_TFC10 , UL_TFC0 , UL_TFC10	UL_TFC0 , UL_TFC1 , UL_TFC3 , UL_TFC5 , UL_TFC8 , UL_TFC10 , UL_TFC13 , UL_TFC15 , UL_TFC18	RB5: 640 RB6: 952	RB5: 2x640 RB6: 952
9	DL_TFC9 , DL_TFC19	UL_TFC9 , UL_TFC19	DL_TFC0 , DL_TFC10 , UL_TFC0 , UL_TFC10	UL_TFC0 , UL_TFC1 , UL_TFC4 , UL_TFC5 , UL_TFC9 , UL_TFC10 , UL_TFC14 , UL_TFC15 , UL_TFC19	RB5: 640 RB6: 1272	RB5: 2x640 RB6: 1272

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
<p>NOTE 1: <u>UL TFC0, UL TFC1, UL TFC5 and UL TFC10 are part of minimum set of TFCs.</u></p> <p>NOTE 2: <u>See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB6: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size parameter has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.51.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1, 2, 3, 4: RLC SDUs on RB6 having the same content as sent by the SS; and no data shall be received on RB5.
 - for sub-test 5: RLC SDUs on RB5 having the same content as sent by the SS; and no data shall be received on RB6.
 - for sub-test 6, 7, 8 and 9: RLC SDUs on RB5 and RB6 having the same content as sent by the SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.51.2 Conversational / unknown / UL:64 DL:64 kbps / CS RAB / + Interactive or background / UL:64 DL:64 kbps / PS RAB / Payload 128

18.2.2.51.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.51.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108. clause 6.10.3.4.1.51 for the uplink payload 128 interactive or background case.

18.2.2.51.2.3 Method of test

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Conversational / unknown / UL:64 DL:64 kbps / CS RAB (RB5):

	RB5 (Conv. 64 kbps)
Uplink RLC TM RLC Segmentation indication Transmission RLC discard CHOICE SDU Discard Mode Timer based no explicit Timer discard	FALSE 100ms
Downlink RLC TM RLC Segmentation indication	FALSE
NOTE: Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTI.	

See 18.2.1.2 for test procedure.

Uplink TFS:

	TFI	RB5 (Conv. 64 kbps)	RB6 (I/B 64 kbps)	DCCH
TFS	TF0, bits	0x640	0x144	0x148
	TF1, bits	2x640	1x144	1x148
	TF2, bits	N/A	3x144	N/A
	TF3, bits	N/A	7x144	N/A
	TF4, bits	N/A	10x144	N/A

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>UL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>UL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>UL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>UL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>UL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>UL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>UL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>UL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>UL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>UL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>UL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>UL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>UL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5 (Conv. 64 kbps)</u>	<u>RB6 (I/E 64 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x640</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>

Downlink TFCs:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>DL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>DL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>DL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>DL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>DL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>DL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>DL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>DL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	<u>DL TFC1,</u> <u>DL TFC11</u>	<u>UL TFC1,</u> <u>UL TFC11</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC10,</u> <u>UL TFC11</u>	<u>RB5: 640</u> <u>RB6: 120</u>	<u>RB5: No data</u> <u>RB6: 312</u>
2	<u>DL TFC2,</u> <u>DL TFC12</u>	<u>UL TFC2,</u> <u>UL TFC12</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC2,</u> <u>UL TFC10,</u> <u>UL TFC12</u>	<u>RB5: 640</u> <u>RB6: 376</u>	<u>RB5: No data</u> <u>RB6: 632</u>
3	<u>DL TFC3,</u> <u>DL TFC13</u>	<u>UL TFC3,</u> <u>UL TFC13</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC3,</u> <u>UL TFC10,</u> <u>UL TFC13</u>	<u>RB5: 640</u> <u>RB6: 888</u>	<u>RB5: No data</u> <u>RB6: 952</u>
4	<u>DL TFC4,</u> <u>DL TFC14</u>	<u>UL TFC4,</u> <u>UL TFC14</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC4,</u> <u>UL TFC10,</u> <u>UL TFC14</u>	<u>RB5: 640</u> <u>RB6: 1272</u>	<u>RB5: No data</u> <u>RB6: 1272</u>
5	<u>DL TFC5,</u> <u>DL TFC15</u>	<u>UL TFC5,</u> <u>UL TFC15</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC5,</u> <u>UL TFC5,</u> <u>UL TFC10,</u> <u>UL TFC11,</u> <u>UL TFC15,</u> <u>UL TFC15</u>	<u>RB5: 640</u> <u>RB6: 120</u>	<u>RB5: 2x640</u> <u>RB6: No data</u>
6	<u>DL TFC6,</u> <u>DL TFC16</u>	<u>UL TFC6,</u> <u>UL TFC16</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC6,</u> <u>UL TFC10,</u> <u>UL TFC16</u>	<u>RB5: 640</u> <u>RB6: 120</u>	<u>RB5: 2x640</u> <u>RB6: 312</u>
7	<u>DL TFC7,</u> <u>DL TFC17</u>	<u>UL TFC7,</u> <u>UL TFC17</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC2,</u> <u>UL TFC5,</u> <u>UL TFC7,</u> <u>UL TFC10,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC17</u>	<u>RB5: 640</u> <u>RB6: 376</u>	<u>RB5: 2x640</u> <u>RB6: 632</u>
8	<u>DL TFC8,</u> <u>DL TFC18</u>	<u>UL TFC8,</u> <u>UL TFC18</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC3,</u> <u>UL TFC5,</u> <u>UL TFC8,</u> <u>UL TFC10,</u> <u>UL TFC13,</u> <u>UL TFC15,</u> <u>UL TFC18</u>	<u>RB5: 640</u> <u>RB6: 888</u>	<u>RB5: 2x640</u> <u>RB6: 952</u>
9	<u>DL TFC9,</u> <u>DL TFC19</u>	<u>UL TFC9,</u> <u>UL TFC19</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC9,</u> <u>UL TFC10,</u> <u>UL TFC14,</u> <u>UL TFC15,</u> <u>UL TFC19</u>	<u>RB5: 640</u> <u>RB6: 1272</u>	<u>RB5: 2x640</u> <u>RB6: 1272</u>

NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.
RB6: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB5 and RB6 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size for RB6 has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit) and the UL RLC SDU size for RB5 has been set equal to the uplink TB size.

18.2.2.51.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual subtest.
3. At step 15 the UE shall return
 - for sub-test 1: an RLC SDU on RB6 having the content equal to the first 120 bits of the test data sent by the SS in downlink; and no data shall be received on RB5.
 - for sub-test 2: an RLC SDU on RB6 having the content equal to the first 376 bits of the test data sent by the SS in downlink; and no data shall be received on RB5.
 - for sub-test 3: an RLC SDU on RB6 having the content equal to the first 888 bits of the test data sent by the SS in downlink; and no data shall be received on RB5.
 - for sub-test 4: an RLC SDU on RB6 having the same content as sent by SS; and no data shall be received on RB5.
 - for sub-test 5: two RLC SDUs on RB5 having the same content as sent by SS; and no data shall be received on RB6.
 - for sub-test 6: two RLC SDUs on RB5 and one RLC SDU on RB6 having the content equal to the first 120 bits of the test data sent by the SS in downlink.
 - for sub-test 7: two RLC SDUs on RB5 and one RLC SDU on RB6 having the content equal to the first 376 bits of the test data sent by the SS in downlink.
 - for sub-test 8: two RLC SDUs on RB5 and one RLC SDU on RB6 having the content equal to the first 888 bits of the test data sent by the SS in downlink.
 - for sub-test 9: two RLC SDUs on RB5 and one RLC SDU on RB6 having the same content as sent by SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.51a Conversational / unknown / UL:64 DL:64 kbps / CS RAB + Interactive or Background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.51a.1 Conversational / unknown / UL:64 DL:64 kbps / CS RAB / + Interactive or background / UL:8 DL:8 kbps / PS RAB / Payload 320, 40 ms TTI

18.2.2.51a.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.51a.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.51a for the uplink payload 320, 40 ms TTI case.

18.2.2.51a.1.3 Method of test

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Conversational / unknown / UL:64 DL:64 kbps / CS RAB (RB5):

	RB5 (Conv. 64 kbps)
Uplink RLC TM RLC Segmentation indication Transmission RLC discard CHOICE SDU Discard Mode Timer based no explicit Timer discard	FALSE 100ms
Downlink RLC TM RLC Segmentation indication	FALSE
NOTE: Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTI.	

See 18.2.1.2 for test procedure.

Uplink TFS:

	TFI	RB5 (Conv. 64 kbps)	RB6 (I/B 8 kbps)	DCCH
TFS	TF0, bits	0x640	0x336	0x148
	TF1, bits	2x640	1x336	1x148

Uplink TFCS:

TFCI	(RB5, RB6, DCCH)
UL_TFC0	(TF0, TF0, TF0)
UL_TFC1	(TF0, TF1, TF0)
UL_TFC2	(TF1, TF0, TF0)
UL_TFC3	(TF1, TF1, TF0)
UL_TFC4	(TF0, TF0, TF1)
UL_TFC5	(TF0, TF1, TF1)
UL_TFC6	(TF1, TF0, TF1)
UL_TFC7	(TF1, TF1, TF1)

Downlink TFS:

	TFI	RB5 (Conv. 64 kbps)	RB6 (I/B 8kbps)	DCCH
TFS	TF0, bits	0x640	0x336	0x148
	TF1, bits	2x640	1x336	1x148

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC5</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC6</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC7</u>	<u>(TF1, TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
1	<u>DL_TFC1</u> <u>DL_TFC5</u>	<u>UL_TFC1</u> <u>UL_TFC5</u>	<u>DL_TFC0, DL_TFC4,</u> <u>UL_TFC0, UL_TFC4,</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC4,</u> <u>UL_TFC5</u>	<u>RB5: 640</u> <u>RB6: 312</u>	<u>RB5: No data</u> <u>RB6: 312</u>
2	<u>DL_TFC2</u> <u>DL_TFC6</u>	<u>UL_TFC2</u> <u>UL_TFC6</u>	<u>DL_TFC0, DL_TFC4,</u> <u>UL_TFC0, UL_TFC4,</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC4,</u> <u>UL_TFC6</u>	<u>RB5: 640</u> <u>RB6: 312</u>	<u>RB5: 2x640</u> <u>RB6: No data</u>
3	<u>DL_TFC3</u> <u>DL_TFC7</u>	<u>UL_TFC3</u> <u>UL_TFC7</u>	<u>DL_TFC0, DL_TFC4,</u> <u>UL_TFC0, UL_TFC4,</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7</u>	<u>RB5: 640</u> <u>RB6: 312</u>	<u>RB5: 2x640</u> <u>RB6: 312</u>

NOTE 1: UL_TFC0, UL_TFC1, UL_TFC5 and UL_TFC10 are part of minimum set of TFCIs.

NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.

RB6: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB5 is the same for both downlink and uplink then UL RLC SDU size has been set equal to the uplink TB size.

18.2.2.51a.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB6 having the same content as sent by SS; and no data shall be received on RB5.
 - for sub-test 2: two RLC SDUs on RB5 having the same content as sent by SS; and no data shall be received on RB6.
 - for sub-test 3: two RLC SDUs on RB5 having the same content as sent by SS; and an RLC SDU on RB6 having the same content as sent by SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.51a.2 Conversational / unknown / UL:64 DL:64 kbps / CS RAB / + Interactive or background / UL:8 DL:8 kbps / PS RAB / Payload 128, 80 ms TTI

18.2.2.51a.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.51a.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.51a for the uplink payload 128, 80 ms TTI case.

18.2.2.51a.2.3 Method of test

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Conversational / unknown / UL:64 DL:64 kbps / CS RAB (RB5):

	RB5 (Conv. 64 kbps)
<u>Uplink RLC</u> <u>TM RLC</u> <u>Segmentation indication</u> <u>Transmission RLC discard</u> <u>CHOICE SDU</u> <u>Discard Mode</u> <u>Timer based</u> <u>no explicit</u> <u>Timer discard</u>	 FALSE 100ms
<u>Downlink RLC</u> <u>TM RLC</u> <u>Segmentation indication</u>	 FALSE
<u>NOTE: Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTI .</u>	

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (Conv. 64 kbps)</u>	<u>RB6 (I/B 8 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x640</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>5x144</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>UL_TFC3</u>	<u>(TF1, TF0, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF1, TF2, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF1)</u>
<u>UL_TFC7</u>	<u>(TF0, TF1, TF1)</u>
<u>UL_TFC8</u>	<u>(TF0, TF2, TF1)</u>
<u>UL_TFC9</u>	<u>(TF1, TF0, TF1)</u>
<u>UL_TFC10</u>	<u>(TF1, TF1, TF1)</u>
<u>UL_TFC11</u>	<u>(TF1, TF2, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5 (Conv. 64 kbps)</u>	<u>RB6 (I/B 64 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x640</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>1x336</u>	<u>1x148</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC5</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC6</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC7</u>	<u>(TF1, TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) Note 1</u>	<u>Test data size (bits) Note 1</u>
<u>1</u>	<u>DL TFC1, DL TFC5</u>	<u>UL TFC1, UL TFC7</u>	<u>DL TFC0, DL TFC4, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC1, UL TFC6, UL TFC7</u>	<u>RB5: 640 RB6: 56</u>	<u>RB5: No data RB6: 312</u>
<u>2</u>	<u>DL TFC1, DL TFC5</u>	<u>UL TFC2, UL TFC8</u>	<u>DL TFC0, DL TFC4, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC1, UL TFC2, UL TFC6, UL TFC7, UL TFC8</u>	<u>RB5: 640 RB6: 312</u>	<u>RB5: No data RB6: 312</u>
<u>3</u>	<u>DL TFC2, DL TFC6</u>	<u>UL TFC3, UL TFC9</u>	<u>DL TFC0, DL TFC4, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC1, UL TFC3, UL TFC6, UL TFC7, UL TFC9</u>	<u>RB5: 640 RB6: 56</u>	<u>RB5: 2x640 RB6: No data</u>
<u>4</u>	<u>DL TFC3, DL TFC7</u>	<u>UL TFC4, UL TFC10</u>	<u>DL TFC0, DL TFC4, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC1, UL TFC3, UL TFC4, UL TFC6, UL TFC7, UL TFC9, UL TFC10</u>	<u>RB5: 640 RB6: 56</u>	<u>RB5: 2x640 RB6: 312</u>
<u>5</u>	<u>DL TFC3, DL TFC7</u>	<u>UL TFC5, UL TFC11</u>	<u>DL TFC0, DL TFC4, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC1, UL TFC3, UL TFC5, UL TFC6, UL TFC7, UL TFC9, UL TFC11</u>	<u>RB5: 640 RB6: 312</u>	<u>RB5: 2x640 RB6: 312</u>
<p><u>NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB6: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB5 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.51a.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: two RLC SDUs on RB6 having the same content equal to the first 56 bits of the test data sent by SS in downlink; and no data shall be received on RB5.
 - for sub-test 2: two RLC SDUs on RB6 having the same content as the test data sent by SS in downlink; and no data shall be received on RB5.
 - for sub-test 3: two RLC SDUs on RB5 having the same content as the test data sent by SS in downlink; and no data shall be received on RB6.
 - for sub-test 4: two RLC SDUs on RB6 having the same content equal to the first 56 bits of the test data sent by SS in downlink; and two RLC SDU on RB5 having the same content as sent by SS.
 - for sub-test 5: two RLC SDUs on RB6 having the same content as the test data sent by SS in downlink; and two RLC SDU on RB5 having the same content as sent by SS.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.51b Conversational / unknown / UL:64 DL:64 kbps / CS RAB + Interactive or Background / UL:16 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.51b.1 Conversational / unknown / UL:64 DL:64 kbps / CS RAB / + Interactive or background / UL:16 DL:64 kbps / PS RAB / Payload 320

18.2.2.51b.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.51b.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.51b for the uplink payload 320 case.

18.2.2.51b.1.3 Method of test

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Conversational / unknown / UL:64 DL:64 kbps / CS RAB (RB5):

	<u>RB5 (Conv. 64 kbps)</u>
<u>Uplink RLC</u>	
<u>TM RLC</u>	
<u>Segmentation indication</u>	<u>FALSE</u>
<u>Transmission RLC discard</u>	
<u>CHOICE SDU</u>	
<u>Discard Mode</u>	
<u>Timer based</u>	
<u>no explicit</u>	
<u>Timer discard</u>	<u>100ms</u>
<u>Downlink RLC</u>	
<u>TM RLC</u>	
<u>Segmentation indication</u>	<u>FALSE</u>
<u>NOTE: Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTI .</u>	

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (Conv. 64 kbps)</u>	<u>RB6 (I/B 16 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x640</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>UL_TFC3</u>	<u>(TF1, TF0, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF1, TF2, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF1)</u>
<u>UL_TFC7</u>	<u>(TF0, TF1, TF1)</u>
<u>UL_TFC8</u>	<u>(TF0, TF2, TF1)</u>
<u>UL_TFC9</u>	<u>(TF1, TF0, TF1)</u>
<u>UL_TFC10</u>	<u>(TF1, TF1, TF1)</u>
<u>UL_TFC11</u>	<u>(TF1, TF2, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5 (Conv. 64 kbps)</u>	<u>RB6 (I/B 64 kbps)</u>	<u>DCCH</u>
TFS	<u>TF0, bits</u>	<u>0x640</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>DL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>DL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>DL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>DL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>DL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>DL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>DL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>DL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
1	<u>DL_TFC1,</u> <u>DL_TFC11</u>	<u>UL_TFC1,</u> <u>UL_TFC7</u>	<u>DL_TFC0, DL_TFC10,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC7</u>	<u>RB5: 640</u> <u>RB6: 152</u>	<u>RB5: No data</u> <u>RB6: 312</u>
2	<u>DL_TFC2,</u> <u>DL_TFC12</u>	<u>UL_TFC2</u> <u>,UL_TFC8</u>	<u>DL_TFC0, DL_TFC10,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC8</u>	<u>RB5: 640</u> <u>RB6: 312</u>	<u>RB5: No data</u> <u>RB6: 632</u>
3	<u>DL_TFC3,</u> <u>DL_TFC13</u>	<u>UL_TFC2,</u> <u>UL_TFC8</u>	<u>DL_TFC0, DL_TFC10,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC8</u>	<u>RB5: 640</u> <u>RB6: 312</u>	<u>RB5: No data</u> <u>RB6: 952</u>
4	<u>DL_TFC4,</u> <u>DL_TFC14</u>	<u>UL_TFC2</u> <u>,UL_TFC8</u>	<u>DL_TFC0, DL_TFC10,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC8</u>	<u>RB5: 640</u> <u>RB6: 312</u>	<u>RB5: No data</u> <u>RB6: 1272</u>
5	<u>DL_TFC5,</u> <u>DL_TFC15</u>	<u>UL_TFC3</u> <u>,UL_TFC9</u>	<u>DL_TFC0, DL_TFC10,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC3,</u> <u>UL_TFC6,</u> <u>UL_TFC9</u>	<u>RB5: 640</u> <u>RB6: 152</u>	<u>RB5: 2x640</u> <u>RB6: No data</u>
6	<u>DL_TFC6,</u> <u>DL_TFC16</u>	<u>UL_TFC4,</u> <u>UL_TFC10</u>	<u>DL_TFC0, DL_TFC10,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC9,</u> <u>UL_TFC10</u>	<u>RB5: 640</u> <u>RB6: 152</u>	<u>RB5: 2x640</u> <u>RB6: 312</u>
7	<u>DL_TFC7,</u> <u>DL_TFC17</u>	<u>UL_TFC5,</u> <u>UL_TFC11</u>	<u>DL_TFC0, DL_TFC10,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC9,</u> <u>UL_TFC11</u>	<u>RB5: 640</u> <u>RB6: 312</u>	<u>RB5: 2x640</u> <u>RB6: 632</u>
8	<u>DL_TFC8,</u> <u>DL_TFC18</u>	<u>UL_TFC5,</u> <u>UL_TFC11</u>	<u>DL_TFC0, DL_TFC10,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC9,</u> <u>UL_TFC11</u>	<u>RB5: 640</u> <u>RB6: 312</u>	<u>RB5: 2x640</u> <u>RB6: 952</u>
9	<u>DL_TFC9,</u> <u>DL_TFC19</u>	<u>UL_TFC5,</u> <u>UL_TFC11</u>	<u>DL_TFC0, DL_TFC10,</u> <u>UL_TFC0, UL_TFC6</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC8,</u> <u>UL_TFC9,</u> <u>UL_TFC11</u>	<u>RB5: 640</u> <u>RB6: 312</u>	<u>RB5: 2x640</u> <u>RB6: 1272</u>

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitly tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
NOTE 1: UL_TFC0, UL_TFC1, UL_TFC3 and UL_TFC6 are part of minimum set of TFCs.						
NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.						
<u>RB6: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size for RB6 has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit) divided by 2 (40ms uplink TTI divided by 20 ms downlink TTI) and the UL RLC SDU size for RB5 has been set equal to the uplink TB size.</u>						

18.2.2.51b.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: two RLC SDUs on RB6 having the content equal to the first 152 bits of the test data sent by SS in downlink; and no data shall be received on RB5.
 - for sub-test 2, 3, and 4: two RLC SDUs on RB6 having the content equal to the first 312 bits of the test data sent by SS in downlink; and no data shall be received on RB5.
 - for sub-test 5: two RLC SDUs on RB5 having the same content as sent by the SS in downlink per TB; and no data shall be received on RB6.
 - for sub-test 6: two RLC SDUs on RB5 having the same content as sent by the SS in downlink per TB; and two RLC SDUs on RB6 having the content equal to the first 152 bits of the test data sent by the SS in downlink.
 - for sub-test 7, 8, and 9: two RLC SDUs on RB5 having the same content as sent by the SS in downlink per TB; and two RLC SDUs on RB6 having the content equal to the first 312 bits of the test data sent by the SS in downlink.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.51b.2 Conversational / unknown / UL:64 DL:64 kbps / CS RAB / + Interactive or background / UL:16 DL:64 kbps / PS RAB / Payload 128

18.2.2.51b.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.51b.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.51b for the uplink payload 128 case.

18.2.2.51b.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 (Conv. 64 kbps)</u>	<u>RB6 (I/B 16 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x640</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>5x144</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>UL_TFC3</u>	<u>(TF1, TF0, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF1, TF2, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF1)</u>
<u>UL_TFC7</u>	<u>(TF0, TF1, TF1)</u>
<u>UL_TFC8</u>	<u>(TF0, TF2, TF1)</u>
<u>UL_TFC9</u>	<u>(TF1, TF0, TF1)</u>
<u>UL_TFC10</u>	<u>(TF1, TF1, TF1)</u>
<u>UL_TFC11</u>	<u>(TF1, TF2, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5 (Conv. 64 kbps)</u>	<u>RB6 (I/E 64 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x640</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>3x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>DL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>DL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>DL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>DL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>DL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>DL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>DL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>DL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) Note 1</u>	<u>Test data size (bits) Note 1</u>
1	<u>DL TFC1, DL TFC11</u>	<u>UL TFC1, UL TFC7</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC1, UL TFC6, UL TFC7</u>	<u>RB5: 640 RB6: 56</u>	<u>RB5: No data RB6: 312</u>
2	<u>DL TFC2, DL TFC12</u>	<u>UL TFC2, UL TFC8</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC2, UL TFC6, UL TFC8</u>	<u>RB5: 640 RB6: 312</u>	<u>RB5: No data RB6: 632</u>
3	<u>DL TFC3, DL TFC13</u>	<u>UL TFC2, UL TFC8</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC2, UL TFC6, UL TFC8</u>	<u>RB5: 640 RB6: 312</u>	<u>RB5: No data RB6: 952</u>
4	<u>DL TFC4, DL TFC14</u>	<u>UL TFC2, UL TFC8</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC2, UL TFC6, UL TFC8</u>	<u>RB5: 640 RB6: 312</u>	<u>RB5: No data RB6: 1272</u>
5	<u>DL TFC5, DL TFC15</u>	<u>UL TFC3, UL TFC9</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC3, UL TFC6, UL TFC9</u>	<u>RB5: 640 RB6: 56</u>	<u>RB5: 2x640 RB6: No data</u>
6	<u>DL TFC6, DL TFC16</u>	<u>UL TFC4, UL TFC10</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC1, UL TFC3, UL TFC4, UL TFC6, UL TFC7, UL TFC9, UL TFC10</u>	<u>RB5: 640 RB6: 56</u>	<u>RB5: 2x640 RB6: 312</u>
7	<u>DL TFC7, DL TFC17</u>	<u>UL TFC5, UL TFC11</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC2, UL TFC3, UL TFC5, UL TFC6, UL TFC8, UL TFC9, UL TFC11</u>	<u>RB5: 640 RB6: 312</u>	<u>RB5: 2x640 RB6: 632</u>
8	<u>DL TFC8, DL TFC18</u>	<u>UL TFC5, UL TFC11</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC2, UL TFC3, UL TFC5, UL TFC6, UL TFC8, UL TFC9, UL TFC11</u>	<u>RB5: 640 RB6: 312</u>	<u>RB5: 2x640 RB6: 952</u>
9	<u>DL TFC9, DL TFC19</u>	<u>UL TFC5, UL TFC11</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC6</u>	<u>UL TFC0, UL TFC2, UL TFC3, UL TFC5, UL TFC6, UL TFC8, UL TFC9, UL TFC11</u>	<u>RB5: 640 RB6: 312</u>	<u>RB5: 2x640 RB6: 1272</u>

NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.
RB6: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size for RB6 has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit) divided by 2 (40ms uplink TTI divided by 20 ms downlink TTI) and the UL RLC SDU size for RB5 has been set equal to the uplink TB size.

18.2.2.51b.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.

2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.

3. At step 15a and step 15b the UE shall return

- for sub-test 1: two RLC SDUs on RB6 having the content equal to the first 56 bits of the test data sent by SS in downlink; and no data shall be received on RB5.
- for sub-test 2, 3, and 4: two RLC SDUs on RB6 having the content equal to the first 312 bits of the test data sent by SS in downlink; and no data shall be received on RB5.
- for sub-test 5: two RLC SDUs on RB5 having the same content as sent by the SS in downlink per TB; and no data shall be received on RB6.
- for sub-test 6: two RLC SDUs on RB5 having the same content as sent by the SS in downlink per TB; and two RLC SDUs on RB6 having the content equal to the first 56 bits of the test data sent by the SS in downlink.
- for sub-test 7, 8, and 9: two RLC SDUs on RB5 having the same content as sent by the SS in downlink per TB; and two RLC SDUs on RB6 having the content equal to the first 312 bits of the test data sent by the SS in downlink.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.52 Conversational / unknown / UL:64 DL:64 kbps / CS RAB + Interactive or background / UL:64 DL:128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH

18.2.2.52.1 Conversational / unknown / UL:64 DL:64 kbps / CS RAB / + Interactive or background / UL:64 DL:128 kbps / PS RAB / Payload 320

18.2.2.52.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.52.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.52 for the uplink payload 320 case.

18.2.2.52.1.3 Method of test

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Conversational / unknown / UL:64 DL:64 kbps / CS RAB (RB5):

	RB5 (Conv. 64 kbps)
Uplink RLC TM RLC Segmentation indication Transmission RLC discard CHOICE SDU Discard Mode Timer based no explicit Timer discard	FALSE 100ms
Downlink RLC TM RLC Segmentation indication	FALSE
NOTE: Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTI.	

See 18.2.1.2 for test procedure.

Uplink TFS:

	TFI	RB5 (Conv. 64 kbps)	RB6 (I/B 64 kbps)	DCCH
TFS	TF0, bits	0x640	0x336	0x148
	TF1, bits	2x640	1x336	1x148
	TF2, bits	N/A	2x336	N/A
	TF3, bits	N/A	3x336	N/A
	TF4, bits	N/A	4x336	N/A

Uplink TFCS:

TFCI	(RB5, RB6, DCCH)
UL_TFC0	(TF0, TF0, TF0)
UL_TFC1	(TF0, TF1, TF0)
UL_TFC2	(TF0, TF2, TF0)
UL_TFC3	(TF0, TF3, TF0)
UL_TFC4	(TF0, TF4, TF0)
UL_TFC5	(TF1, TF0, TF0)
UL_TFC6	(TF1, TF1, TF0)
UL_TFC7	(TF1, TF2, TF0)
UL_TFC8	(TF1, TF3, TF0)
UL_TFC9	(TF1, TF4, TF0)
UL_TFC10	(TF0, TF0, TF1)
UL_TFC11	(TF0, TF1, TF1)
UL_TFC12	(TF0, TF2, TF1)
UL_TFC13	(TF0, TF3, TF1)
UL_TFC14	(TF0, TF4, TF1)
UL_TFC15	(TF1, TF0, TF1)
UL_TFC16	(TF1, TF1, TF1)
UL_TFC17	(TF1, TF2, TF1)
UL_TFC18	(TF1, TF3, TF1)
UL_TFC19	(TF1, TF4, TF1)

Downlink TFS:

	<u>TFI</u>	<u>RB5 (Conv. 64 kbps)</u>	<u>RB6 (I/E 128 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x640</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>8x336</u>	<u>N/A</u>

Downlink TFCs:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>DL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>DL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>DL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>DL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>DL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>DL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>DL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>DL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	DL TFC1 , DL TFC11	UL TFC1 , UL TFC11	DL TFC0 , DL TFC10 , UL TFC0 , UL TFC10	UL TFC0 , UL TFC1 , UL TFC10 , UL TFC11	RB5: 640 RB6: 312	RB5: No data RB6: 312
2	DL TFC2 , DL TFC12	UL TFC2 , UL TFC12	DL TFC0 , DL TFC10 , UL TFC0 , UL TFC10	UL TFC0 , UL TFC2 , UL TFC10 , UL TFC12	RB5: 640 RB6: 632	RB5: No data RB6: 632
3	DL TFC3 , DL TFC13	UL TFC3 , UL TFC13	DL TFC0 , DL TFC10 , UL TFC0 , UL TFC10	UL TFC0 , UL TFC3 , UL TFC10 , UL TFC13	RB5: 640 RB6: 952	RB5: No data RB6: 1272
4	DL TFC4 , DL TFC14	UL TFC4 , UL TFC14	DL TFC0 , DL TFC10 , UL TFC0 , UL TFC10	UL TFC0 , UL TFC4 , UL TFC10 , UL TFC14	RB5: 640 RB6: 1272	RB5: No data RB6: 2552
5	DL TFC5 , DL TFC15	UL TFC5 , UL TFC15	DL TFC0 , DL TFC10 , UL TFC0 , UL TFC10	UL TFC0 , UL TFC5 , UL TFC10 , UL TFC15	RB5: 640 RB6: 312	RB5: 2x640 RB6: No data
6	DL TFC6 , DL TFC16	UL TFC6 , UL TFC16	DL TFC0 , DL TFC10 , UL TFC0 , UL TFC10	UL TFC0 , UL TFC1 , UL TFC5 , UL TFC6 , UL TFC10 , UL TFC11 , UL TFC15 , UL TFC16	RB5: 640 RB6: 312	RB5: 2x640 RB6: 312
7	DL TFC7 , DL TFC17	UL TFC7 , UL TFC17	DL TFC0 , DL TFC10 , UL TFC0 , UL TFC10	UL TFC0 , UL TFC2 , UL TFC5 , UL TFC7 , UL TFC10 , UL TFC12 , UL TFC15 , UL TFC17	RB5: 640 RB6: 632	RB5: 2x640 RB6: 632
8	DL TFC8 , DL TFC18	UL TFC8 , UL TFC18	DL TFC0 , DL TFC10 , UL TFC0 , UL TFC10	UL TFC0 , UL TFC3 , UL TFC5 , UL TFC8 , UL TFC10 , UL TFC13 , UL TFC15 , UL TFC18	RB5: 640 RB6: 952	RB5: 2x640 RB6: 1272
9	DL TFC9 , DL TFC19	UL TFC9 , UL TFC19	DL TFC0 , DL TFC10 , UL TFC0 , UL TFC10	UL TFC0 , UL TFC4 , UL TFC5 , UL TFC9 , UL TFC10 , UL TFC14 , UL TFC15 , UL TFC19	RB5: 640 RB6: 1272	RB5: 2x640 RB6: 2552

NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.
 RB6: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB5 and RB6 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size for RB6 has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit) and the UL RLC SDU size for RB5 has been set equal to the uplink TB size.

18.2.2.52.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual subtest.
3. At step 15 the UE shall return
 - for sub-test 1, 2, 3, 4: an RLC SDU on RB6 having the same content as sent by SS; and no data shall be received on RB5.
 - for sub-test 5: two RLC SDUs on RB5 having the same content as sent by SS; and no data shall be received on RB6.
 - for sub-test 6: two RLC SDUs on RB5 and one RLC SDU on RB6 having the same content as sent by SS.
 - For sub-test 3: RLC SDU on RB6 having the content equal to the first 952 bits of the test data sent by the SS in downlink;
 - For sub-test 4: RLC SDU on RB6 having the content equal to the first 1272 bits of the test data sent by the SS in downlink;
 - for sub-test 8: an RLC SDU on RB6 having the content equal to the first 952 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS.
 - for sub-test 9: an RLC SDU on RB6 having the content equal to the first 1272 bits of the test data sent by the SS in downlink; an RLC SDU on RB5 having the same content as sent by SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.52.2 Conversational / unknown / UL:64 DL:64 kbps / CS RAB / + Interactive or background / UL:64 DL:128 kbps / PS RAB / Payload 128

18.2.2.52.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.52.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.51 for the uplink payload 128 case.

18.2.2.52.2.3 Method of test

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Conversational / unknown / UL:64 DL:64 kbps / CS RAB (RB5):

	RB5 (Conv. 64 kbps)
Uplink RLC TM RLC Segmentation indication Transmission RLC discard CHOICE SDU Discard Mode Timer based no explicit Timer discard	FALSE 100ms
Downlink RLC TM RLC Segmentation indication	FALSE
NOTE: Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTI.	

See 18.2.1.2 for test procedure.

Uplink TFS:

	TFI	RB5 (Conv. 64 kbps)	RB6 (I/B 64 kbps)	DCCH
TFS	TF0, bits	0x640	0x144	0x148
	TF1, bits	2x640	1x144	1x148
	TF2, bits	N/A	3x144	N/A
	TF3, bits	N/A	3x144	N/A
	TF4, bits	N/A	10x144	N/A

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>UL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>UL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>UL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>UL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>UL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>UL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>UL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>UL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>UL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>UL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>UL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>UL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>UL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5 (Conv. 64 kbps)</u>	<u>RB6 (I/B 128 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x640</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>8x336</u>	<u>N/A</u>

Downlink TFCs:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>DL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>DL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>DL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>DL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>DL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>DL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>DL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>DL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	<u>DL TFC1,</u> <u>DL TFC11</u>	<u>UL TFC1,</u> <u>DL TFC11</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC10,</u> <u>UL TFC11</u>	<u>RB5: 640</u> <u>RB6: 120</u>	<u>RB5: No data</u> <u>RB6: 312</u>
2	<u>DL TFC2,</u> <u>DL TFC12</u>	<u>UL TFC2,</u> <u>DL TFC12</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC2,</u> <u>UL TFC10,</u> <u>UL TFC12</u>	<u>RB5: 640</u> <u>RB6: 376</u>	<u>RB5: No data</u> <u>RB6: 632</u>
3	<u>DL TFC3,</u> <u>DL TFC13</u>	<u>UL TFC3,</u> <u>DL TFC13</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC3,</u> <u>UL TFC10,</u> <u>UL TFC13</u>	<u>RB5: 640</u> <u>RB6: 888</u>	<u>RB5: No data</u> <u>RB6: 1272</u>
4	<u>DL TFC4,</u> <u>DL TFC14</u>	<u>UL TFC4,</u> <u>DL TFC14</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC4,</u> <u>UL TFC10,</u> <u>UL TFC14</u>	<u>RB5: 640</u> <u>RB6: 1272</u>	<u>RB5: No data</u> <u>RB6: 2552</u>
5	<u>DL TFC5,</u> <u>DL TFC15</u>	<u>UL TFC5,</u> <u>DL TFC15</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC5,</u> <u>UL TFC10,</u> <u>UL TFC15</u>	<u>RB5: 640</u> <u>RB6: 120</u>	<u>RB5: 2x640</u> <u>RB6: No data</u>
6	<u>DL TFC6,</u> <u>DL TFC16</u>	<u>UL TFC6,</u> <u>DL TFC16</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC10,</u> <u>UL TFC11,</u> <u>UL TFC15,</u> <u>UL TFC16</u>	<u>RB5: 640</u> <u>RB6: 120</u>	<u>RB5: 2x640</u> <u>RB6: 312</u>
7	<u>DL TFC7,</u> <u>DL TFC17</u>	<u>UL TFC7,</u> <u>DL TFC17</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC2,</u> <u>UL TFC5,</u> <u>UL TFC7,</u> <u>UL TFC10,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC17</u>	<u>RB5: 640</u> <u>RB6: 376</u>	<u>RB5: 2x640</u> <u>RB6: 632</u>
8	<u>DL TFC8,</u> <u>DL TFC18</u>	<u>UL TFC8,</u> <u>DL TFC18</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC3,</u> <u>UL TFC5,</u> <u>UL TFC8,</u> <u>UL TFC10,</u> <u>UL TFC13,</u> <u>UL TFC15,</u> <u>UL TFC18</u>	<u>RB5: 640</u> <u>RB6: 888</u>	<u>RB5: 2x640</u> <u>RB6: 1272</u>
9	<u>DL TFC9,</u> <u>DL TFC19</u>	<u>UL TFC9,</u> <u>DL TFC19</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC4,</u> <u>UL TFC15,</u> <u>UL TFC9,</u> <u>UL TFC10,</u> <u>UL TFC14,</u> <u>UL TFC15,</u> <u>UL TFC19</u>	<u>RB5: 640</u> <u>RB6: 1272</u>	<u>RB5: 2x640</u> <u>RB6: 2552</u>

NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.
RB6: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB5 and RB6 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size for RB6 has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit) and the UL RLC SDU size for RB5 has been set equal to the uplink TB size.

18.2.2.52.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual subtest.
3. At step 15 the UE shall return
 - for sub-test 1: an RLC SDU on RB6 having the content equal to the first 120 bits of the test data sent by SS in downlink; and no data shall be received on RB5.
 - for sub-test 2: an RLC SDU on RB6 having the content equal to the first 376 bits of the test data sent by SS in downlink; and no data shall be received on RB5.
 - for sub-test 3: an RLC SDU on RB6 having the content equal to the first 888 bits of the test data sent by SS in downlink; and no data shall be received on RB5.
 - for sub-test 4: an RLC SDU on RB6 having the same content as sent by SS; and no data shall be received on RB5.
 - for sub-test 5: two RLC SDUs on RB5 having the same content as sent by SS; and no data shall be received on RB6.
 - for sub-test 6: two RLC SDUs on RB5 and an RLC SDU on RB6 having the content equal to the first 120 bits of the test data sent by SS in downlink.
 - for sub-test 7: two RLC SDUs on RB5 and an RLC SDU on RB6 having the content equal to the first 376 bits of the test data sent by SS in downlink.
 - for sub-test 8: two RLC SDUs on RB5 and an RLC SDU on RB6 having the content equal to the first 888 bits of the test data sent by SS in downlink.
 - for sub-test 9: two RLC SDUs on RB5 and an RLC SDU on RB6 having the content equal to the first 1272 bits of the test data sent by SS in downlink.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.53 Conversational / unknown / UL:64 DL:64 kbps / CS RAB + Interactive or background / UL:128 DL:128 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH

18.2.2.53.1 Conversational / unknown / UL:64 DL:64 kbps / CS RAB / + Interactive or background / UL:128 DL:128 kbps / PS RAB / Payload 320

18.2.2.53.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.53.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108. clause 6.10.3.4.1.53 for the uplink payload 320 case.

18.2.2.53.1.3 Method of test

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Conversational / unknown / UL:64 DL:64 kbps / CS RAB (RB5):

	RB5 (Conv. 64 kbps)
Uplink RLC TM RLC Segmentation indication Transmission RLC discard CHOICE SDU Discard Mode Timer based no explicit Timer discard	FALSE 100ms
Downlink RLC TM RLC Segmentation indication	FALSE
NOTE: Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTI.	

See 18.2.1.2 for test procedure.

Uplink TFS:

	TFI	RB5 (Conv. 64 kbps)	RB6 (I/B 128 kbps)	DCCH
TFS	TF0, bits	0x640	0x336	0x148
	TF1, bits	2x640	1x336	1x148
	TF2, bits	N/A	2x336	N/A
	TF3, bits	N/A	4x336	N/A
	TF4, bits	N/A	8x336	N/A

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>UL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>UL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>UL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>UL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>UL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>UL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>UL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>UL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>UL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>UL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>UL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>UL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>UL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5 (Conv. 64 kbps)</u>	<u>RB6 (I/B 128 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x640</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>2x640</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>2x336</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>N/A</u>	<u>4x336</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>N/A</u>	<u>8x336</u>	<u>N/A</u>

Downlink TFCs:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>DL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>DL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>DL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>DL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>DL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>DL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>DL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>DL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	<u>DL TFC1,</u> <u>DL TFC11</u>	<u>UL TFC1,</u> <u>DL TFC11</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC10,</u> <u>UL TFC11</u>	<u>RB5: 640</u> <u>RB6: 312</u>	<u>RB5: No data</u> <u>RB6: 312</u>
2	<u>DL TFC2,</u> <u>DL TFC12</u>	<u>UL TFC2,</u> <u>DL TFC12</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC2,</u> <u>UL TFC10,</u> <u>UL TFC12</u>	<u>RB5: 640</u> <u>RB6: 632</u>	<u>RB5: No data</u> <u>RB6: 632</u>
3	<u>DL TFC3,</u> <u>DL TFC13</u>	<u>UL TFC3,</u> <u>DL TFC13</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC3,</u> <u>UL TFC10,</u> <u>UL TFC13</u>	<u>RB5: 640</u> <u>RB6: 1272</u>	<u>RB5: No data</u> <u>RB6: 1272</u>
4	<u>DL TFC4,</u> <u>DL TFC14</u>	<u>UL TFC4,</u> <u>DL TFC14</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC4,</u> <u>UL TFC10,</u> <u>UL TFC14</u>	<u>RB5: 640</u> <u>RB6: 2552</u>	<u>RB5: No data</u> <u>RB6: 2552</u>
5	<u>DL TFC5,</u> <u>DL TFC15</u>	<u>UL TFC5,</u> <u>DL TFC15</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC5,</u> <u>UL TFC10,</u> <u>UL TFC15</u>	<u>RB5: 640</u> <u>RB6: 312</u>	<u>RB5: 2x640</u> <u>RB6: No data</u>
6	<u>DL TFC6,</u> <u>DL TFC16</u>	<u>UL TFC6,</u> <u>DL TFC16</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC1,</u> <u>UL TFC5,</u> <u>UL TFC6,</u> <u>UL TFC10,</u> <u>UL TFC11,</u> <u>UL TFC15,</u> <u>UL TFC16</u>	<u>RB5: 640</u> <u>RB6: 312</u>	<u>RB5: 2x640</u> <u>RB6: 312</u>
7	<u>DL TFC7,</u> <u>DL TFC17</u>	<u>UL TFC7,</u> <u>DL TFC17</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC2,</u> <u>UL TFC5,</u> <u>UL TFC7,</u> <u>UL TFC10,</u> <u>UL TFC12,</u> <u>UL TFC15,</u> <u>UL TFC17</u>	<u>RB5: 640</u> <u>RB6: 632</u>	<u>RB5: 2x640</u> <u>RB6: 632</u>
8	<u>DL TFC8,</u> <u>DL TFC18</u>	<u>UL TFC8,</u> <u>DL TFC18</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC3,</u> <u>UL TFC5,</u> <u>UL TFC8,</u> <u>UL TFC10,</u> <u>UL TFC13,</u> <u>UL TFC15,</u> <u>UL TFC18</u>	<u>RB5: 640</u> <u>RB6: 1272</u>	<u>RB5: 2x640</u> <u>RB6: 1272</u>
9	<u>DL TFC9,</u> <u>DL TFC19</u>	<u>UL TFC9,</u> <u>DL TFC19</u>	<u>DL TFC0,</u> <u>DL TFC10,</u> <u>UL TFC0,</u> <u>UL TFC10</u>	<u>UL TFC0,</u> <u>UL TFC4,</u> <u>UL TFC5,</u> <u>UL TFC9,</u> <u>UL TFC10,</u> <u>UL TFC14,</u> <u>UL TFC15,</u> <u>UL TFC19</u>	<u>RB5: 640</u> <u>RB6: 2552</u>	<u>RB5: 2x640</u> <u>RB6: 2552</u>
<p><u>NOTE:</u> See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. <u>RB6:</u> Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB5 and RB6 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size for RB6 has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit) and the UL RLC SDU size for RB5 has been set equal to the uplink TFS size under test.</p>						

18.2.2.53.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual subtest.
3. At step 15 the UE shall return
 - for sub-test 1, 2, 3, 4: an RLC SDU on RB6 having the same content as sent by SS; and no data shall be received on RB5.
 - for sub-test 5: two RLC SDUs on RB5 having the same content as sent by SS; and no data shall be received on RB6.
 - for sub-test 6, 7, 8 and 9: two RLC SDUs on RB5 and one RLC SDU on RB6 having the same content as sent by SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.53.2 Conversational / unknown / UL:64 DL:64 kbps / CS RAB / + Interactive or background / UL:128 DL:128 kbps / PS RAB / Payload 128

18.2.2.53.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.53.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.53 for the uplink payload 128 case.

18.2.2.53.2.3 Method of test

Initial Conditions

The following RLC Info parameter values shall be set by the SS for the Conversational / unknown / UL:64 DL:64 kbps / CS RAB (RB5):

	<u>RB5 (Conv. 64 kbps)</u>
<u>Uplink RLC</u>	
<u>TM RLC</u>	
<u>Segmentation indication</u>	<u>FALSE</u>
<u>Transmission RLC discard</u>	
<u>CHOICE SDU</u>	
<u>Discard Mode</u>	
<u>Timer based</u>	
<u>no explicit</u>	
<u>Timer discard</u>	<u>100ms</u>
<u>Downlink RLC</u>	
<u>TM RLC</u>	
<u>Segmentation indication</u>	<u>FALSE</u>
<u>NOTE: Timer based discard without explicit signalling is used in uplink to secure that the UE will be able to return data for the case when the UE test loop function will not deliver all the SDUs in one and the same TTI .</u>	

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5</u> <u>(Conv.</u> <u>64 kbps)</u>	<u>RB6</u> <u>(I/B</u> <u>128 kbps)</u>	<u>DCCH</u>
TFS	<u>TF0, bits</u>	0x640	0x144	0x148
	<u>TF1, bits</u>	2x640	1x144	1x148
	<u>TF2, bits</u>	N/A	7x144	N/A
	<u>TF3, bits</u>	N/A	14x144	N/A
	<u>TF4, bits</u>	N/A	20x144	N/A

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>UL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>UL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>UL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>UL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>UL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>UL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>UL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>UL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>UL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>UL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>UL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>UL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>UL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>UL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>UL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>UL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5</u> <u>(Conv.</u> <u>64 kbps)</u>	<u>RB6</u> <u>(I/B</u> <u>128 kbps)</u>	<u>DCCH</u>
TFS	<u>TF0, bits</u>	0x640	0x336	0x148
	<u>TF1, bits</u>	4x640	1x336	1x148
	<u>TF2, bits</u>	N/A	2x336	N/A
	<u>TF3, bits</u>	N/A	4x336	N/A
	<u>TF4, bits</u>	N/A	8x336	N/A

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF0, TF2, TF0)</u>
<u>DL_TFC3</u>	<u>(TF0, TF3, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF4, TF0)</u>
<u>DL_TFC5</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC6</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC7</u>	<u>(TF1, TF2, TF0)</u>
<u>DL_TFC8</u>	<u>(TF1, TF3, TF0)</u>
<u>DL_TFC9</u>	<u>(TF1, TF4, TF0)</u>
<u>DL_TFC10</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC11</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC12</u>	<u>(TF0, TF2, TF1)</u>
<u>DL_TFC13</u>	<u>(TF0, TF3, TF1)</u>
<u>DL_TFC14</u>	<u>(TF0, TF4, TF1)</u>
<u>DL_TFC15</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC16</u>	<u>(TF1, TF1, TF1)</u>
<u>DL_TFC17</u>	<u>(TF1, TF2, TF1)</u>
<u>DL_TFC18</u>	<u>(TF1, TF3, TF1)</u>
<u>DL_TFC19</u>	<u>(TF1, TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	<u>DL TFC1, UL TFC11</u>	<u>UL TFC1, UL TFC11</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC10</u>	<u>UL TFC0, UL TFC1, UL TFC10, UL TFC11</u>	<u>RB5: 640 RB6: 120</u>	<u>RB5: No data RB6: 312</u>
2	<u>DL TFC2, UL TFC12</u>	<u>UL TFC2, UL TFC12</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC10</u>	<u>UL TFC0, UL TFC2, UL TFC10, UL TFC12</u>	<u>RB5: 640 RB6: 888</u>	<u>RB5: No data RB6: 632</u>
3	<u>DL TFC3, UL TFC13</u>	<u>UL TFC3, UL TFC13</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC10</u>	<u>UL TFC0, UL TFC3, UL TFC10, UL TFC13</u>	<u>RB5: 640 RB6: 1784</u>	<u>RB5: No data RB6: 1272</u>
4	<u>DL TFC4, UL TFC14</u>	<u>UL TFC4, UL TFC14</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC10</u>	<u>UL TFC0, UL TFC4, UL TFC10, UL TFC14</u>	<u>RB5: 640 RB6: 2552</u>	<u>RB5: No data RB6: 2552</u>
5	<u>DL TFC5, UL TFC15</u>	<u>UL TFC5, UL TFC15</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC10</u>	<u>UL TFC0, UL TFC5, UL TFC10, UL TFC15</u>	<u>RB5: 640 RB6: 120</u>	<u>RB5: 2x640 RB6: No data</u>
6	<u>DL TFC6, UL TFC16</u>	<u>UL TFC6, UL TFC16</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC10</u>	<u>UL TFC0, UL TFC1, UL TFC5, UL TFC6, UL TFC10, UL TFC11, UL TFC15, UL TFC16</u>	<u>RB5: 640 RB6: 120</u>	<u>RB5: 2x640 RB6: 312</u>
7	<u>DL TFC7, UL TFC17</u>	<u>UL TFC7, UL TFC17</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC10</u>	<u>UL TFC0, UL TFC2, UL TFC5, UL TFC7, UL TFC10, UL TFC12, UL TFC15, UL TFC17</u>	<u>RB5: 640 RB6: 888</u>	<u>RB5: 2x640 RB6: 632</u>
8	<u>DL TFC8, UL TFC18</u>	<u>UL TFC8, UL TFC18</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC10</u>	<u>UL TFC0, UL TFC3, UL TFC5, UL TFC8, UL TFC10, UL TFC13, UL TFC15, UL TFC18</u>	<u>RB5: 640 RB6: 1784</u>	<u>RB5: 2x640 RB6: 1272</u>
9	<u>DL TFC9, UL TFC19</u>	<u>UL TFC9, UL TFC19</u>	<u>DL TFC0, DL TFC10, UL TFC0, UL TFC10</u>	<u>UL TFC0, UL TFC4, UL TFC5, UL TFC9, UL TFC10, UL TFC14, UL TFC15, UL TFC19</u>	<u>RB5: 640 RB6: 2552</u>	<u>RB5: 2x640 RB6: 2552</u>

NOTE: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.
RB6: Test data size has been set to DL TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the TTI for RB5 and RB6 is the same for both downlink and uplink then UL RLC SDU size has been set to achieve UE to return one SDU per TTI, i.e. the UL RLC SDU size for RB6 has been set equal to the uplink TFS size under test minus 8 bits (size of 7 bit length indicator and expansion bit) and the UL RLC SDU size for RB5 has been set equal to the uplink TFS size under test.

18.2.2.53.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual subtest.
3. At step 15 the UE shall return
 - for sub-test 1: an RLC SDU on RB6 having the content equal to the first 120 bits of the test data sent by SS in downlink; and no data shall be received on RB5.
 - for sub-test 2: an RLC SDU on RB6 having the same content as 1 times plus 256 lsb's of the test data sent by the SS in downlink; and no data shall be received on RB5.
 - for sub-test 3: an RLC SDU on RB6 having the same content as 1 times plus 512 lsb's of the test data sent by the SS in downlink; and no data shall be received on RB5.
 - for sub-test 4: an RLC SDU on RB6 having the same content as sent by SS in downlink; and no data shall be received on RB5..
 - for sub-test 5: two RLC SDUs on RB5 having the same content as sent by SS; and no data shall be received on RB6.
 - for sub-test 6: two RLC SDUs on RB5 having the same content as sent by SS; and an RLC SDU on RB6 having the same content as 1 times plus 120 lsb's of the test data sent by the SS in downlink.
 - for sub-test 6: two RLC SDUs on RB5 having the same content as sent by SS; and an RLC SDU on RB6 having the same content as 1 times plus 256 lsb's of the test data sent by the SS in downlink.
 - for sub-test 6: two RLC SDUs on RB5 having the same content as sent by SS; and an RLC SDU on RB6 having the same content as 1 times plus 512 lsb's of the test data sent by the SS in downlink.
 - for sub-test 9: two RLC SDUs on RB5 and an RLC SDU on RB6 having the same content as sent by SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.54 Void

18.2.2.55 Void

18.2.2.56 Interactive or background / UL:8 DL:8 kbps / PS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.56.1 Interactive or background / UL:8 DL:8 kbps / PS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320, TTI 40.

18.2.2.56.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.56.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.56 for the uplink payload 320, TTI 40ms case.

18.2.2.56.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 + RB6</u> <u>(2x8 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	0x340	0x148
	<u>TF1, bits</u>	1x340	1x148

Uplink TFCS:

<u>TFCI</u>	<u>(RB5 + RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF1)</u>
<u>UL_TFC3</u>	<u>(TF1, TF1)</u>

Downlink TFS:

		<u>RB5 + RB6</u> <u>(2x8 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	0x340	0x148
	<u>TF1, bits</u>	1x340	1x148

Downlink TFCS:

<u>TFCI</u>	<u>(RB5+RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF0, TF1)</u>
<u>DL_TFC3</u>	<u>(TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCS Under Test</u>	<u>Uplink TFCS Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
<u>1</u>	<u>DL_TFC1, DL_TFC3</u>	<u>UL_TFC1, UL_TFC3</u>	<u>DL_TFC0, DL_TFC2, UL_TFC0, UL_TFC2</u>	<u>UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3</u>	<u>RB5: 312 RB6: 312</u>	<u>RB5: 312 RB6: no data</u>
<u>2</u>	<u>DL_TFC1, DL_TFC3</u>	<u>UL_TFC1, UL_TFC3</u>	<u>DL_TFC0, DL_TFC2, UL_TFC0, UL_TFC2</u>	<u>UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC3</u>	<u>RB5: 312 RB6: 312</u>	<u>RB5: no data RB6: 312</u>
<u>NOTE:</u> See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB5 and RB6: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).						

18.2.2.56.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return

- for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6.
- for sub-test 2: an RLC SDU on RB6 having the same content as sent by SS; and no data shall be received on RB5.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.56.2 Interactive or background / UL:8 DL:8 kbps / PS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128, TTI 80.

18.2.2.56.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.56.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.56 for the uplink payload 128, TTI 80ms case.

18.2.2.56.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 + RB6 (2x8 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>5x144</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5 + RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF1)</u>
<u>UL_TFC4</u>	<u>(TF1, TF1)</u>
<u>UL_TFC5</u>	<u>(TF2, TF1)</u>

Downlink TFS:

		<u>RB5 + RB6 (2x8 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x340</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x340</u>	<u>1x148</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5+RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF0, TF1)</u>
<u>DL_TFC3</u>	<u>(TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u>	<u>UL RLC SDU size (bits) (note)</u>	<u>Test data size (bits) (note)</u>
1	<u>DL TFC1, DL TFC3</u>	<u>UL TFC1, UL TFC4</u>	<u>DL TFC0, DL TFC2, UL TFC0, UL TFC3</u>	<u>UL TFC0, UL TFC1, UL TFC3, UL TFC4</u>	<u>RB5: 56 RB6: 56</u>	<u>RB5: 312 RB6: no data</u>
2	<u>DL TFC1, DL TFC3</u>	<u>UL TFC2, UL TFC5</u>	<u>DL TFC0, DL TFC2, UL TFC0, UL TFC3</u>	<u>UL TFC0, UL TFC1, UL TFC2, UL TFC3, UL TFC4, UL TFC5</u>	<u>RB5: 312 RB6: 312</u>	<u>RB5: 312 RB6: no data</u>
3	<u>DL TFC1, DL TFC3</u>	<u>UL TFC1, UL TFC4</u>	<u>DL TFC0, DL TFC2, UL TFC0, UL TFC3</u>	<u>UL TFC0, UL TFC1, UL TFC3, UL TFC4</u>	<u>RB5: 56 RB6: 56</u>	<u>RB5: no data RB6: 312</u>
4	<u>DL TFC1, DL TFC3</u>	<u>UL TFC2, UL TFC5</u>	<u>DL TFC0, DL TFC2, UL TFC0, UL TFC3</u>	<u>UL TFC0, UL TFC1, UL TFC2, UL TFC3, UL TFC4, UL TFC5</u>	<u>RB5: 312 RB6: 312</u>	<u>RB5: no data RB6: 312</u>
<p><u>NOTE:</u> See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. <u>RB5 and RB6: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit) divided by 2(80ms/40ms).</u></p>						

18.2.2.56.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the content equal to the first 56 bits of the test data sent by SS in downlink; and no data shall be received on RB6.
 - for sub-test 2: an RLC SDU on RB5 having the same content as sent by SS in downlink; and no data shall be received on RB6.
 - for sub-test 3: an RLC SDU on RB6 having the content equal to the first 56 bits of the test data sent by SS in downlink; and no data shall be received on RB5.
 - for sub-test 4: an RLC SDU on RB6 having the same content as sent by SS in downlink; and no data shall be received on RB5.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.57 Interactive or background / UL:64 DL:64 kbps / PS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.57.1 Interactive or background / UL:64 DL:64 kbps / PS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320.

18.2.2.57.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.57.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.57 for the uplink payload 320 case.

18.2.2.57.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	TFI	RB5 + RB6 (64 kbps RAB)	DCCH
TFS	<u>TF0, bits</u>	<u>0x340</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x340</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>2x340</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>3x340</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>4x340</u>	<u>N/A</u>

Uplink TFCS:

TFCI	(RB5 + RB6, DCCH)
<u>UL_TFC0</u>	<u>(TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0)</u>
<u>UL_TFC2</u>	<u>(TF2, TF0)</u>
<u>UL_TFC3</u>	<u>(TF3, TF0)</u>
<u>UL_TFC4</u>	<u>(TF4, TF0)</u>
<u>UL_TFC5</u>	<u>(TF0, TF1)</u>
<u>UL_TFC6</u>	<u>(TF1, TF1)</u>
<u>UL_TFC7</u>	<u>(TF2, TF1)</u>
<u>UL_TFC8</u>	<u>(TF3, TF1)</u>
<u>UL_TFC9</u>	<u>(TF4, TF1)</u>

Downlink TFS:

	TFI	RB5 + RB6 (64 kbps RAB)	DCCH
TFS	<u>TF0, bits</u>	<u>0x340</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x340</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>2x340</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>3x340</u>	<u>N/A</u>
	<u>TF4, bits</u>	<u>4x340</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5 + RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF0)</u>
<u>DL_TFC3</u>	<u>(TF3, TF0)</u>
<u>DL_TFC4</u>	<u>(TF4, TF0)</u>
<u>DL_TFC5</u>	<u>(TF0, TF1)</u>
<u>DL_TFC6</u>	<u>(TF1, TF1)</u>
<u>DL_TFC7</u>	<u>(TF2, TF1)</u>
<u>DL_TFC8</u>	<u>(TF3, TF1)</u>
<u>DL_TFC9</u>	<u>(TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
<u>1</u>	<u>DL_TFC1</u> <u>DL_TFC6</u>	<u>UL_TFC1</u> <u>DL_TFC6</u>	<u>DL_TFC0,</u> <u>DL_TFC5,</u> <u>UL_TFC0,</u> <u>UL_TFC5</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC5,</u> <u>UL_TFC6</u>	<u>RB5: 312</u> <u>RB6: 312</u>	<u>RB5: 312</u> <u>RB6: No data</u>
<u>2</u>	<u>DL_TFC2</u> <u>DL_TFC7</u>	<u>UL_TFC2</u> <u>DL_TFC7</u>	<u>DL_TFC0,</u> <u>DL_TFC5,</u> <u>UL_TFC0,</u> <u>UL_TFC5</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC5,</u> <u>UL_TFC7</u>	<u>RB5: 632</u> <u>RB6: 632</u>	<u>RB5: 632</u> <u>RB6: No data</u>
<u>3</u>	<u>DL_TFC3</u> <u>DL_TFC8</u>	<u>UL_TFC3</u> <u>DL_TFC8</u>	<u>DL_TFC0,</u> <u>DL_TFC5,</u> <u>UL_TFC0,</u> <u>UL_TFC5</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC3,</u> <u>UL_TFC5,</u> <u>UL_TFC8</u>	<u>RB5: 952</u> <u>RB6: 952</u>	<u>RB5: 952</u> <u>RB6: No data</u>
<u>4</u>	<u>DL_TFC4</u> <u>DL_TFC9</u>	<u>UL_TFC4</u> <u>DL_TFC9</u>	<u>DL_TFC0,</u> <u>DL_TFC5,</u> <u>UL_TFC0,</u> <u>UL_TFC5</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC9</u>	<u>RB5: 1272</u> <u>RB6: 1272</u>	<u>RB5: 1272</u> <u>RB6: No data</u>
<u>5</u>	<u>DL_TFC4</u> <u>DL_TFC9</u>	<u>UL_TFC4</u> <u>DL_TFC9</u>	<u>DL_TFC0,</u> <u>DL_TFC5,</u> <u>UL_TFC0,</u> <u>UL_TFC5</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC9</u>	<u>RB5: 1272</u> <u>RB5: 1272</u>	<u>RB5: No data</u> <u>RB6: 1272</u>

NOTE 1: UL_TFC0, UL_TFC1 and UL_TFC5 are part of minimum set of TFCIs.

NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.

RB5 and RB6: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).

18.2.2.57.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1 to 4: an RLC SDU on RB5 having the same content as the test data sent by SS in downlink; and no data shall be received on RB6.
 - for sub-test 5: an RLC SDU on RB6 having the same content as the test data sent by SS in downlink; and no data shall be received on RB5..

4. [At step 15b the UE shall send at least one MEASUREMENT REPORT message.](#)

[18.2.2.57.2 Interactive or background / UL:64 DL:64 kbps / PS RAB + Interactive or background / UL:64 DL:64 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128.](#)

[18.2.2.57.2.1 Conformance requirement](#)

[See 18.2.2.4.1.](#)

[18.2.2.57.2.2 Test purpose](#)

[Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.57 for the uplink payload 128 case.](#)

[18.2.2.57.2.3 Method of test](#)

[See 18.2.1.2 for test procedure.](#)

[Uplink TFS:](#)

	TFI	RB5 + RB6 (64 kbps RAB)	DCCH
TFS	TF0, bits	0x148	0x148
	TF1, bits	1x148	1x148
	TF2, bits	3x148	N/A
	TF3, bits	7x148	N/A
	TF4, bits	10x148	N/A

[Uplink TFCS:](#)

TFCI	(RB5 + RB6, DCCH)
UL_TFC0	(TF0, TF0)
UL_TFC1	(TF1, TF0)
UL_TFC2	(TF2, TF0)
UL_TFC3	(TF3, TF0)
UL_TFC4	(TF4, TF0)
UL_TFC5	(TF0, TF1)
UL_TFC6	(TF1, TF1)
UL_TFC7	(TF2, TF1)
UL_TFC8	(TF3, TF1)
UL_TFC9	(TF4, TF1)

[Downlink TFS:](#)

	TFI	RB5 + RB6 (64 kbps RAB)	DCCH
TFS	TF0, bits	0x340	0x148
	TF1, bits	1x340	1x148
	TF2, bits	2x340	N/A
	TF3, bits	3x340	N/A
	TF4, bits	4x340	N/A

[Downlink TFCS:](#)

<u>TFCI</u>	<u>(RB5 + RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF0)</u>
<u>DL_TFC3</u>	<u>(TF3, TF0)</u>
<u>DL_TFC4</u>	<u>(TF4, TF0)</u>
<u>DL_TFC5</u>	<u>(TF0, TF1)</u>
<u>DL_TFC6</u>	<u>(TF1, TF1)</u>
<u>DL_TFC7</u>	<u>(TF2, TF1)</u>
<u>DL_TFC8</u>	<u>(TF3, TF1)</u>
<u>DL_TFC9</u>	<u>(TF4, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
<u>1</u>	<u>DL_TFC1</u> <u>DL_TFC6</u>	<u>UL_TFC1</u> <u>DL_TFC6</u>	<u>DL_TFC0,</u> <u>DL_TFC5,</u> <u>UL_TFC0,</u> <u>UL_TFC5</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC5,</u> <u>UL_TFC6</u>	<u>RB5: 120</u> <u>RB6: 120</u>	<u>RB5: 312</u> <u>RB6: No data</u>
<u>2</u>	<u>DL_TFC2</u> <u>DL_TFC7</u>	<u>UL_TFC2</u> <u>DL_TFC7</u>	<u>DL_TFC0,</u> <u>DL_TFC5,</u> <u>UL_TFC0,</u> <u>UL_TFC5</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC5,</u> <u>UL_TFC7</u>	<u>RB5: 376</u> <u>RB6: 376</u>	<u>RB5: 632</u> <u>RB6: No data</u>
<u>3</u>	<u>DL_TFC3</u> <u>DL_TFC8</u>	<u>UL_TFC3</u> <u>DL_TFC8</u>	<u>DL_TFC0,</u> <u>DL_TFC5,</u> <u>UL_TFC0,</u> <u>UL_TFC5</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC3,</u> <u>UL_TFC5,</u> <u>UL_TFC8</u>	<u>RB5: 888</u> <u>RB6: 888</u>	<u>RB5: 952</u> <u>RB6: No data</u>
<u>4</u>	<u>DL_TFC4</u> <u>DL_TFC9</u>	<u>UL_TFC4</u> <u>DL_TFC9</u>	<u>DL_TFC0,</u> <u>DL_TFC5,</u> <u>UL_TFC0,</u> <u>UL_TFC5</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC9</u>	<u>RB5: 1272</u> <u>RB6: 1272</u>	<u>RB5: 1272</u> <u>RB6: No data</u>
<u>5</u>	<u>DL_TFC4</u> <u>DL_TFC9</u>	<u>UL_TFC4</u> <u>DL_TFC9</u>	<u>DL_TFC0,</u> <u>DL_TFC5,</u> <u>UL_TFC0,</u> <u>UL_TFC5</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC9</u>	<u>RB5: 1272</u> <u>RB5: 1272</u>	<u>RB5: No data</u> <u>RB6: 1272</u>

NOTE 1: UL_TFC0, UL_TFC1 and UL_TFC5 are part of minimum set of TFCIs.

NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.

RB5 and RB6: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).

18.2.2.57.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the content equal to the first 120 bits of the test data sent by SS in downlink; and no data shall be received on RB6.
 - for sub-test 2: an RLC SDU on RB5 having the content equal to the first 376 bits of the test data sent by SS in downlink; and no data shall be received on RB6.

- for sub-test 3: an RLC SDU on RB5 having the content equal to the first 888 bits of the test data sent by SS in downlink; and no data shall be received on RB6.
- for sub-test 4: an RLC SDU on RB5 having the same content as the test data sent by SS in downlink; and no data shall be received on RB6.
- for sub-test 5: an RLC SDU on RB6 having the same content as the test data sent by SS in downlink; and no data shall be received on RB5..

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.58 Streaming / unknown / UL:16 DL:64 kbps / PS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH.

18.2.2.58.1 Streaming / unknown / UL:16 DL:64 kbps / PS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320, TTI 40ms.

18.2.2.58.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.58.1.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.58 for the uplink payload 320, and TTI 40ms case.

18.2.2.58.1.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 uplink (16 kbps, 20 ms TTI)</u>	<u>RB6 (8 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x336</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x336</u>	<u>1x336</u>	<u>1x148</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF1, TF0)</u>
<u>UL_TFC3</u>	<u>(TF1, TF1, TF0)</u>
<u>UL_TFC4</u>	<u>(TF0, TF0, TF1)</u>
<u>UL_TFC5</u>	<u>(TF1, TF0, TF1)</u>
<u>UL_TFC6</u>	<u>(TF0, TF1, TF1)</u>
<u>UL_TFC7</u>	<u>(TF1, TF1, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5 (64 kbps, 40 ms TTI)</u>	<u>RB6 (8 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x656</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x656</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>2x656</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>4x656</u>	<u>N/A</u>	<u>N/A</u>

Downlink TFCs:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF3, TF0, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC5</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC6</u>	<u>(TF2, TF1, TF0)</u>
<u>DL_TFC7</u>	<u>(TF3, TF1, TF0)</u>
<u>DL_TFC8</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC9</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC10</u>	<u>(TF2, TF0, TF1)</u>
<u>DL_TFC11</u>	<u>(TF3, TF0, TF1)</u>
<u>DL_TFC12</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC13</u>	<u>(TF1, TF1, TF1)</u>
<u>DL_TFC14</u>	<u>(TF2, TF1, TF1)</u>
<u>DL_TFC15</u>	<u>(TF3, TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
1	DL_TFC1 , DL_TFC9	UL_TFC1 , UL_TFC5	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC4	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC4 , UL_TFC5	RB5: 632 RB6: 312	RB5: 632 RB6: no data
2	DL_TFC2 , DL_TFC10	UL_TFC1 , UL_TFC5	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC4	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC4 , UL_TFC5	RB5: 632 RB6: 312	RB5: 1272 RB6: no data
3	DL_TFC3 , DL_TFC11	UL_TFC1 , UL_TFC5	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC4	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC4 , UL_TFC5	RB5: 632 RB6: 312	RB5: 2552 RB6: no data
4	DL_TFC4 , DL_TFC12	UL_TFC2 , UL_TFC6	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC4	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC4 , UL_TFC6	RB5: 632 RB6: 312	RB5: no data RB6: 312
5	DL_TFC5 , DL_TFC13	UL_TFC3 , UL_TFC7	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC4	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC4 , UL_TFC5 , UL_TFC6	RB5: 632 RB6: 312	RB5: 632 RB6: 312
6	DL_TFC6 , DL_TFC14	UL_TFC3 , UL_TFC7	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC4	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC4 , UL_TFC5 , UL_TFC6	RB5: 632 RB6: 312	RB5: 1272 RB6: 312
7	DL_TFC7 , DL_TFC15	UL_TFC3 , UL_TFC7	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC4	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC4 , UL_TFC5 , UL_TFC6 , UL_TFC7	RB5: 632 RB6: 312	RB5: 2552 RB6: 312

NOTE 1: [UL_TFC0](#), [UL_TFC1](#) and [UL_TFC4](#) are part of minimum set of TFCs.

NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.

[RB5](#): Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the uplink TTI for [RB5](#) is 20 ms while the downlink TTI is 40 ms then, to achieve continous data transmission in uplink the size of the uplink RLC SDU has been set such that it will be transmitted over two subsequent TTIs, i.e. UL RLC SDU size has been set to two times the payload size of the UL TF under test minus 8 bits (the size of a 7 bit length indicator and expansion bit).

[RB6](#): Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size has been set equal to the uplink TFS size minus 8 bits (the size of a 7 bit length indicator and expansion bit).

[18.2.2.58.1.4](#) Test requirements

See [18.2.1.2](#) for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by the SS; and no data shall be received on RB6.
 - for sub-test 2 and 3: an RLC SDU on RB5 having the content equal to the first 632 bits of the test data sent by SS in downlink; and no data shall be received on RB6.
 - for sub-test 4: RLC SDUs on RB6 having the same content as sent by the SS; and no data shall be received on RB5.
 - for sub-test 5: RLC SDUs on RB5 and RB6 having the same content as sent by the SS.
 - for sub-test 6 and 7: RLC SDUs on RB5 having the content equal to the first 632 bits of the test data sent by theSS in downlink; and RLC SDUs on RB6 having the same content as sent by the SS.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.58.2 Streaming / unknown / UL:16 DL:64 kbps / PS RAB + Interactive or background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128, TTI 80ms.

18.2.2.58.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.58.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.58 for the uplink payload 128, and TTI 80ms case.

18.2.2.58.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 uplink (16 kbps, 20 ms TTI)</u>	<u>RB6 (8 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x336</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x336</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>5x144</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF1, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF2, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF1, TF2, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF1)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF1)</u>
<u>UL_TFC8</u>	<u>(TF0, TF1, TF1)</u>
<u>UL_TFC9</u>	<u>(TF0, TF2, TF1)</u>
<u>UL_TFC10</u>	<u>(TF1, TF1, TF1)</u>
<u>UL_TFC11</u>	<u>(TF1, TF2, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5 (64 kbps, 40 ms TTI)</u>	<u>RB6 (8 kbps)</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x656</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x656</u>	<u>1x336</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>2x656</u>	<u>N/A</u>	<u>N/A</u>
	<u>TF3, bits</u>	<u>4x656</u>	<u>N/A</u>	<u>N/A</u>

Downlink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC2</u>	<u>(TF2, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF3, TF0, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC5</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC6</u>	<u>(TF2, TF1, TF0)</u>
<u>DL_TFC7</u>	<u>(TF3, TF1, TF0)</u>
<u>DL_TFC8</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC9</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC10</u>	<u>(TF2, TF0, TF1)</u>
<u>DL_TFC11</u>	<u>(TF3, TF0, TF1)</u>
<u>DL_TFC12</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC13</u>	<u>(TF1, TF1, TF1)</u>
<u>DL_TFC14</u>	<u>(TF2, TF1, TF1)</u>
<u>DL_TFC15</u>	<u>(TF3, TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
1	DL_TFC1 , DL_TFC9	UL_TFC1 , UL_TFC7	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC6	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC6 , UL_TFC7 , UL_TFC8	RB5: 632 RB6: 56	RB5: 632 RB6: no data
2	DL_TFC2 , DL_TFC10	UL_TFC1 , UL_TFC7	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC6	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC6 , UL_TFC7 , UL_TFC8	RB5: 632 RB6: 56	RB5: 1272 RB6: no data
3	DL_TFC3 , DL_TFC11	UL_TFC1 , UL_TFC7	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC6	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC6 , UL_TFC7 , UL_TFC8	RB5: 632 RB6: 56	RB5: 2552 RB6: no data
4	DL_TFC4 , DL_TFC12	UL_TFC2 , UL_TFC8	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC6	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC6 , UL_TFC7 , UL_TFC8	RB5: 632 RB6: 56	RB5: no data RB6: 312
5	DL_TFC4 , DL_TFC12	UL_TFC3 , UL_TFC9	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC6	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC3 , UL_TFC6 , UL_TFC7 , UL_TFC8 , UL_TFC9	RB5: 632 RB6: 312	RB5: no data RB6: 312
6	DL_TFC5 , DL_TFC13	UL_TFC4 , UL_TFC10	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC6	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC4 , UL_TFC6 , UL_TFC7 , UL_TFC8 , UL_TFC10	RB5: 632 RB6: 56	RB5: 632 RB6: 312
7	DL_TFC6 , DL_TFC14	UL_TFC5 , UL_TFC11	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC6	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC5 , UL_TFC6 , UL_TFC7 , UL_TFC8 , UL_TFC11	RB5: 632 RB6: 312	RB5: 1272 RB6: 312
8	DL_TFC7 , DL_TFC15	UL_TFC5 , UL_TFC11	DL_TFC0 , DL_TFC8 , UL_TFC0 , UL_TFC6	UL_TFC0 , UL_TFC1 , UL_TFC2 , UL_TFC5 , UL_TFC6 , UL_TFC7 , UL_TFC8 , UL_TFC11	RB5: 632 RB6: 312	RB5: 2552 RB6: 312

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs</u> (note 1)	<u>UL RLC SDU size (bits)</u> (note 2)	<u>Test data size (bits)</u> (note 2)
NOTE 1: UL TFC0, UL TFC1, UL TFC2, UL TFC6, UL TFC7, and UL TFC8 are part of minimum set of TFCs.						
NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs. RB5: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). As the uplink TTI for RB5 is 20 ms while the downlink TTI is 40 ms then, to achieve continous data transmission in uplink the size of the uplink RLC SDU has been set such that it will be transmitted over two subsequent TTIs, i.e. UL RLC SDU size has been set to two times the payload size of the UL TF under test minus 8 bits (the size of a 7 bit length indicator and expansion bit). RB6: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size has been set equal to the uplink TFS size minus 8 bits (the size of a 7 bit length indicator and expansion bit) divided by 2 (80ms TTI / 40ms TTI).						

18.2.2.58.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by the SS; and no data shall be received on RB6.
 - for sub-test 2 and 3: an RLC SDU on RB5 having the content equal to the first 632 bits of the test data sent by SS in downlink; and no data shall be received on RB6.
 - for sub-test 4: two RLC SDUs on RB6 having the content equal to the first 56 bits of the test data sent by SS in downlink; and no data shall be received on RB5.
 - for sub-test 5: two RLC SDUs on RB6 having the same content as sent by the SS; and no data shall be received on RB5.
 - for sub-test 6: an RLC SDU on RB5 having the same content as sent by the SS; and two RLC SDUs on RB6 having the content equal to the first 56 bits of the test data sent by SS in downlink.
 - for sub-test 7 and 8: an RLC SDU on RB5 having the content equal to the first 632 bits of the test data sent by the SS in downlink; and an RLC SDU on RB6 having the same content as sent by the SS.
5. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.59 Reserved for future use

18.2.2.60 Reserved for future use

18.2.2.61 Conversational / unknown / UL:8 DL:8 kbps / PS RAB + Interactive or Background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH

18.2.2.61.1 Conversational / unknown / UL:8 DL:8 kbps / PS RAB + Interactive or Background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 320, TTI 40ms

18.2.2.61.1.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.61.1.2 Test purpose

[Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.56 for the uplink payload 320, and TTI 40ms case.](#)

18.2.2.61.1.3 Method of test

[See 18.2.1.2 for test procedure.](#)

[Uplink TFS:](#)

	TFI	RB5 Conversational	RB6 Inter or Back	DCCH
TFS	TF0, bits	0x328	0x336	0x148
	TF1, bits	1x328	1x336	1x148

[Uplink TFCS:](#)

TFCI	(8 kbps Conversational RAB, 8 kbps I/B RAB, DCCH)
UL_TFC0	(TF0, TF0, TF0)
UL_TFC1	(TF0, TF1, TF0)
UL_TFC2	(TF1, TF0, TF0)
UL_TFC3	(TF1, TF1, TF0)
UL_TFC4	(TF0, TF0, TF1)
UL_TFC5	(TF0, TF1, TF1)
UL_TFC6	(TF1, TF0, TF1)
UL_TFC7	(TF1, TF1, TF1)

[Downlink TFS:](#)

	TFI	RB5 Conversational	RB6 Inter or Back	DCCH
TFS	TF0, bits	0x328	0x336	0x148
	TF1, bits	1x328	1x336	1x148

[Downlink TFCS:](#)

TFCI	(8 kbps Conversational RAB, 8 kbps I/B RAB, DCCH)
DL_TFC0	(TF0, TF0, TF0)
DL_TFC1	(TF0, TF1, TF0)
DL_TFC2	(TF1, TF0, TF0)
DL_TFC3	(TF1, TF1, TF0)
DL_TFC4	(TF0, TF0, TF1)
DL_TFC5	(TF0, TF1, TF1)
DL_TFC6	(TF1, TF0, TF1)
DL_TFC7	(TF1, TF1, TF1)

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCIs (note 1)</u>	<u>UL RLC SDU size (bits) (note 2)</u>	<u>Test data size (bits) (note 2)</u>
1	<u>DL_TFC1</u> <u>DL_TFC5,</u>	<u>UL_TFC1</u> <u>UL_TFC5</u>	<u>DL_TFC0, DL_TFC4,</u> <u>UL_TFC0, UL_TFC4,</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC4,</u> <u>UL_TFC5</u>	<u>RB5: 312</u> <u>RB6: 312</u>	<u>RB5: 312</u> <u>RB6: no data</u>
2	<u>DL_TFC2</u> <u>DL_TFC6,</u>	<u>UL_TFC2</u> <u>UL_TFC6</u>	<u>DL_TFC0, DL_TFC4,</u> <u>UL_TFC0, UL_TFC4,</u>	<u>UL_TFC0,</u> <u>UL_TFC2,</u> <u>UL_TFC4,</u> <u>UL_TFC6</u>	<u>RB5: 312</u> <u>RB6: 312</u>	<u>RB5: no data</u> <u>RB6: 312</u>
3	<u>DL_TFC3</u> <u>DL_TFC7,</u>	<u>UL_TFC3</u> <u>UL_TFC7</u>	<u>DL_TFC0, DL_TFC4,</u> <u>UL_TFC0, UL_TFC4,</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC3,</u> <u>UL_TFC4,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7</u>	<u>RB5: 312</u> <u>RB6: 312</u>	<u>RB5: 312</u> <u>RB6: 312</u>
<p>NOTE 1: <u>UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC6, UL_TFC7, and UL_TFC8 are part of minimum set of TFCIs.</u></p> <p>NOTE 2: <u>See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB5 and RB6: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.61.1.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCIs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6.
 - for sub-test 2: an RLC SDU on RB6; and no data shall be received on RB5.
4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

18.2.2.61.2 Conversational / unknown / UL:8 DL:8 kbps / PS RAB + Interactive or Background / UL:8 DL:8 kbps / PS RAB + UL:3.4 DL:3.4 kbps SRBs for DCCH / Payload 128, TTI 80ms

18.2.2.61.2.1 Conformance requirement

See 18.2.2.4.1.

18.2.2.61.2.2 Test purpose

Test to verify establishment and data transfer of reference radio bearer configuration as specified in TS 34.108, clause 6.10.3.4.1.56 for the uplink payload 128, and TTI 80ms case.

18.2.2.61.2.3 Method of test

See 18.2.1.2 for test procedure.

Uplink TFS:

	<u>TFI</u>	<u>RB5 Conversational</u>	<u>RB6 Inter or Back</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x328</u>	<u>0x144</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x328</u>	<u>1x144</u>	<u>1x148</u>
	<u>TF2, bits</u>	<u>N/A</u>	<u>5x144</u>	<u>N/A</u>

Uplink TFCS:

<u>TFCI</u>	<u>(RB5, RB6, DCCH)</u>
<u>UL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>UL_TFC1</u>	<u>(TF1, TF0, TF0)</u>
<u>UL_TFC2</u>	<u>(TF0, TF1, TF0)</u>
<u>UL_TFC3</u>	<u>(TF0, TF2, TF0)</u>
<u>UL_TFC4</u>	<u>(TF1, TF1, TF0)</u>
<u>UL_TFC5</u>	<u>(TF1, TF2, TF0)</u>
<u>UL_TFC6</u>	<u>(TF0, TF0, TF1)</u>
<u>UL_TFC7</u>	<u>(TF1, TF0, TF1)</u>
<u>UL_TFC8</u>	<u>(TF0, TF1, TF1)</u>
<u>UL_TFC9</u>	<u>(TF0, TF2, TF1)</u>
<u>UL_TFC10</u>	<u>(TF1, TF1, TF1)</u>
<u>UL_TFC11</u>	<u>(TF1, TF2, TF1)</u>

Downlink TFS:

	<u>TFI</u>	<u>RB5 Conversational</u>	<u>RB6 Inter or Back</u>	<u>DCCH</u>
<u>TFS</u>	<u>TF0, bits</u>	<u>0x328</u>	<u>0x336</u>	<u>0x148</u>
	<u>TF1, bits</u>	<u>1x328</u>	<u>1x336</u>	<u>1x148</u>

Downlink TFCS:

<u>TFCI</u>	<u>(8 kbps Conversational RAB, 8 kbps I/B RAB, DCCH)</u>
<u>DL_TFC0</u>	<u>(TF0, TF0, TF0)</u>
<u>DL_TFC1</u>	<u>(TF0, TF1, TF0)</u>
<u>DL_TFC2</u>	<u>(TF1, TF0, TF0)</u>
<u>DL_TFC3</u>	<u>(TF1, TF1, TF0)</u>
<u>DL_TFC4</u>	<u>(TF0, TF0, TF1)</u>
<u>DL_TFC5</u>	<u>(TF0, TF1, TF1)</u>
<u>DL_TFC6</u>	<u>(TF1, TF0, TF1)</u>
<u>DL_TFC7</u>	<u>(TF1, TF1, TF1)</u>

Sub-tests:

<u>Sub-test</u>	<u>Downlink TFCs Under Test</u>	<u>Uplink TFCs Under test</u>	<u>Implicitely tested</u>	<u>Restricted UL TFCs (note 1)</u>	<u>UL RLC SDU size (bits) (note 2)</u>	<u>Test data size (bits) (note 2)</u>
1	<u>DL_TFC1</u> <u>DL_TFC5,</u>	<u>UL_TFC1</u> <u>UL_TFC7</u>	<u>DL_TFC0, DL_TFC4,</u> <u>UL_TFC0, UL_TFC6,</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC8</u>	<u>RB5: 312</u> <u>RB6: 56</u>	<u>RB5: 312</u> <u>RB6: no data</u>
2	<u>DL_TFC2</u> <u>DL_TFC6,</u>	<u>UL_TFC2</u> <u>UL_TFC8</u>	<u>DL_TFC0, DL_TFC4,</u> <u>UL_TFC0, UL_TFC6,</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC8</u>	<u>RB5: 312</u> <u>RB6: 56</u>	<u>RB5: no data</u> <u>RB6: 312</u>
3	<u>DL_TFC2</u> <u>DL_TFC6,</u>	<u>UL_TFC3</u> <u>UL_TFC9</u>	<u>DL_TFC0, DL_TFC4,</u> <u>UL_TFC0, UL_TFC6,</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC8,</u> <u>UL_TFC9</u>	<u>RB5: 312</u> <u>RB6: 312</u>	<u>RB5: no data</u> <u>RB6: 312</u>
4	<u>DL_TFC3</u> <u>DL_TFC7,</u>	<u>UL_TFC4</u> <u>UL_TFC1</u> <u>0</u>	<u>DL_TFC0, DL_TFC4,</u> <u>UL_TFC0, UL_TFC6,</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC4,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC8,</u> <u>UL_TFC10</u>	<u>RB5: 312</u> <u>RB6: 56</u>	<u>RB5: 312</u> <u>RB6: 312</u>
5	<u>DL_TFC3</u> <u>DL_TFC7,</u>	<u>UL_TFC5</u> <u>UL_TFC1</u> <u>1</u>	<u>DL_TFC0, DL_TFC4,</u> <u>UL_TFC0, UL_TFC6,</u>	<u>UL_TFC0,</u> <u>UL_TFC1,</u> <u>UL_TFC2,</u> <u>UL_TFC5,</u> <u>UL_TFC6,</u> <u>UL_TFC7,</u> <u>UL_TFC8,</u> <u>UL_TFC11</u>	<u>RB5: 312</u> <u>RB6: 312</u>	<u>RB5: 312</u> <u>RB6: 312</u>
<p><u>NOTE 1: UL_TFC0, UL_TFC1, UL_TFC2, UL_TFC6, UL_TFC7, and UL_TFC8 are part of minimum set of TFCs.</u></p> <p><u>NOTE 2: See TS 34.109 [10] clause 5.3.2.6.2 for details regarding loopback of RLC SDUs.</u> <u>RB5 and RB6: Test data size has been set to the payload size of the DL TF under test minus 8 bits (size of 7 bit length indicator and expansion bit). The UL RLC SDU size has been set equal to the size of the payload size of the UL TF under test minus 8 bits (the size of 7 bit length indicator and expansion bit).</u></p>						

18.2.2.61.2.4 Test requirements

See 18.2.1.2 for definition of step 10 and step 15.

1. At step 10 the UE shall send RADIO BEARER SETUP COMPLETE.
2. At step 15a and step 15b the UE transmitted transport format shall be within the set of restricted TFCs as specified for the actual sub-test.
3. At step 15a and step 15b the UE shall return
 - for sub-test 1: an RLC SDU on RB5 having the same content as sent by SS; and no data shall be received on RB6.
 - for sub-test 2: two RLC SDUs on RB6 having the content equal to the first 56 bits of the test data sent by SS in downlink; and no data shall be received on RB5.

- for sub-test 3: two RLC SDUs on RB6 having the same content as sent by the SS; and no data shall be received on RB5.
- for sub-test 4: an RLC SDU on RB5 having the same content as sent by the SS; and two RLC SDUs on RB6 having the content equal to the first 56 bits of the test data sent by SS in downlink.
- for sub-test 5: an RLC SDU on RB5 having the same content as sent by the SS; and two RLC SDUs on RB6 having the same content as sent by SS in downlink.

4. At step 15b the UE shall send at least one MEASUREMENT REPORT message.

CHANGE REQUEST

34.123-1 CR 1240 rev - Current version: 5.11.1

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the symbols.

Proposed change affects: UICC apps ME Radio Access Network Core Network

Title:	Add TDD to RRC test case 8.4.1.33		
Source:	3GPP TSG RAN WG5 (Testing)		
Work item code:	TEI	Date:	12/04/2005
Category:	F	Release:	Rel-5
	Use <u>one</u> of the following categories: F (correction) A (corresponds to a correction in an earlier release) B (addition of feature), C (functional modification of feature) D (editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900 .		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6)

Reason for change:	TDD not in this test
Summary of change:	1. Add TDD levels table on RF power 2. TDD added to MEASUREMENT CONTROL (Step 4)
Consequences if not approved:	TC might fail a conformant UE.

Clauses affected:	8.4.1.33						
Other specs affected:	<table border="1" style="display: inline-table; border-collapse: collapse;"> <tr> <td style="width: 20px; text-align: center;">Y</td> <td style="width: 20px; text-align: center;">N</td> </tr> <tr> <td style="text-align: center;"><input type="checkbox"/></td> <td style="text-align: center;"><input checked="" type="checkbox"/></td> </tr> </table> Other core specifications	Y	N	<input type="checkbox"/>	<input checked="" type="checkbox"/>		
Y	N						
<input type="checkbox"/>	<input checked="" type="checkbox"/>						
	<table border="1" style="display: inline-table; border-collapse: collapse;"> <tr> <td style="width: 20px; text-align: center;">X</td> </tr> <tr> <td style="text-align: center;"><input checked="" type="checkbox"/></td> </tr> </table> Test specifications	X	<input checked="" type="checkbox"/>				
X							
<input checked="" type="checkbox"/>							
	<table border="1" style="display: inline-table; border-collapse: collapse;"> <tr> <td style="width: 20px; text-align: center;">X</td> </tr> <tr> <td style="text-align: center;"><input checked="" type="checkbox"/></td> </tr> </table> O&M Specifications	X	<input checked="" type="checkbox"/>				
X							
<input checked="" type="checkbox"/>							
Other comments:							

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <http://www.3gpp.org/specs/CR.htm>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked contain pop-up help information about the field that they are closest to.

- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <ftp://ftp.3gpp.org/specs/> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

8.4.1.33 Measurement Control and Report: Inter-RAT measurement, event 3a

8.4.1.33.1 Definition

8.4.1.33.2 Conformance requirement

1. When this event is ordered by UTRAN in a MEASUREMENT CONTROL message the UE shall send a report when the estimated quality of the currently used frequency is below the value of the IE "Threshold own system" and the hysteresis and time to trigger conditions are fulfilled and the estimated quality of the other system is above the value of the IE "Threshold other system" and the hysteresis and time to trigger conditions are fulfilled.
2. If the IE "DPCH Compressed Mode Status Info" is present, [in the MEASUREMENT CONTROL message]:
 - after the time indicated by IE "TGPS reconfiguration CFN" has elapsed:
 - activate the pattern sequence stored in the variable TGPS_IDENTITY corresponding to each IE "TGPSI" for which the "TGPS status flag" is set to "activate" at the time indicated by IE "TGCFN"; and
 - begin the inter-frequency and/or inter-RAT measurements corresponding to the pattern sequence measurement purpose of each activated pattern sequence;
 - if the values of IE "TGPS reconfiguration CFN" and IE "TGCFN" are equal:
 - start the concerned pattern sequence immediately at that CFN;
 - not alter pattern sequences stored in variable TGPS_IDENTITY, but not identified in IE "TGPSI"
3. The UE shall perform GSM RSSI measurements in the gaps of compressed mode pattern sequence specified for GSM RSSI measurement purpose. The UE shall perform Initial BSIC identification in compressed mode pattern sequence specified for Initial BSIC identification measurement purpose. The UE shall be able to measure the "Observed time difference to GSM cell" during a compressed mode pattern sequence configured for this purpose. The UE shall perform BSIC re-confirmation in compressed mode pattern sequence specified for BSIC re-confirmation measurement purpose.
4. If the IE "Inter-RAT measurement quantity" is received in a MEASUREMENT CONTROL message and CHOICE system is GSM, the UE shall:
 - if IE "BSIC verification required" is set to "required", for cells that match any of the BCCH ARFCN and BSIC combinations in the list of inter-RAT cells that the UE has received in IE "Inter-RAT cell info list", and that has a "verified" BSIC:
 - report measurement quantities according to IE "inter-RAT reporting quantity" taking into account the restrictions defined in TS 25.331 clause 8.6.7.6;
 - trigger inter-RAT events according to IE "inter-RAT measurement reporting criteria"; and
 - perform event evaluation for event-triggered reporting after BSIC has been verified for a GSM cell
 - indicate non-verified BSIC for a GSM cell in the "Inter-RAT measured results list" IE
5. The UE shall include measured results in MEASUREMENT REPORT as specified in the IE "Inter-RAT reporting quantity".
6. If IE "Observed time difference to GSM cell Reporting indicator" is set to "TRUE" [, the UE shall]:
 - include optional IE "Observed time difference to GSM cell" with the value set to the time difference to that GSM cell for the GSM cells that have a BSIC that is "verified", and that match any of the BCCH ARFCN and BSIC combinations in the list of inter-RAT cells that the UE has received in IE "Inter-RAT cell info list".
 - if IE "GSM Carrier RSSI" is set to "TRUE"[, the UE shall]:

- include optional IE "GSM Carrier RSSI" with a value set to the measured RXLEV to that GSM cell in IE "Inter-RAT measured results list".
 - if the BSIC of reported GSM cell is "verified"[, the UE shall]:
 - set the CHOICE BSIC to "Verified BSIC" and IE "inter-RAT cell id" to the value that GSM cell had in the IE "Inter-RAT cell info list";
7. If the IE "Reporting Cell Status" is received, the UE shall set the IE "Measured Results" in MEASUREMENT REPORT as follows.
- the maximum number of the IE "Cell Measured Results" to be included in the IE "Measured Results" is the number specified in "Reporting Cell Status".

Reference

3GPP TS 25.331 clauses 8.4.1.3, 8.6.7.5, 8.6.7.6, 8.6.7.9, 14.3.1.1, 14.3.2.1, 14.3.2.2, 14.3.2.3.

8.4.1.33.3 Test Purpose

1. To confirm that the UE starts compressed mode and inter-RAT measurements when so required by the network in a MEASUREMENT CONTROL message.
2. To confirm that the UE sends MEASUREMENT REPORT message if event 3a is configured, if the quality of the currently used UTRAN frequency is below a given threshold and the estimated quality of the other system is above a certain threshold.
3. To confirm that the hysteresis and time to trigger behaviours for event 3a are correctly implemented.
4. To confirm that the UE verifies the BSIC of the cell triggering the event if so required by UTRAN and if the proper compressed mode patterns have been configured in the UE by UTRAN.
5. To confirm that the content of the MEASUREMENT REPORT sent by the UE is according to what was required by UTRAN.

NOTE: Test purpose 1 verifies conformance requirement 1 and 2.

NOTE: Test purpose 2 and 3 verifies conformance requirement 1.

NOTE: Test purpose 4 verifies conformance requirement 2, 3 and 4.

NOTE: Test purpose 5 verifies conformance requirement 4, 5, 6 and 7.

8.4.1.33.4 Method of test

Initial Condition

System simulator: 1 UTRAN FDD cell and 3 GSM cells. The initial configurations of the 3 cells in the SS shall follow the values indicated in the column marked T0. The table is found in "Test procedure".

UE: CELL_DCH state, state 6-9 as specified in clause 7.4 of TS 34.108.

Related ICS/IXIT statements

- Compressed mode required yes/no

Test procedure

Table 8.4.1.33.4-1

	Parameter	Unit	Cell 1 (GSM)					Cell 2 (GSM)					Cell 3 (GSM)				
			T0	T1	T2	T3	T4	T0	T1	T2	T3	T4	T0	T1	T2	T3	T4
Test Channel	#		GSM Ch.1					GSM Ch.2					GSM Ch.3				
BCCH ARFCN	#		1					7					39				
CELL identity	#		0					1					2				
BSIC	#		BSIC 1					BSIC 2					BSIC 3				
RF Signal Level	dBm		-85	-85	-70	-76	-70	-85	-85	-85	-84	-84	-90	-90	-90	-90	-90

Table 8.4.1.33.4-2

Parameter	Unit	Cell 1 (UTRA)				
		T0	T1	T2	T3	T4
UTRA RF Channel Number		Ch.1				
CPICH Ec (FDD)	dBm /3.84 Mhz	-60	-80	-80	-80	-60
P-CCPCH (TDD)	dBm	-60	-80	-80	-80	-60

The two tables above illustrate the downlink power to be applied for the two cells at various instants of the test execution. Column marked "T0" denotes the initial conditions, while columns marked "T1", "T2", "T3" and "T4" indicate the values to be applied subsequently.

The UE is initially in CELL_DCH, state 6-9 as specified in clause 7.4 of TS 34.108. UTRA cell 1 is the only cell in the active set of the UE. If the UE requires compressed mode (refer ICS/IXIT), the SS sends a PHYSICAL CHANNEL RECONFIGURATION message to the UE to configure the compressed mode pattern sequence parameters to the UE. Three compressed mode patterns are configured, according to the message specified below. When the PHYSICAL CHANNEL RECONFIGURATION COMPLETE is received from the UE, the SS sends a MEASUREMENT CONTROL message to the UE, to set up inter-RAT measurements. Event 3a is set up in this message, and if the UE requires compressed mode (refer ICS/IXIT), compressed mode is activated.

At instant T1, the CPICH Ec drops as described in table 8.4.1.33.4-2.

At instant T2, the RF signal for GSM cell 1 increases, and crosses the threshold for the other system defined for event 3a.

After reception of the MEASUREMENT REPORT message, at instant T3, the RF signal strength for GSM cell 2 increases but remains below the threshold for the other system for event 3a. During that time, the RF signal strength for GSM cell 1 decreases, but remains above the releasing condition for event 3a.

At instant T4, the RF signal strength for GSM cell 1 increases above the threshold for the other system for event 3a+hysteresis. SS calls for generic procedure C.3 to check that UE is in CELL_DCH state.

Expected Sequence

Step	Direction		Message	Comment
	UE	SS		
1				The UE is brought to the CELL_DCH state in the cell 1. If the UE does not require compressed mode (refer ICS/IXIT), then goto step 4.
2		←	PHYSICAL CHANNEL RECONFIGURATION	Compressed mode pattern sequence parameters are loaded to UE.
3		→	PHYSICAL CHANNEL RECONFIGURATION COMPLETE	
4		←	MEASUREMENT CONTROL	SS configures event 3a in the UE. If the UE requires compressed mode (refer ICS/IXIT), compressed mode is started.
5				SS re-adjusts the downlink transmission power settings according to columns "T1" in tables 8.4.1.33.4-1 and 8.4.1.33.4-2.
6				SS waits for approximately 10 seconds and verifies that no MEASUREMENT REPORT messages are detected on uplink DCCH.
7				SS re-adjusts the downlink transmission power settings according to columns "T2" in tables 8.4.1.33.4-1 and 8.4.1.33.4-2.
8		→	MEASUREMENT REPORT	After about 2s, the UE sends a MEASUREMENT REPORT to SS triggered by event 3a.
9				SS re-adjusts the downlink transmission power settings according to columns "T3" in tables 8.4.1.33.4-1 and 8.4.1.33.4-2.
10				SS waits for approximately 10 seconds and verifies that no MEASUREMENT REPORT messages are detected on uplink DCCH.
11				SS re-adjusts the downlink transmission power settings according to columns "T4" in tables 8.4.1.33.4-1 and 8.4.1.33.4-2.
12				SS waits for approximately 10 seconds and verifies that no MEASUREMENT REPORT messages are detected on uplink DCCH.
13		↔	CALL C.3	If the test result of C.3 indicates that UE is in CELL_DCH state, the test passes, otherwise it fails.

Specific Message Content

PHYSICAL CHANNEL RECONFIGURATION (Step 2)

Use the same message sub-type in Annex A titled "Speech in CS", with the following exceptions:

Information Element	Value/remark
<p>Downlink information common for all radio links</p> <ul style="list-style-type: none"> - DPCH compressed mode info - TGPSI - TGPS Status Flag - TGCFN - Transmission gap pattern sequence <p>configuration parameters</p> <ul style="list-style-type: none"> - TGMP - TGPRC - TGSN - TGL1 - TGL2 - TGD - TGPL1 - TGPL2 - RPP - ITP <p>CHOICE UL/DL Mode</p> <ul style="list-style-type: none"> - Downlink compressed mode method - Uplink compressed mode method <ul style="list-style-type: none"> - Downlink frame type - DeltaSIR1 - DeltaSIRAfter1 - DeltaSIR2 - DeltaSIR2After2 - N identify abort - T Reconfirm abort - TGPSI - TGPS Status Flag - TGCFN - Transmission gap pattern sequence <p>configuration parameters</p> <ul style="list-style-type: none"> - TGMP - TGPRC - TGSN - TGL1 - TGL2 - TGD - TGPL1 - TGPL2 - RPP - ITP <p>CHOICE UL/DL Mode</p> <ul style="list-style-type: none"> - Downlink compressed mode method - Uplink compressed mode method <ul style="list-style-type: none"> - Downlink frame type - DeltaSIR1 - DeltaSIRAfter1 - DeltaSIR2 - DeltaSIR2After2 - N identify abort - T Reconfirm abort - TGPSI - TGPS Status Flag - TGCFN - Transmission gap pattern sequence <p>configuration parameters</p> <ul style="list-style-type: none"> - TGMP - TGPRC 	<p>1</p> <p>Deactivate</p> <p>Not present</p> <p>GSM Carrier RSSI Measurement</p> <p>Infinity</p> <p>4</p> <p>7</p> <p>Not present</p> <p>undefined</p> <p>12</p> <p>Not present</p> <p>Mode 1</p> <p>Mode 0</p> <p>UL&DL or UL-only or DL-only (depends on UE's Measurement capability)</p> <p>SF/2</p> <p>SF/2</p> <p>A</p> <p>1.0</p> <p>0.5</p> <p>Not Present</p> <p>Not Present</p> <p>Not Present</p> <p>Not Present</p> <p>2</p> <p>Deactivate</p> <p>Not present</p> <p>GSM BSIC identification</p> <p>Infinity</p> <p>4</p> <p>7</p> <p>Not present</p> <p>undefined</p> <p>8</p> <p>Not present</p> <p>Mode 1</p> <p>Mode 0</p> <p>UL&DL or UL-only or DL-only (depends on UE's Measurement capability)</p> <p>SF/2</p> <p>SF/2</p> <p>A</p> <p>1.0</p> <p>0.5</p> <p>Not Present</p> <p>Not Present</p> <p>66</p> <p>Not Present</p> <p>3</p> <p>Deactivate</p> <p>Not present</p> <p>GSM BSIC re-confirmation</p> <p>Infinity</p>

- TGSN	4
- TGL1	7
- TGL2	Not present
- TGD	undefined
- TGPL1	8
- TGPL2	Not present
- RPP	Mode 1
- ITP	Mode 0
CHOICE UL/DL Mode	UL&DL or UL-only or DL-only (depends on UE's Measurement capability)
- Downlink compressed mode method	SF/2
- Uplink compressed mode method	SF/2
- Downlink frame type	A
- DeltaSIR1	1.0
- DeltaSIRAfter1	0.5
- DeltaSIR2	Not Present
- DeltaSIR2After2	Not Present
- N identify abort	Not Present
- T Reconfirm abort	5 s

MEASUREMENT CONTROL (Step 4) [\(FDD\)](#)

Information Element	Value/remark
Measurement Identity	3
Measurement Command	Setup
Measurement Reporting Mode	
- Measurement Reporting Transfer Mode	Acknowledged Mode RLC
- Periodic Reporting / Event Trigger Reporting Mode	Event triggered
Additional measurements list	Not Present
CHOICE measurement type	
- inter-RAT measurement	
- inter-RAT measurement object list	
CHOICE Inter-RAT Cell Removal	Remove all inter-RAT cells
- Remove all inter-RAT cells	(No Data)
New inter-RAT cells (1 to <MaxCellMeas>)	MaxCellMeas=3
- inter-RAT cell id	0
CHOICE Radio Access Technology	GSM
- Cell individual offset	0
- Cell selection and re-selection info	Not present
- BSIC	BSIC1
- Band indicator	DCS 1800 band used
- BCCH ARFCN	1
- inter-RAT cell id	1
CHOICE Radio Access Technology	GSM
- Cell individual offset	0
- Cell selection and re-selection info	Not present
- BSIC	BSIC2
- Band indicator	DCS 1800 band used
- BCCH ARFCN	7
- inter-RAT cell id	2
CHOICE Radio Access Technology	GSM
- Cell individual offset	0
- Cell selection and re-selection info	Not present
- BSIC	BSIC3
- Band indicator	DCS 1800 band used
- BCCH ARFCN	39
- Cell for measurement	Not present
- inter-RAT measurement quantity	
- Measurement quantity for UTRAN quality estimate	
- Intra-frequency measurement quantity	
- Filter coefficient	0
- CHOICE mode	FDD
- Measurement quantity	CPICH RSCP
CHOICE system	GSM
- Measurement quantity	GSM carrier RSSI
- Filter coefficient	0

<ul style="list-style-type: none"> - BSIC verification required - inter-RAT reporting quantity CHOICE system <ul style="list-style-type: none"> - Observed time difference to to GSM cell reporting indicator - GSM carrier RSSI reporting indicator CHOICE report criteria <ul style="list-style-type: none"> - Inter-RAT measurements reporting criteria <ul style="list-style-type: none"> - Parameters required for each event (1 to <maxMeasEvent>) <ul style="list-style-type: none"> - Inter-RAT event identity - Threshold own system - W - Threshold other system - Hysteresis - Time to Trigger - Reporting cell status - Maximum number of reported cells 	<p>required</p> <p>GSM FALSE</p> <p>TRUE</p> <p><MaxMeasEvent>=1</p> <p>3a -66 0 -80 5 640 ms</p> <p>Report cells within active set or within virtual active set or of the other RAT</p> <p>2 cells</p> <p>If the UE requires compressed mode (refer ICS/IXIT), this IE is present and contains the IEs as follows. If the UE does not require compressed mode (refer ICS/IXIT), this IE is not present.</p> <p>(Current CFN + (250 – TTI/10msec))mod 256 <MaxTGPS>=3</p> <p>1 Activate (Current CFN + (252 – TTI/10msec))mod 256</p> <p>2 Activate (Current CFN + (254 – TTI/10msec))mod 256</p> <p>3 Activate (Current CFN + (250 – TTI/10msec))mod 256</p>
<p>Physical channel information elements</p> <ul style="list-style-type: none"> - DPCH compressed mode status info <ul style="list-style-type: none"> - TGPS reconfiguration CFN - Transmission gap pattern sequence (1 to <MaxTGPS>) - TGPSI - TGPS status flag - TGCFN - TGPSI - TGPS status flag - TGCFN - TGPSI - TGPS status flag - TGCFN 	

MEASUREMENT CONTROL (Step 4) (TDD)

<u>Information Element</u>	<u>Value/remark</u>
<u>Measurement Identity</u>	3
<u>Measurement Command</u>	<u>Setup</u>
<u>Measurement Reporting Mode</u>	
- <u>Measurement Reporting Transfer Mode</u>	<u>Acknowledged Mode RLC</u>
- <u>Periodic Reporting / Event Trigger Reporting Mode</u>	<u>Event triggered</u>
<u>Additional measurements list</u>	<u>Not Present</u>
<u>CHOICE measurement type</u>	
- <u>inter-RAT measurement</u>	
- <u>inter-RAT measurement object list</u>	
CHOICE <u>Inter-RAT Cell Removal</u>	<u>Remove all inter-RAT cells</u>
- <u>Remove all inter-RAT cells</u>	<u>(No Data)</u>
<u>New inter-RAT cells (1 to <MaxCellMeas>)</u>	<u>MaxCellMeas=3</u>
- <u>inter-RAT cell id</u>	0
CHOICE <u>Radio Access Technology</u>	<u>GSM</u>
- <u>Cell individual offset</u>	0
- <u>Cell selection and re-selection info</u>	<u>Not present</u>
- <u>BSIC</u>	<u>BSIC1</u>
- <u>Band indicator</u>	<u>DCS 1800 band used</u>
- <u>BCCH ARFCN</u>	1
- <u>inter-RAT cell id</u>	1
CHOICE <u>Radio Access Technology</u>	<u>GSM</u>
- <u>Cell individual offset</u>	0
- <u>Cell selection and re-selection info</u>	<u>Not present</u>
- <u>BSIC</u>	<u>BSIC2</u>
- <u>Band indicator</u>	<u>DCS 1800 band used</u>
- <u>BCCH ARFCN</u>	7
- <u>inter-RAT cell id</u>	2

<u>CHOICE Radio Access Technology</u>	<u>GSM</u>
- <u>Cell individual offset</u>	<u>0</u>
- <u>Cell selection and re-selection info</u>	<u>Not present</u>
- <u>BSIC</u>	<u>BSIC3</u>
- <u>Band indicator</u>	<u>DCS 1800 band used</u>
- <u>BCCH ARFCN</u>	<u>39</u>
- <u>Cell for measurement</u>	<u>Not present</u>
- <u>inter-RAT measurement quantity</u>	
- <u>Measurement quantity for UTRAN quality estimate</u>	
- <u>Intra-frequency measurement quantity</u>	
- <u>Filter coefficient</u>	<u>0</u>
- <u>CHOICE mode</u>	<u>TDD</u>
- <u>Measurement quantity</u>	<u>CCPCH RSCP</u>
<u>CHOICE system</u>	<u>GSM</u>
- <u>Measurement quantity</u>	<u>GSM carrier RSSI</u>
- <u>Filter coefficient</u>	<u>0</u>
- <u>BSIC verification required</u>	<u>required</u>
- <u>inter-RAT reporting quantity</u>	
<u>CHOICE system</u>	<u>GSM</u>
- <u>Observed time difference to to GSM cell reporting indicator</u>	<u>FALSE</u>
- <u>GSM carrier RSSI reporting indicator</u>	<u>TRUE</u>
<u>CHOICE report criteria</u>	
- <u>Inter-RAT measurements reporting criteria</u>	
- <u>Parameters required for each event (1 to<maxMeasEvent>)</u>	<u><MaxMeasEvent>=1</u>
- <u>Inter-RAT event identity</u>	<u>3a</u>
- <u>Threshold own system</u>	<u>-66</u>
- <u>W</u>	<u>0</u>
- <u>Threshold other system</u>	<u>-80</u>
- <u>Hysteresis</u>	<u>5</u>
- <u>Time to Trigger</u>	<u>640 ms</u>
- <u>Reporting cell status</u>	<u>Report cells within active set or within virtual active set or of the other RAT</u>
- <u>Maximum number of reported cells</u>	<u>2 cells</u>
<u>Physical channel information elements</u>	

MEASUREMENT REPORT (Step 8)

Information Element	Value/remark
Measurement identity	Check to see if set to 3
Measured Results	
- CHOICE measurement	Check to see if set to "Inter-RAT measured results list"
- Inter-RAT measured result list	
- CHOICE system	GSM
- Measured GSM cells	Check that measurement results for two GSM cells are included
	Check that measurement result is reasonable. RXLEV is mapped to a value between 0 and 63. The RSSI bits are numbered b0 to b5, where b0 is the least significant bit. When mapping the RXLEV value to the RSSI bit string, the first/ leftmost bit of the bit string contains the most significant bit.
- GSM carrier RSSI	Check that measurement result is reasonable
	Verified BSIC
CHOICE BSIC	Check it is set to verified BSIC
- inter-RAT cell id	Check that it is set to 0.
- Observed time difference to GSM cell	Check that not present
- GSM carrier RSSI	Check that measurement result is reasonable
CHOICE BSIC	Verified BSIC
- inter-RAT cell id	Check that is set to 1
- Observed time difference to GSM cell	Check that not present
Measured results on RACH	Check that not present
Additional Measured results	Check that not present
Event results	Check that the IE is included
- CHOICE event result	Check that this is set to inter-RAT measurement event results
	Check that this is set to 3a
- Inter-RAT event identity	Check that this is set to 3a
- Cells to report (1 to <maxCellMeas>)	Check that <maxCellMeas> is set to 1
- CHOICE BSIC	Check that this is set to verified BSIC
- Inter-RAT cell id	Check that this is set to 0.

8.4.1.33.5 Test requirement

The UE shall not send any measurement report between instants T1 and T2.

Event 3a shall be triggered in the UE (i.e.the transmission of the MEASUREMENT REPORT) after instant T2.

Between instants T2 and T3, no MEASUREMENT REPORT message shall be received from the UE (since the hysteresis condition for triggering event 3a is not fulfilled).

No MEASUREMENT REPORT message shall be received from the UE after instant T4 (since the signal strength for cell 1 has not dropped under Threshold for event 3a-hysteresis).

CHANGE REQUEST

34.123-1 CR 1241 rev - Current version: **5.11.1**

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the symbols.

Proposed change affects: | UICC apps ME Radio Access Network Core Network

Title:	Correction to Package 4 RRC test case 8.4.1.26 to change TDD content		
Source:	3GPP TSG RAN WG5 (Testing)		
Work item code:	TEI	Date:	14/04/2005
Category:	F	Release:	Rel-5
	Use <u>one</u> of the following categories: F (correction) A (corresponds to a correction in an earlier release) B (addition of feature), C (functional modification of feature) D (editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900 .		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6)

Reason for change: Section 8.4.1.26.4 of 34.123-1 mentions the downlink power to be applied for Cell A at various time instants of the test execution. Column marked "T0" denotes the initial conditions, while columns marked "T1" are to be applied subsequently. But downlink power to be applied for cell A in columns marked 'T1' (-80db) is well below the threshold set by Measurement Control message and can cause UE to loose Sync with SS.

As per Section 14.2.1.4 of 25.331 the equation need to be satisfied to trigger event 2d is as below:

$$Q_{Used} \leq T_{Used2d} - H_{2d} / 2$$

In case of 8.4.1.26, T_{Used2d} is given as -70 db and H_{2d} is given as 1 db, hence any value less than -71db should be able to trigger Event 2d. Down link power settings for Cell A at 'T1' should be changed to -75 db to make the test case more reliable.

Summary of change: Following change is made to 34.123-1 section 8.4.1.26.4:

- Downlink power to be applied for cell A in Column marked 'T1' are changed to -75db.

Consequences if not approved: Test case may fail a conformant UE.

Clauses affected: 8.4.1.26.4

Other specs affected:	<input type="checkbox"/>	Y	N	Other core specifications Test specifications O&M Specifications	<input type="checkbox"/>	
	<input checked="" type="checkbox"/>		X			
	<input checked="" type="checkbox"/>	X				
Other comments:	<input type="checkbox"/>	Affects R99, Rel4 and Rel5 UEs				<input type="checkbox"/>

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <http://www.3gpp.org/specs/CR.htm>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <ftp://ftp.3gpp.org/specs/> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

<< START OF MODIFIED SECTION >>

8.4.1.26 Measurement Control and Report: Measurement for events 2D and 2F

8.4.1.26.1 Definition

8.4.1.26.2 Conformance requirement

When event 2d is configured in the UE within a measurement, the UE shall:

- 1> if equation 1 below has been fulfilled for the used frequency during the time "Time to trigger":
 - 2> if the variable TRIGGERED_2D_EVENT is set to FALSE:
 - 3> set the variable TRIGGERED_2D_EVENT to TRUE;
 - 3> send a measurement report with IEs set as below:
 - 4> set in "inter-frequency event results": "inter-frequency event identity" to "2d" and no IE "Inter-frequency cells";
 - 4> set the IE "measured results" and the IE "additional measured results" according to TS 25.331 subclause 8.4.2.
 - 1> if the variable TRIGGERED_2D_EVENT is set to TRUE and if equation 2 is fulfilled for the used frequency:
 - 2> set the variable TRIGGERED_2D_EVENT to FALSE.

Triggering condition:

Equation 1:

$$Q_{Used} \leq T_{Used\ 2d} - H_{2d} / 2$$

The variables in the formula are defined as follows:

Q_{Used} is the quality estimate of the used frequency.

$T_{Used\ 2d}$ is the absolute threshold that applies for the used frequency and event 2d.

H_{2d} is the hysteresis parameter for the event 2d.

Leaving triggered state condition:

Equation 2:

$$Q_{Used} > T_{Used\ 2d} + H_{2d} / 2$$

The variables in the formula are defined as follows:

Q_{Used} is the quality estimate of the used frequency.

$T_{Used\ 2d}$ is the absolute threshold that applies for the used frequency and event 2d.

H_{2d} is the hysteresis parameter for the event 2d.

...

When event 2f is configured in the UE within a measurement, the UE shall:

- 1> if equation 1 below has been fulfilled for the used frequency during the time "Time to trigger":
 - 2> if the variable TRIGGERED_2F_EVENT is set to FALSE:
 - 3> set the variable TRIGGERED_2F_EVENT to TRUE;

3> send a measurement report with IEs set as below:

4> set in "inter-frequency event results": "inter-frequency event identity" to "2f", and no IE "Inter-frequency cells";

4> set the IE "measured results" and the IE "additional measured results" according to TS 25.331 subclause 8.4.2.

1> if the variable TRIGGERED_2F_EVENT is set to TRUE and if equation 2 is fulfilled for the used frequency:

2> set the variable TRIGGERED_2F_EVENT to FALSE.

Triggering condition:

Equation 1:

$$Q_{Used} \geq T_{Used\ 2f} + H_{2f} / 2$$

The variables in the formula are defined as follows:

Q_{Used} is the quality estimate of the used frequency.

$T_{Used\ 2f}$ is the absolute threshold that applies for the used frequency and event 2f.

H_{2f} is the hysteresis parameter for the event 2f.

Leaving triggered state condition:

Equation 2:

$$Q_{Used} < T_{Used\ 2f} - H_{2f} / 2$$

The variables in the formula are defined as follows:

Q_{Used} is the quality estimate of the used frequency.

$T_{Used\ 2f}$ is the absolute threshold that applies for the used frequency and event 2f.

H_{2f} is the hysteresis parameter for the event 2f.

Reference

3GPP TS 25.331 clause 14.2.1.4, 14.2.1.6

8.4.1.26.3 Test Purpose

1. To confirm that the UE sends MEASUREMENT REPORT message when event 2F is configured and estimated quality of the currently used frequency is above the value of the IE "Threshold used frequency".
2. To confirm that the UE sends MEASUREMENT REPORT message when event 2D is configured and estimated quality of the currently used frequency is below the value of the IE "Threshold used frequency".

8.4.1.26.4 Method of test

Initial Condition

System Simulator: 1 cells – The initial configurations of the cell in the SS shall follow the values indicated in the column marked "T0" in table 8.4.1.26-1. The table is found in "Test Procedure" clause.

UE: CS-DCCH+DTCH_DCH (State 6-9) or PS-DCCH+DTCH_DCH (State 6-10) in cell 1 as specified in clause 7.4 of TS 34.108, depending on the CN domain supported by the UE.

Test Procedure

Table 8.4.1.26-1 illustrates the downlink power to be applied for the cell at various time instants of the test execution. Column marked "T0" denotes the initial conditions, while columns marked "T1" is to be applied subsequently. The exact instant on which these values shall be applied is described in the text in this clause.

Table 8.4.1.26-1

Parameter	Unit	Cell 1	
		T0	T1
UTRA RF Channel Number		Ch. 1	
CPICH Ec (FDD)	dBm /3.8 4 MHz	-55	-75
P-CCPCH RSCP(TDD)	dBm	-60	- 80 75

The UE is initially in CELL_DCH state of cell 1. SS commands the UE to perform Inter-frequency measurements and report event 2D and/or event 2F by sending MEASUREMENT CONTROL message. Since quality estimate of used frequency is above threshold, the UE sends MEASUREMENT REPORT message indicating event 2F. SS then configures itself according to the values in columns "T1" shown above. Quality estimate for used frequency is now below threshold, the UE sends MEASUREMENT REPORT message to report it. SS calls for generic procedure C.3 to check that UE is in CELL_DCH state.

Expected Sequence

Step	Direction		Message	Comment
	UE	SS		
1			Void	
2			Void	
3			Void	
4		←	MEASUREMENT CONTROL	SS commands the UE to perform Inter-frequency measurements and to report event 2D and 2F.
5		→	MEASUREMENT REPORT	The UE shall report event 2F
6				SS re-adjusts the downlink transmission power settings according to columns "T1" in table 8.4.1.26-1.
7		→	MEASUREMENT REPORT	The UE shall report event 2D.
8		↔	CALL C.3	If the test result of C.3 indicates that UE is in CELL_DCH state, the test passes, otherwise it fails.

Specific Message Contents

All messages indicated below shall use the same content as described in default message content, with the following exceptions:

MEASUREMENT CONTROL (Step 4) (FDD)

Information Element	Value/remark
Measurement identity	10
Measurement command	Setup
- CHOICE measurement type	Inter-frequency measurement
- Inter-frequency cell info list	
- Inter-frequency cell removal	Not present
- New inter-frequency info list	
- Inter-frequency cell id	Any valid identity other than that of Cell 1
- Frequency Information	Any valid frequency other than that of Cell 1
- Cell info	
- Cell individual offset	Not present
- Reference time difference to cell	Not present
- CHOICE mode	FDD
- Read SFN Indicator	FALSE
- Primary CPICH Info	
- Primary scrambling code	Any value of Primary scrambling code
- Primary CPICH TX power	Not present
- TX Diversity Indicator	FALSE
- Cell for measurement	Not present
- Inter-frequency measurement quantity	
- Filter Coefficient	4
- Frequency quality estimate quantity	CPICH RSCP
- Inter-frequency reporting quantity	
- UTRAN Carrier RSSI	FALSE
- Frequency quality estimate	FALSE
- Non frequency related quantities	
- Cell synchronisation information reporting indicator	FALSE
- Cell identity reporting indicator	FALSE
- CPICH Ec/No reporting indicator	FALSE
- CPICH RSCP reporting indicator	FALSE
- Pathloss reporting indicator	FALSE
- Measurement validity	CELL_DCH state
- UE autonomous update mode	Not present
- CHOICE report criteria	Inter-frequency measurement reporting criteria
- Parameters required for each events	
- Inter-frequency event identity	2D
- Used frequency threshold	-70 dBm
- Used frequency W	0
- Hysteresis	1 dB
- Time to trigger	5000 mSec
- Reporting cell status	Not present
- Inter-frequency event identity	2F
- Used frequency threshold	-70 dBm
- Used frequency W	0
- Hysteresis	1 dB
- Time to trigger	5000 mSec
- Reporting cell status	Not present
Measurement reporting mode	
- Measurement reporting transfer mode	Unacknowledged Mode RLC
- Periodic reporting / Event trigger reporting mode	Event trigger
Additional measurement list	Not present
DPCH compressed mode status info	Not present

MEASUREMENT CONTROL (Step 4) (1.28 Mcps TDD)

Information Element	Value/remark
Measurement identity	10
Measurement command	Setup
Measurement reporting mode	
- Measurement reporting transfer mode	Unacknowledged Mode RLC
- Periodic reporting / Event trigger reporting mode	Event trigger
Additional measurement list	Not present
- CHOICE measurement type	Inter-frequency measurement
- Inter-frequency measurement objects list	
- Inter-frequency cell removal	Not present
- New inter-frequency info list	
- Inter-frequency cell id	Any valid identity other than that of Cell 1
- Frequency Information	Any valid frequency other than that of Cell 1
- Cell info	
- Cell individual offset	0
- Reference time difference to cell	Not present
- Read SFN Indicator	FALSE
- CHOICE mode	TDD
- Primary CCPCH Info	
- CHOICE Mode	TDD
- CHOICE TDD option	1.28 Mcps TDD
- TSTD indicator	FALSE
- Cell parameters ID	Any value of Cell parameters ID
- SCTD indicator	FALSE
- Primary CCPCH TX power	Not present
- Timeslot list	Not present
- Cell for measurement	Not present
- Inter-frequency measurement quantity	
- CHOICE reporting criteria	Inter-frequency reporting criteria
- Filter Coefficient	4
- CHOICE mode	TDD
- Measurement quantity for frequency quality estimate	P-CCPCH RSCP
- Inter-frequency reporting quantity	
- UTRAN Carrier RSSI	FALSE
- Frequency quality estimate	FALSE
- Non frequency related quantities	
- Cell synchronisation information reporting indicator	FALSE
- Cell identity reporting indicator	FALSE
- CHOICE mode	TDD
- Timeslot ISCP reporting indicator	FALSE
- Proposed TGSN reporting indicator	FALSE
- Primary CCPCH RSCP reporting indicator	FALSE
- Pathloss reporting indicator	FALSE
- Measurement validity	CELL_DCH state
- CHOICE report criteria	Inter-frequency measurement reporting criteria
- Parameters required for each events	
- Inter-frequency event identity	2D
- Threshold used frequency	-70 dBm
- W used frequency	0
- Hysteresis	1 dB
- Time to trigger	5000 mSec
- Reporting cell status	Report cells within active set
- Maximum number of reported cells	2
- Inter-frequency event identity	2F
- Threshold used frequency	-70 dBm
- W used frequency	0
- Hysteresis	1 dB
- Time to trigger	5000 mSec
- Reporting cell status	Report cells within active set
- Maximum number of reported cells	2
DPCH compressed mode status info	Not present

MEASUREMENT CONTROL (Step 4) (3.84 Mcps TDD)

Information Element	Value/remark	Release
Measurement identity	10	
Measurement command	Setup	
Measurement reporting mode	Unacknowledged Mode RLC	
- Measurement reporting transfer mode	Event trigger	
- Periodic reporting / Event trigger reporting mode	Event trigger	
Additional measurement list	Not present	
- CHOICE measurement type	Inter-frequency measurement	
- Inter-frequency measurement objects list	Not present	
- Inter-frequency cell removal	Not present	
- New inter-frequency info list	Not present	
- Inter-frequency cell id	Any valid identity other than that of Cell 1	
- Frequency Information	Any valid frequency other than that of Cell 1	
- Cell info	Not present	
- Cell individual offset	0	
- Reference time difference to cell	Not present	
- Read SFN Indicator	FALSE	
- CHOICE mode	TDD	
- Primary CCPCH Info	TDD	
- CHOICE Mode	TDD	
- CHOICE TDD option	3.84 Mcps TDD	REL-4
- Cell parameters ID	Any value of Cell parameters ID	
- SCTD indicator	FALSE	
- Primary CCPCH TX power	Not present	
- Timeslot list	Not present	
- Cell for measurement	Not present	
- Inter-frequency measurement quantity	Inter-frequency measurement reporting criteria	
- CHOICE reporting criteria	Inter-frequency reporting criteria	
- Filter Coefficient	4	
- CHOICE mode	TDD	
- Measurement quantity for frequency quality estimate	P-CCPCH RSCP	
- Inter-frequency reporting quantity	P-CCPCH RSCP	
- UTRAN Carrier RSSI	FALSE	
- Frequency quality estimate	FALSE	
- Non frequency related quantities	FALSE	
- Cell synchronisation information reporting indicator	FALSE	
- Cell identity reporting indicator	FALSE	
- CHOICE mode	TDD	
- Timeslot ISCP reporting indicator	FALSE	
- Proposed TGSN reporting indicator	FALSE	
- Primary CCPCH RSCP reporting indicator	FALSE	
- Pathloss reporting indicator	FALSE	
- Measurement validity	CELL_DCH state	
- CHOICE report criteria	Inter-frequency measurement reporting criteria	
- Parameters required for each events	Inter-frequency measurement reporting criteria	
- Inter-frequency event identity	2D	
- Threshold used frequency	-70 dBm	
- W used frequency	0	
- Hysteresis	1 dB	
- Time to trigger	5000 mSec	
- Reporting cell status	Report cells within active set	
- Maximum number of reported cells	2	
- Inter-frequency event identity	2F	
- Threshold used frequency	-70 dBm	
- W used frequency	0	
- Hysteresis	1 dB	

- Time to trigger	5000 mSec	
- Reporting cell status	Report cells within active set	
- Maximum number of reported cells	2	
DPCH compressed mode status info	Not present	

MEASUREMENT REPORT (Step 5) (FDD)

Information Element	Value/remark
Measurement identity	Check to see if set to 10
Measured results	Check to see if it is absent
Measured results on RACH	Check to see if it is absent
Additional measured results	Check to see if it is absent
Event results	Inter-frequency measurement event results,
- Inter-frequency event identity	2F

MEASUREMENT REPORT (Step 5)(TDD)

Information Element	Value/remark
Measurement identity	Check to see if set to 10
Measured results	Check to see if it is absent
Measured results on RACH	Check to see if it is absent
Additional measured results	Check to see if it is absent
Event results	
- CHOICE event results	Check to see if set to Inter-frequency measurement event results,
- Inter-frequency event identity	Check to see if set to 2F

MEASUREMENT REPORT (Step 7) (FDD)

Information Element	Value/remark
Measurement identity	Check to see if set to 10
Measured results	Check to see if it is absent
Measured results on RACH	Check to see if it is absent
Additional measured results	Check to see if it is absent
Event results	Inter-frequency measurement event results,
- Inter-frequency event identity	2D

MEASUREMENT REPORT (Step 7)(TDD)

Information Element	Value/remark
Measurement identity	Check to see if set to 10
Measured results	Check to see if it is absent
Measured results on RACH	Check to see if it is absent
Additional measured results	Check to see if it is absent
Event results	
- CHOICE event results	Check to see if set to Inter-frequency measurement event results,
- Inter-frequency event identity	Check to see if set to 2D

8.4.1.26.5 Test Requirement

1. In step 5 the UE shall send MEASUREMENT REPORT message indicating event 2F.
2. In step 7 the UE shall send MEASUREMENT REPORT message indicating event 2D.

<< END OF MODIFIED SECTION >>

CR-Form-v7

CHANGE REQUEST

34.123-1 CR 1242 rev - Current version: **5.11.1**

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the symbols.

Proposed change affects: | UICC apps ME Radio Access Network Core Network

Title:	Correction RRC test case 8.4.1.7A (TDD)		
Source:	3GPP TSG RAN WG5 (Testing)		
Work item code:	TEI	Date:	8/04/2005
Category:	F	Release:	Rel-5
	Use <u>one</u> of the following categories: F (correction) A (corresponds to a correction in an earlier release) B (addition of feature), C (functional modification of feature) D (editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900 .		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6)

Reason for change:	1. The specific message contents of Cell Update message at Step 22. <ul style="list-style-type: none"> • Do not indicate the presence of following mandatory IE's: Start List, RB Timer Indicator and AM_RLC error. • Indicate presence of following IE's which are not part of Cell Update message: Protocol error indicator and Protocol error information. 2. Minor editorial changes
Summary of change:	1. Following changes are made to 34.123-1 section 8.4.1.7A.4 for Specific Message content of Cell Update message at Step 22: <ul style="list-style-type: none"> • Mandatory IE's Start List, RB Timer Indicator and AM_RLC error indication are added. • Removed extra IE's Protocol error indicator and Protocol error information. 2. Minor editorial changes
Consequences if not approved:	Test will fail a compliant UE.

Clauses affected:	8.4.1.7A.4										
Other specs affected:	<table border="1" style="display: inline-table; border-collapse: collapse;"> <tr> <td style="text-align: center;">Y</td> <td style="text-align: center;">N</td> </tr> <tr> <td style="text-align: center;">X</td> <td style="text-align: center;">X</td> </tr> <tr> <td style="text-align: center;">X</td> <td style="text-align: center;">X</td> </tr> <tr> <td style="text-align: center;">X</td> <td style="text-align: center;">X</td> </tr> </table> Other core specifications Test specifications O&M Specifications	Y	N	X	X	X	X	X	X		
Y	N										
X	X										
X	X										
X	X										

Other comments: ☞ Affects R99, Rel4 and Rel5 Ues

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <http://www.3gpp.org/specs/CR.htm>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked ☞ contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <ftp://ftp.3gpp.org/specs/> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

<< START OF MODIFIED SECTION >>

8.4.1.7A Measurement Control and Report: Intra-frequency measurement for transition from CELL_FACH to CELL_DCH state (TDD)

8.4.1.7A.1 Definition

8.4.1.7A.2 Conformance requirement

Upon transition from CELL_FACH to CELL_DCH state:

- 1> if intra-frequency measurements applicable to CELL_DCH state are stored in the variable MEASUREMENT_IDENTITY:
- 2> if the cell in which the UE transitioned from CELL_FACH state is included in the active set for the CELL_DCH state, the UE shall:
 - 3> resume the measurement reporting.
- 2> otherwise:
 - 3> the UE should not resume the measurement reporting. If the UE does not resume the measurement reporting, the measurement shall be restarted when a MEASUREMENT CONTROL message is received with the corresponding measurement identity.

...

Upon cell reselection while in CELL_FACH/CELL_PCH/URA_PCH state and the cell reselection has occurred after the measurement control information was stored, the UE shall:

- 1> delete all measurements of type intra-frequency, inter-frequency, and inter-RAT associated with the variable MEASUREMENT_IDENTITY;

...

1> delete the traffic volume measurements that have not been set up or modified through a MEASUREMENT CONTROL message.

...

Upon reception of a MEASUREMENT CONTROL message the UE shall perform actions specified in subclause 8.6 unless otherwise specified below.

The UE shall:

- 1> read the IE "Measurement command";
- 1> if the IE "Measurement command" has the value "setup":
 - 2> store this measurement in the variable MEASUREMENT_IDENTITY according to the IE "measurement identity", first releasing any previously stored measurement with that identity if that exists;
 - 2> if the measurement type is quality, UE internal, intra-frequency, inter-frequency or inter-RAT:
 - 3> if the UE is in CELL_FACH state:
 - 4> the UE behaviour is not specified.
 - 2> for measurement types "inter-RAT measurement" or "inter-frequency measurement" that require measurements on a frequency other than the actually used frequency:
 - ...
 - 2> for measurement type "inter-frequency measurement" that requires measurements only on the same frequency as the actually used frequency:

- ...
- 2> for measurement type "UE positioning measurement":
- ...
- 2> for any other measurement type:
 - 3> if the measurement is valid in the current RRC state of the UE:
 - 4> begin measurements according to the stored control information for this measurement identity.
- 1> if the IE "Measurement command" has the value "modify":
 - 2> for all IEs present in the MEASUREMENT CONTROL message:
 - 3> if a measurement was stored in the variable MEASUREMENT_IDENTITY associated to the identity by the IE "measurement identity":
 - 4> if the measurement type is quality, UE internal, intra-frequency, inter-frequency or inter-RAT:
 - 5> if the UE is in CELL_FACH state:
 - 6> the UE behaviour is not specified.
 - 4> if measurement type is set to "intra-frequency measurement", for any of the optional IEs "Intra-frequency measurement objects list", "Intra-frequency measurement quantity", "Intra-frequency reporting quantity", "Measurement Validity", "report criteria" and "parameters required for each event" (given "report criteria" is set to "intra-frequency measurement reporting criteria") that are present in the MEASUREMENT CONTROL message:
 - 5> replace the corresponding information (the IEs listed above and all their children) stored in variable MEASUREMENT_IDENTITY associated to the identity indicated by the IE "measurement identity" with the one received in the MEASUREMENT CONTROL message;
 - 5> leave all other stored information elements unchanged in the variable MEASUREMENT_IDENTITY.
- 1> if the IE "measurement command" has the value "release":
 - 2> terminate the measurement associated with the identity given in the IE "measurement identity";
 - 2> clear all stored measurement control information related associated to this measurement identity in variable MEASUREMENT_IDENTITY.

"If the IE "Reporting Cell Status" is received, the UE shall set the IE "Measured Results" in MEASUREMENT REPORT as follows. The UE shall:

- 1> for intra-frequency measurement and inter-frequency measurement:
 - 2> include the IE "Cell Measured Results" for cells (excluding cells of another RAT) that satisfy the condition (such as "Report cells within active set") specified in the IE "Reporting Cell Status", in descending order by the measurement quantity"

If the IE "Cells for measurement" has been included in a MEASUREMENT CONTROL message, only monitored set cells explicitly indicated for a given intra-frequency (resp. inter-frequency, interRAT) measurement by the IE "Cells for measurement" shall be considered for measurement. If the IE "Cells for measurement" has not been included in a MEASUREMENT CONTROL message, all of the intra-frequency (resp. inter-frequency, inter RAT) cells stored in the variable CELL_INFO_LIST shall be considered for measurement. The IE "Cells for measurement" is not applicable to active set cells or virtual active set cells e.g. when the triggering condition refers to active set cells, the UE shall

consider all active set cells in the CELL_INFO_LIST for measurement irrespective if these cells are explicitly indicated by the IE "Cells for measurement".

Reference

3GPP TS 25.331, clause 8.4.1.3, 8.4.1.6a, 8.4.1.7.1, 8.4.0 and 8.6.7.9

8.4.1.7A.3 Test Purpose

- To confirm that UE retrieves stored measurement control information for intra-frequency measurement type with "measurement validity" assigned to "CELL_DCH", after it enters CELL_DCH state from CELL_FACH state.
- To confirm that the UE continues to monitor the neighbouring cells listed "intra-frequency cell info" IE in the System Information Block type 11 or 12 messages, if no intra-frequency measurements applicable to CELL_DCH are stored.
- To confirm that the UE transmits MEASUREMENT REPORT messages if reporting criteria stated in IE "intra-frequency measurement reporting criteria" in System Information Block type 11 or 12 messages are fulfilled.
- To confirm that a MEASUREMENT CONTROL message received in CELL_DCH state overrides the measurement and associated reporting contexts maintained in the UE by virtue of System Information Block type 11 or 12 messages only if the measurement identities defined within the MEASUREMENT CONTROL message and System Information Block type 11 or 12 are identical.
- To confirm that the UE delete all measurements of type intra-frequency upon cell reselection while in CELL_FACH.

8.4.1.7A.4 Method of test

Initial Condition

System Simulator: 3 cells – Cell 1, cell 2 and cell 3 are active.

[SYSTEM INFORMATION BLOCK TYPE 1 \(see specific message contents\).](#)

UE: PS-DCCH+DTCH_FACH (state 6-11).

Test Procedure

Table 8.4.1.7A-1 illustrates the downlink power to be applied for the 3 cells at various time instants of the test execution. Column marked "T0" denotes the initial conditions, while columns marked "T1" are to be applied subsequently. The exact instants on which these values shall be applied are described in the text in this clause.

Table 8.4.1.7A-1

Para-meter	Unit	Cell 1								Cell 2								Cell 3							
		T 0	T 1	T 2	T 3	T 4	T 5	T 6	T 7	T 0	T 1	T 2	T 3	T 4	T 5	T 6	T 7	T 0	T 1	T 2	T 3	T 4	T 5	T 6	T 7
UTRA RF Channel Number		Ch. 1								Ch. 1								Ch. 1							
PCCPCH Ec	dBm /1.28 MHz	-6 0	-7 0	-6 0	-7 0	-6 0	-7 0	-7 5	-7 0	-6 0	-7 0	-7 0	-6 0	-7 0	-6 0	-7 0	-7 5	-7 5	-7 5	-7 5	-7 5	-7 5	-7 5	-7 5	-6 0

The UE is brought to CELL_FACH state in cell 1. (step 1) SS sends SYSTEM INFORMATION CHANGE INDICATION message to UE to inform UE of the modification in the system information.

SS sends a RADIO BEARER RECONFIGURATION message to UE (step2), and configures dedicated physical channels on both uplink and downlink directions. The UE shall move to CELL_DCH state and then return RADIO BEARER RECONFIGURATION COMPLETE message (step3). SS reconfigures the downlink transmission power settings for cells 1 to 3 according to column "T1" in table 8.4.1.7A .The UE shall send a MEASUREMENT REPORT message containing IE "Measured results" to report cell 2's PCCPCH RSCP value and IE "event results" to report triggering of event type "1g" (step 4). After receiving the MEASUREMENT REPORT message, SS transmits a MEASUREMENT CONTROL message with cell 3 included in the IE "new intra-frequency cell info" (step 5). After

receiving such a message, the UE shall transmit another set of MEASUREMENT REPORT message for measurement identity = 11. SS verifies that measurement readings for cell 1,2,3 's PCCPCH RSCP are reported in IE "cell measured results" in this message (step 6). SS modifies the downlink transmission power of the respect cells according to the settings in columns "T2" in table 8.4.1.7A-1. The UE shall send a MEASUREMENT REPORT message containing IE "Measured results" to report cell 1's PCCPCH RSCP value and IE "event results" to report triggering of event type "1g" (step 6b). Next, SS sends a PHYSICAL CHANNEL RECONFIGURATION message (step 7). SS configures common physical channels for both the uplink and the downlink directions. The UE shall transit to CELL_FACH state and then reply with a PHYSICAL CHANNEL RECONFIGURATION COMPLETE message (step 8). SS waits and checks the uplink RACH to confirm that no MEASUREMENT REPORT messages are received (step 9).

SS transmits then a RADIO BEARER RECONFIGURATION message to the UE, to move it to CELL_DCH state (step 9a). The UE shall move to that state, and transmit a RADIO BEARER RECONFIGURATION COMPLETE message to SS (step 9b). SS modifies the downlink transmission power of the respect cells according to the settings in columns "T3" in table 8.4.1.7A-1. Shortly after, a MEASUREMENT REPORT message shall be received which has been triggered by cell 2, i.e. the UE shall have deleted the measurement configured through the MEASUREMENT CONTROL message of step 5, and instead apply the measurement configured in SIB12: a MEASUREMENT REPORT message with measurement identity 10 shall be received while no such message with measurement identity 11 shall be sent by the UE (step 9c). SS modifies the downlink transmission power of the respect cells according to the settings in columns "T4" in table 8.4.1.7A-1. The UE shall send a MEASUREMENT REPORT message containing IE "Measured results" to report cell 1's PCCPCH RSCP value and IE "event results" to report triggering of event type "1g".

SS transmits MEASUREMENT CONTROL message on the downlink DCCH, to configure periodic intra-frequency measurements with validity CELL_DCH (step 10). The UE shall send a MEASUREMENT REPORT message (with IE "Measurement identity" = 12) to the SS (step 14).

SS transmits a PHYSICAL CHANNEL RECONFIGURATION message to the UE to move it to CELL_FACH state (step 14a). The UE shall move to that state and transmit a PHYSICAL CHANNEL RECONFIGURATION COMPLETE message to SS (step 14b). SS shall wait and check that no MEASUREMENT REPORT messages are detected on the uplink DCCH (step 14c).

SS transmits a RADIO BEARER RECONFIGURATION message to the UE, to move it to CELL_DCH state (step 14d). The UE shall move to that state, and transmit a RADIO BEARER RECONFIGURATION COMPLETE message to SS (step 14e). Shortly after, a MEASUREMENT REPORT message shall be received, i.e the UE shall have retrieved the measurement configured through the MEASUREMENT CONTROL message of step 10, instead of the ones that are broadcast in SIB12 (step 14f).

Following the reception of the MEASUREMENT REPORT message, SS commands the UE using MEASUREMENT CONTROL message to release measurement control information stored in "measurement identity" = 12 (step 15). Thereafter, SS verifies that no MEASUREMENT REPORT messages are detected on the uplink DCCH (step 16). After this requirement is satisfied, SS sends MEASUREMENT CONTROL on the downlink DCCH once more (step 17). This message is identical to the one sent in step 10 (see specific message content). A periodical MEASUREMENT REPORT message shall be received from the UE (step 17a).

SS transmits a PHYSICAL CHANNEL RECONFIGURATION message on the downlink DCCH and configures common physical channel (step 18). The UE shall transit to CELL_FACH state and then respond with a PHYSICAL CHANNEL RECONFIGURATION COMPLETE message (step 19). SS monitors the uplink DCCH once more to verify that no MEASUREMENT REPORT messages are detected (step 20). System information block type 11 and System Information Block type 12 for cell 2 shall be different from the default settings according to what is defined in the specific message content part of this section (step 21). SS modifies the downlink transmission power of the respect cells according to the settings in columns "T5" in table 8.4.1.7A-1. The UE shall initiate a cell re-selection procedure. This is verified in the SS when a CELL UPDATE message is received on the uplink CCCH with the "cell update cause" IE set to "cell reselection" (step 22). SS transmits a CELL UPDATE CONFIRM message, which includes "New C-RNTI", on the DCCH (step 23). Then the UE shall reply with UTRAN MOBILITY INFORMATION CONFIRM message (step 23a). Next, SS sends a RADIO BEARER RECONFIGURATION message on the downlink DCCH, assigning dedicated physical channels in both uplink and downlink directions (step 24). The UE shall respond with a RADIO BEARER RECONFIGURATION COMPLETE message and then return to CELL_DCH state (step 25). SS modifies the downlink transmission power of all cells according to the settings in columns "T6" in table 8.4.1.7A-1. UE shall then send MEASUREMENT REPORT messages reporting cell 3's PCCPCH RSCP according to the content in System Information Block type 12 messages broadcasted in cell 2 (step 21). SS transmits a MEASUREMENT CONTROL message (step 27) whereby the measurement identity is set to the same value as that in the SIB type 12 messages (step 21). UE shall send MEASUREMENT REPORT message (step 28) reporting cell 1,2,3's PCCPCH RSCP according to the MEASUREMENT CONTROL message (step 27).

NOTE: If the UE fails the test because of a failure to reselect to a right cell, then the operator may re-run the test.

Expected Sequence

Step	Direction		Message	Comment
	UE	SS		
1		←	System Information Block type 11 and 12	UE is initially in PS-DCCH+DTCH_FACH (state 6-11) in cell 1. System Information Block type 11 and 12 messages are changed with respect to the default contents according to the descriptions in "Specific Message Contents" clause.
1a		←	SYSTEM INFORMATION CHANGE INDICATION	
2		←	RADIO BEARER RECONFIGURATION	SS configures dedicated physical channels.
3		→	RADIO BEARER RECONFIGURATION COMPLETE	UE shall move to CELL_DCH state.
3a				SS reconfigures the downlink transmission power settings for cells 1 to 3 according to column "T1" in table 8.4.1.7A
4		→	MEASUREMENT REPORT	Reports cell 2's PCCPCH RSCP measurement value, with "measurement identity" IE set to "10".
5		←	MEASUREMENT CONTROL	A periodic measurement is setup with measurement identity of 11. Cell 3 is added to the list of monitored set of the UE.
6		→	MEASUREMENT REPORT	SS shall receive a MEASUREMENT REPORT message after the period set in step 5 in which the report for cell 1, cell 2 and cell 3 are included.
6a				SS reconfigures the downlink transmission power settings for cells 1 to 3 according to column "T2" in table 8.4.1.7A
6b		→	MEASUREMENT REPORT	Cell 1 shall trigger the event 1g and a MEASUREMENT REPORT message shall be sent to SS with the measurement identity 10.
7		←	PHYSICAL CHANNEL RECONFIGURATION	SS configures PRACH and S-CCPCH physical channels.
8		→	PHYSICAL CHANNEL RECONFIGURATION COMPLETE	UE shall move to CELL_FACH state.
9				SS waits and checks that no MEASUREMENT REPORT messages are sent by UE.
9a		←	RADIO BEARER RECONFIGURATION	SS configures dedicated physical channels.
9b		→	RADIO BEARER RECONFIGURATION COMPLETE	UE shall move to CELL_DCH state.
9b1				SS reconfigures the downlink transmission power settings for cells 1 to 3 according to column "T3" in table 8.4.1.7A
9c		→	MEASUREMENT REPORT	Cell 2 shall trigger the event 1g and a MEASUREMENT REPORT message shall be sent to SS with the measurement identity 10.

Step	Direction		Message	Comment
	UE	SS		
9d				SS reconfigures the downlink transmission power settings for cells 1 to 3 according to column "T4" in table 8.4.1.7A
9e		→	MEASUREMENT REPORT	Cell 1 shall trigger the event 1g and a MEASUREMENT REPORT message shall be sent to SS with the measurement identity 10.
10		←	MEASUREMENT CONTROL	SS instructs the UE to setup intra-frequency measurement with measurement identity of 12. Measurement validity" IE is set to CELL_DCH state.
11			Void	
12		←	Void	
13		→	Void	
14		→	MEASUREMENT REPORT	UE reports cell 1 and cell 2's measured results for PCCPCH RSCP, with "measurement identity" IE set to "12".
14a		←	PHYSICAL CHANNEL RECONFIGURATION	SS configures PRACH and S-CCPCH physical channels.
14b		→	PHYSICAL CHANNEL RECONFIGURATION COMPLETE	UE shall move to CELL_FACH state.
14c				SS waits and check that no MEASUREMENT REPORT messages are sent by the UE.
14d		←	RADIO BEARER RECONFIGURATION	SS configures dedicated physical channels.
14e		→	RADIO BEARER RECONFIGURATION COMPLETE	UE shall move to CELL_DCH state.
14f		→	MEASUREMENT REPORT	UE shall have retrieved and resumed the measurement set up through the MEASUREMENT CONTROL of step 10. The "measurement identity" IE shall be set to "12".
15		←	MEASUREMENT CONTROL	Terminate all the intra-frequency measurement and reporting activities related to "measurement identity" = 12.
16				SS waits and verifies that UE stop transmitting MEASUREMENT REPORT messages.
17		←	MEASUREMENT CONTROL	This message is the same as in step 10.
17a		→	MEASUREMENT REPORT	UE shall transmit a MEASUREMENT REPORT message with "measurement identity" IE set to "12".
18		←	PHYSICAL CHANNEL RECONFIGURATION	Allocates common physical channels.
19		→	PHYSICAL CHANNEL RECONFIGURATION COMPLETE	UE shall move to CELL_FACH state.
20				SS checks that no MEASUREMENT REPORT messages are received.

Step	Direction		Message	Comment
	UE	SS		
21		←	System Information Block type 11 System Information Block type 12	SS sends SIB11 and SIB12 with specific values to Cell2. SS reconfigures the downlink transmission power settings for cells 1 to 3 according to column "T5" in table 8.4.1.7A.
22		→	CELL UPDATE	UE shall re-selects to cell 2 and then perform a cell update procedure.
23		←	CELL UPDATE CONFIRM	UE shall stay in CELL_FACH state.
23a		→	UTRAN MOBILITY INFORMATION CONFIRM	
24		←	RADIO BEARER RECONFIGURATION	Dedicated physical channels are assigned to the UE in this message.
25		→	RADIO BEARER RECONFIGURATION COMPLETE	UE shall return to CELL_DCH state. UE shall not send Measurement Report message with "measurement identity" = '12'.
25a				SS reconfigures the downlink transmission power settings of all cells according to column "T6" in table 8.4.1.7A-1.
26		→	MEASUREMENT REPORT	UE begins to report cell 3's measured results for PCCPCH RSCP, with "measurement identity" IE set to "1", event is 1g.
27		←	MEASUREMENT CONTROL	SS instructs the UE to setup period intra-frequency measurement. "measurement identity" IE set to "1"
28		→	MEASUREMENT REPORT	UE shall transmit a period MEASUREMENT REPORT message, with "measurement identity" IE set to "1".

Specific Message Content

System Information Block type 1 (TDD)

Use the default system information block with the same type specified in clause 6.19 of TS 34.108, with the following exceptions:

Information Element	Value/remark
- UE Timers and constants in connected mode	
- T312	2

Master Information Block (Step 1)

Information Element	Value/Remarks
MIB Value Tag	3

System Information Block type 11 for cell 1 (Step 1)

All messages content below shall use the same content as described in default message content [specified in clause 6.1 of TS 34.108](#), with the following exception:

Information Element	Value/remark
SIB12 indicator FACH measurement occasion info Measurement control system information <ul style="list-style-type: none"> - Use of HCS - Cell selection and reselection quality measure - Intra-frequency measurement system information - Intra-frequency measurement identity - Intra-frequency cell info list - CHOICE intra-frequency cell removal - New intra-frequency cells - Intra-frequency cell id - Cell info <ul style="list-style-type: none"> - Cell individual offset - Reference time difference to cell - Read SFN indicator - CHOICE mode <ul style="list-style-type: none"> - Primary CCPCH info - TSTD indicator - Cell parameters Id - SCTD indicator - Primary CCPCH Tx power - timeslot info list - Cell Selection and Re-selection info - Cells for measurement - Intra-frequency measurement quantity - Intra-frequency reporting quantity for RACH reporting <ul style="list-style-type: none"> - Maximum number of reported cells on RACH - Reporting information for state CELL_DCH 	TRUE Not Present Not used PCCPCH RSCP Not present Not present 1 Not present Not present FALSE TDD FALSE Refer to clause titled "Default settings for cell No.1 (TDD)" in clause 6.1.4 of TS34.108 FALSE Not present Not present Not present Not present Not present Not present Not present Not present Not present Not present Not present Not present Not present Not present Not present Not present Not present Not present

System Information Block type 12 for cell 1 (Step 1)

Information Element	Value/remark
FACH measurement occasion info	Not Present
Measurement control system information	Not used
- Use of HCS	PCCPCH RSCP
- Cell selection and reselection quality measure	10
- Intra-frequency measurement system information	Not present
- Intra-frequency measurement identity	2
- Intra-frequency cell info list	Not present
- CHOICE intra-frequency cell removal	Not present
- New intra-frequency cells	FALSE
- Intra-frequency cell id	TDD
- Cell info	FALSE
- Cell individual offset	Not present
- Reference time difference to cell	Not present
- Read SFN Indicator	FALSE
- CHOICE mode	TDD
- Primary CCPCH Info	FALSE
- TSTD indicator	FALSE
- Cell parameters Id	Refer to clause titled "Default settings for cell No.2 (TDD)" in clause 6.1.4 of TS34.108
- SCTD indicator	FALSE
- Primary CCPCH Tx power	Not present
- timeslot info list	Not present
- Cell selection and Re-selection info	Not Present
- Cells for measurement	Not Present
- Intra-frequency measurement quantity	Not present
- Filter Coefficient	TDD
- CHOICE mode	PCCPCH RSCP
- Measurement quantity	Not present
- Intra-frequency reporting quantity for RACH reporting	No report
- Maximum number of reported cells on RACH	FALSE
- Reporting information for state CELL_DCH	FALSE
- Intra-frequency reporting quantity	FALSE
- Reporting quantities for active set cells	FALSE
- Cell synchronisation information reporting indicator	FALSE
- Cell identity reporting indicator	TDD
- CHOICE mode	FALSE
- Timeslot ISCP reporting indicator	FALSE
- Proposed TGSN Reporting required	FALSE
- PCCPCH RSCP reporting indicator	TRUE
- Pathloss reporting indicator	FALSE
- Reporting quantities for monitored set cells	FALSE
- Cell synchronisation information reporting indicator	FALSE
- Cell identity reporting indicator	TDD
- CHOICE mode	FALSE
- Timeslot ISCP reporting indicator	FALSE
- Proposed TGSN Reporting required	FALSE
- PCCPCH RSCP reporting indicator	TRUE
- Pathloss reporting indicator	FALSE
- Reporting quantities for detected cells	Not present
- Measurement Reporting Mode	Acknowledged mode RLC
- Measurement Reporting Transfer Mode	Event trigger
- Periodic Reporting/Event Trigger Reporting Mode	Intra-frequency measurement reporting criteria
- CHOICE report criteria	1g
- Parameter required for each event	Not present
- Intra-frequency event identity	Not present
- Reporting range constant	Not present
- W	1 dB
- Hysteresis	0
- Time to trigger	Not Present
- Amount of reporting	Not Present
- Reporting Interval	Not Present
- Reporting cell status	

- CHOICE reported cells	Report cells within activated and monitored set cells on used frequency
- Maximum number of reported cells	3
- Inter-frequency measurement system information	Not Present
- Inter-RAT measurement system information	Not Present
- Traffic volume measurement system information	Not Present

SYSTEM INFORMATION CHANGE INDICATION (Step 1a)

Information Element	Value/Remarks
BCCH modification info	
- MIB Value Tag	3
- BCCH modification time	Not Present

RADIO BEARER RECONFIGURATION (Step 2, Step 9a, Step 14d and Step 24)

Use the same message type found in Annex A, with condition set to A4.

MEASUREMENT REPORT (Steps 4 and 9c)

Information Element	Value/remark
Measurement identity	Check to see if set to 10
Measured Results	
- CHOICE measurement	Check to see if set to "Intra-frequency measured results list"
- Intra-frequency measurement results	
- Cell measured results	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is absent
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 1
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	Check to see if this IE is absent
- Cell measured results	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is absent
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 2
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	Check to see if this IE is absent
Measured Results on RACH	Check to see if this IE is absent
Additional measured results	Check to see if this IE is absent
Event Results	
- CHOICE event result	Check to see if it's set to 'Intra-frequency measurement event results'
- Intra-frequency event identity	Check to see if this IE is set to '1g'
- Cell measurement event results	
- Cell parameters Id	Check to see if it's the same code for cell 2

MEASUREMENT CONTROL (Step 5).

Information Element	Value/remark
Measurement Identity	11
Measurement Command	Setup
Measurement Reporting Mode	Acknowledged Mode RLC
- Measurement Reporting Transfer Mode	Periodical Reporting
- Periodic Reporting / Event Trigger Reporting Mode	Not Present
Additional measurements list	Intra-frequency measurement
CHOICE measurement type	Remove no intra-frequency cells
- Intra-frequency cell info list	3
- CHOICE intra-frequency cell removal	0 dB
- New intra-frequency info list	Not Present
- Intra-frequency cell id	FALSE
- Cell info	TDD
- Cell individual offset	FALSE
- Reference time difference to cell	Refer to clause titled "Default settings for cell No.2 (TDD)" in clause 6.1.4 of TS34.108
- Read SFN Indicator	FALSE
- CHOICE mode	TDD
- Primary CCPCH Info	FALSE
- TSTD indicator	Refer to clause titled "Default settings for cell No.2 (TDD)" in clause 6.1.4 of TS34.108
- Cell parameters Id	FALSE
- SCTD indicator	FALSE
- Primary CCPCH Tx power	Not present
- timeslot info list	Not present
- Cells selection and Re-selection info	Not Present
- Cells for measurement	Not Present
- Intra-frequency measurement quantity	Not Present
- Filter Coefficient	PCCPCH RSCP
- Measurement quantity	FALSE
- Intra-frequency reporting quantity	FALSE
- Reporting quantities for active set cells	FALSE
- Cell synchronisation information reporting indicator	FALSE
- Cell identity reporting indicator	FALSE
- CHOICE mode	TDD
- Timeslot ISCP reporting indicator	FALSE
- Proposed TGSN Reporting required	FALSE
- PCCPCH RSCP reporting indicator	TRUE
- Pathloss reporting indicator	FALSE
- Reporting quantities for monitored set cells	FALSE
- Cell synchronisation information reporting indicator	FALSE
- Cell identity reporting indicator	FALSE
- CHOICE mode	TDD
- Timeslot ISCP reporting indicator	FALSE
- Proposed TGSN Reporting required	FALSE
- PCCPCH RSCP reporting indicator	TRUE
- Pathloss reporting indicator	FALSE
- Reporting quantities for detected cells	Not present
- Reporting cell status	Not present
- Measurement validity	Not present
- CHOICE report criteria	Periodical reporting criteria
- reporting amount	infinity
- reportingInterval	64s
DPCH compressed mode status info	Not Present

MEASUREMENT REPORT (Steps 6)

Information Element	Value/remark
Measurement identity	Check to see if set to 11
Measured Results	
- CHOICE measurement	Check to see if set to "Intra-frequency measured results list"
- Intra-frequency measurement results	
- Cell measured results	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is absent
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 2
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	Check to see if this IE is absent
- Cell measured results	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is absent
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 1
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	Check to see if this IE is absent
- Cell measured results	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is absent
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 3
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	Check to see if this IE is absent
Measured Results on RACH	Check to see if this IE is absent
Additional measured results	Check to see if this IE is absent
Event Results	Check to see if this IE is absent

MEASUREMENT REPORT (Steps 6b)

Information Element	Value/remark
Measurement identity	Check to see if set to 10
Measured Results	
- CHOICE measurement	Check to see if set to "Intra-frequency measured results list"
- Intra-frequency measurement results	
- Cell measured results	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is absent
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 1
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	Check to see if this IE is absent
- Cell measured results	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is absent
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 2
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	Check to see if this IE is absent
- Cell measured results	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is absent
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 3
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
Measured Results on RACH	Check to see if this IE is absent
Additional measured results	Check to see if this IE is absent
Event Results	
- CHOICE event result	Check to see if it's set to 'Intra-frequency measurement event results'
- Intra-frequency event identity	Check to see if this IE is set to '1g'
- Cell measurement event results	
- Cell parameters Id	Check to see if it's the same code for cell 1

PHYSICAL CHANNEL RECONFIGURATION (Steps 7, 14a and 18)

Use the same message sub-type found in clause 9 of TS 34.108, which is entitled "Packet to CELL_FACH from CELL_DCH in PS".

MEASUREMENT REPORT (Steps 9e)

Information Element	Value/remark
Measurement identity Measured Results <ul style="list-style-type: none"> - CHOICE measurement - Intra-frequency measurement results - Cell measured results <ul style="list-style-type: none"> - Cell Identity - Cell synchronisation information - CHOICE mode <ul style="list-style-type: none"> - cell parameters identity - proposed TGSN - PCCPCH RSCP - Pathloss - timeslotISCP_List - Cell measured results <ul style="list-style-type: none"> - Cell Identity - Cell synchronisation information - CHOICE mode <ul style="list-style-type: none"> - cell parameters identity - proposed TGSN - PCCPCH RSCP - Pathloss - timeslotISCP_List Measured Results on RACH Additional measured results Event Results <ul style="list-style-type: none"> - CHOICE event result - Intra-frequency event identity - Cell measurement event results - Cell parameters Id 	Check to see if set to 10 Check to see if set to "Intra-frequency measured results list" Check to see if this IE is absent Check to see if this IE is absent TDD Check to see if it's the same code for cell 1 Check to see if this IE is absent Check to see if this IE is present Check to see if this IE is absent Check to see if this IE is absent Check to see if this IE is absent Check to see if this IE is absent TDD Check to see if it's the same code for cell 2 Check to see if this IE is absent Check to see if this IE is present Check to see if this IE is absent Check to see if this IE is absent Check to see if this IE is absent Check to see if this IE is absent Check to see if it's set to 'Intra-frequency measurement event results' Check to see if this IE is set to '1g' Check to see if it's the same code for cell 1

MEASUREMENT CONTROL (Step 10 and 17).

Information Element	Value/remark
Measurement Identity Measurement Command Measurement Reporting Mode - Measurement Reporting Transfer Mode - Periodic Reporting / Event Trigger Reporting Mode Additional measurements list CHOICE measurement type - Intra-frequency cell info list - CHOICE intra-frequency cell removal - New intra-frequency info list - Cells for measurement - Intra-frequency measurement quantity - Filter Coefficient - Measurement quantity - Intra-frequency reporting quantity - Reporting quantities for active set cells - Cell synchronisation information reporting indicator - Cell identity reporting indicator - CHOICE mode - Timeslot ISCP reporting indicator - Proposed TGSN Reporting required - PCCPCH RSCP reporting indicator - Pathloss reporting indicator - Reporting quantities for monitored set cells - Cell synchronisation information reporting indicator - Cell identity reporting indicator - CHOICE mode - Timeslot ISCP reporting indicator - Proposed TGSN Reporting required - PCCPCH RSCP reporting indicator - Pathloss reporting indicator - Reporting quantities for detected cells - Reporting cell status - Measurement validity - CHOICE report criteria - reporting amount - reportingInterval DPCH compressed mode status info	12 Setup Acknowledged Mode RLC Periodical Reporting Not Present Intra-frequency measurement Remove no intra-frequency cells Not Present Not Present Not Present PCCPCH RSCP FALSE FALSE TDD FALSE FALSE FALSE TRUE FALSE FALSE FALSE TRUE FALSE Not present Not present CELL DCH Periodical reporting criteria infinity 64s Not Present

MEASUREMENT REPORT (Steps 14, 14f and 17a)

Information Element	Value/remark
Measurement identity	Check to see if set to 12
Measured Results	
- CHOICE measurement	Check to see if set to "Intra-frequency measured results list"
- Intra-frequency measurement results	
- Cell measured results	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is absent
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 1
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	Check to see if this IE is absent
- Cell measured results	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is absent
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 2
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	Check to see if this IE is absent
Measured Results on RACH	Check to see if this IE is absent
Additional measured results	Check to see if this IE is absent
Event Results	Check to see if this IE is absent

MEASUREMENT CONTROL (Step 15)

Information Element	Value/remark
Measurement Identity	12
Measurement Command	Release
Measurement Reporting Mode	Not Present
Additional measurements list	Not Present
CHOICE Measurement type	Not Present
DPCH compressed mode status info	Not Present

System Information Block type 11 for cell 2 (Step 21)

All messages content below shall use the same content as described in default message content [specified in clause 6.1 of TS 34.108](#), with the following exception:

Information Element	Value/Remark
SIB12 indicator	TRUE
FACH measurement occasion info	Not Present
Measurement control system information	Not used
- Use of HCS	PCCPCH RSCP
- Cell selection and reselection quality measure	Not present
- Intra-frequency measurement system information	Not present
- Intra-frequency measurement identity	Not present
- Intra-frequency cell info list	Not Present
- CHOICE intra-frequency cell removal	Not Present
- New intra-frequency cells	2
- Intra-frequency cell id	2
- Cell info	Not Present
- Cell individual offset	Not Present
- Reference time difference to cell	Not present
- Read SFN indicator	FALSE
- CHOICE mode	TDD
- Primary CCPCH info	FALSE
- TSTD indicator	Refer to clause titled "Default settings for cell No.1 (TDD)" in clause 6.1.4 of TS34.108
- Cell parameters Id	FALSE
- SCTD indicator	FALSE
- Primary CCPCH Tx power	Not present
- timeslot info list	Not present
- Cell Selection and Re-selection info	Not present
- Intra-frequency cell id	1
- Cell info	Not Present
- Cell individual offset	Not Present
- Reference time difference to cell	Not present
- Read SFN indicator	TRUE
- CHOICE mode	TDD
- Primary CCPCH info	FALSE
- TSTD indicator	Refer to clause titled "Default settings for cell No.1 (TDD)" in clause 6.1.4 of TS34.108
- Cell parameters Id	FALSE
- SCTD indicator	FALSE
- Primary CCPCH Tx power	Not present
- timeslot info list	Not present
- Cell Selection and Re-selection info	Not present
- Intra-frequency cell id	3
- Cell info	Not Present
- Cell individual offset	Not Present
- Reference time difference to cell	Not present
- Read SFN indicator	TRUE
- CHOICE mode	TDD
- Primary CCPCH info	FALSE
- TSTD indicator	Refer to clause titled "Default settings for cell No.1 (TDD)" in clause 6.1.4 of TS34.108
- Cell parameters Id	FALSE
- SCTD indicator	FALSE
- Primary CCPCH Tx power	Not present
- timeslot info list	Not present
- Cell Selection and Re-selection info	Not present
- Cells for measurement	Not present
- Intra-frequency measurement quantity	Not Present
- Filter coefficient	Not Present
- CHOICE mode	TDD
- Measurement quantity	PCCPCH RSCP
- Intra-frequency reporting quantity for RACH reporting	Not present
- Maximum number of reported cells on RACH	Not present
- Reporting information for state CELL_DCH	Not present

System Information Block type 12 for cell 2 (Step 21)

Information Element	Value/Remark
FACH measurement occasion info	Not Present
Measurement control system information	
- Use of HCS	Not used
- Cell selection and reselection quality measure	PCCPCH RSCP
- Intra-frequency measurement system information	
- Intra-frequency measurement identity	Not present
- Intra-frequency cell info list	Not Present
- Intra-frequency measurement quantity	
- Filter coefficient	Not Present
- CHOICE mode	TDD
- Measurement quantity	PCCPCH RSCP
- Intra-frequency reporting quantity for RACH reporting	Not present
- Maximum number of reported cells on RACH	Not present
- Reporting information for state CELL_DCH	
- Intra-frequency reporting quantity	
- Reporting quantities for active set cells	
- Cell synchronisation information reporting	FALSE
indicator	
- Cell identity reporting indicator	FALSE
- CHOICE mode	TDD
- Timeslot ISCP reporting indicator	FALSE
- Proposed TGSN Reporting required	FALSE
- PCCPCH RSCP reporting indicator	TRUE
- Pathloss reporting indicator	FALSE
- Reporting quantities for monitored set cells	
- Cell synchronisation information reporting	FALSE
indicator	
- Cell identity reporting indicator	FALSE
- CHOICE mode	TDD
- Timeslot ISCP reporting indicator	FALSE
- Proposed TGSN Reporting required	FALSE
- PCCPCH RSCP reporting indicator	TRUE
- Pathloss reporting indicator	FALSE
- Reporting quantities for detected set cells	Not Present
- Measurement reporting mode	
- Measurement Report Transfer Mode	Acknowledged mode RLC
- Periodic Reporting/Event Trigger Reporting Mode	Event trigger
- CHOICE report criteria	Intra-frequency measurement reporting criteria
- Intra-frequency measurement reporting criteria	
- Intra-frequency event identity	1g
- W	0
- Hysteresis	1dB
- Threshold Used Frequency	Not Present
- Reporting deactivation threshold	2
- Replacement activation threshold	Not Present
- Time to trigger	0
- Amount of reporting	Not Present
- Reporting interval	Not Present
- Reporting cell status	
- CHOICE reported cell	Report cell within active set and/or monitored set cells on used frequency
- Maximum number of reported cells	3
- Inter-frequency measurement system information	Not present
- Inter-RAT measurement system information	Not present
- Traffic volume measurement system information	Not Present
- UE internal measurement system information	Not Present

CELL UPDATE (Step 22)

Information Element	Value/remark
U-RNTI	Check to see if set to '0000 0000 0001'
- SRNC Identity	Check to see if set to '0000 0000 0000 0000 0001'
- S-RNTI	Check to see if it is present
START List	Checked to see if it is set to FALSE
AM_RLC error indication(RB2, RB3 or RB4)	Checked to see if it is set to FALSE
AM_RLC error indication(RB>4)	Check to see if set to 'Cell Re-selection'
Cell Update Cause	
RB timer indicator	Checked to see if it is set to 'FALSE'
- T314 expired	Checked to see if it is set to 'FALSE'
- T315 expired	Check to see if it is absent or set to 'FALSE'
Protocol error indicator	
Measured results on RACH	Check to see if it is absent
Protocol error information	Check to see if it is absent

CELL UPDATE CONFIRM (Step 23)

Use the default message content of the same message type in Annex A, with the following exceptions.

Information Element	Value/Remarks
New C-RNTI	'1010 1010 1010 1010'

UTRAN MOBILITY INFORMATION CONFIRM (Step 23a)

Only the message type is checked.

MEASUREMENT REPORT (Step 26)

Information Element	Value/Remarks
Measurement identity	Check to see if set to 1
Measured Results	
- CHOICE measurement	Check to see if set to "Intra-frequency measured results list"
- Intra-frequency measurement results	
- Cell measured results	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is absent
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 3
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	Check to see if this IE is absent
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is present
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 2
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is present
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 1
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	
Measured Results on RACH	Check to see if this IE is absent
Additional measured results	Check to see if this IE is absent
Event Results	
- CHOICE event result	Check to see if it's set to 'Intra-frequency measurement event results'
- Intra-frequency event identity	Check to see if this IE is set to '1g'
- Cell measurement event results	
- CHOICE mode	TDD
- Cell parameters Id	Check to see if it's the same code for cell 3

Note: Cells 2 and 3 can be received in any order

MEASUREMENT CONTROL (Step 27)

Information Element	Value/remark
Measurement Identity Measurement Command Measurement Reporting Mode - Measurement Reporting Transfer Mode - Periodic Reporting / Event Trigger Reporting Mode Additional measurements list CHOICE measurement type - Intra-frequency cell info list - CHOICE intra-frequency cell removal - New intra-frequency info list - Cells for measurement - Intra-frequency measurement quantity - Filter Coefficient - Measurement quantity - Intra-frequency reporting quantity - Reporting quantities for active set cells - Cell synchronisation information reporting indicator - Cell identity reporting indicator - CHOICE mode - Timeslot ISCP reporting indicator - Proposed TGSN Reporting required - PCCPCH RSCP reporting indicator - Pathloss reporting indicator - Reporting quantities for monitored set cells - Cell synchronisation information reporting indicator - Cell identity reporting indicator - CHOICE mode - Timeslot ISCP reporting indicator - Proposed TGSN Reporting required - PCCPCH RSCP reporting indicator - Pathloss reporting indicator - Reporting quantities for detected cells - Reporting cell status - Measurement validity - CHOICE report criteria - reporting amount - reportingInterval DPCH compressed mode status info	1 Setup Acknowledged Mode RLC Period Not Present Intra-frequency measurement Remove no intra-frequency cells Not Present Not Present Not Present PCCPCH RSCP FALSE FALSE TDD FALSE FALSE FALSE TRUE FALSE FALSE FALSE TRUE FALSE Not present Not present Not present period measurement criteria infinity 64s Not Present

MEASUREMENT REPORT (Step 28)

Information Element	Value/Remarks
Measurement identity	Check to see if set to 1
Measured Results	
- CHOICE measurement	Check to see if set to "Intra-frequency measured results list"
- Intra-frequency measurement results	
- Cell measured results	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is absent
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 3
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	Check to see if this IE is absent
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is present
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 2
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	
- Cell Identity	Check to see if this IE is absent
- Cell synchronisation information	Check to see if this IE is present
- CHOICE mode	TDD
- cell parameters identity	Check to see if it's the same code for cell 1
- proposed TGSN	Check to see if this IE is absent
- PCCPCH RSCP	Check to see if this IE is present
- Pathloss	Check to see if this IE is absent
- timeslotISCP_List	
Measured Results on RACH	Check to see if this IE is absent
Additional measured results	Check to see if this IE is absent
Event Results	Check to see if this IE is absent

8.4.1.7A.5 Test Requirement

After step 3 the UE shall report cell 2's PCCPCH RSCP value by transmitting MEASUREMENT REPORT messages.

After step 9 and step 11 the UE shall not transmit MEASUREMENT REPORT messages, which pertain to intra-frequency type measurement reporting.

After step 9b, the UE shall transmit a MEASUREMENT REPORT according to what is broadcast in SIB 11 and 12 of cell 1, and MEASUREMENT REPORT message pertaining to the MEASUREMENT CONTROL message that it had received in step 5.

After steps 13 and 14e, the UE shall resume the measurement and reporting activities as specified in MEASUREMENT CONTROL message received in step 10. The UE shall transmit MEASUREMENT REPORT messages, containing measured results of cell 2's PCCPCH RSCP value.

After step 15 the UE shall stop measurement activities pertaining to periodic reporting of cell 2's PCCPCH RSCP, no MEASUREMENT REPORT messages shall be detectable by the SS on the uplink DCCH.

After step 17, the UE shall transmit a MEASUREMENT REPORT message to the SS as specified in the MEASUREMENT CONTROL message received in step 17.

After step 21 the UE shall re-select to cell 2 and initiate a cell update procedure. SS shall receive a CELL UPDATE message on the uplink CCCH of cell 2, with the "cell update cause" IE stated as "cell re-selection".

After step 23, the UE shall transmit UTRAN MOBILITY INFORMATION CONFIRM message on the uplink DCCH AM RLC.

After step 25, UE shall not send MEASUREMENT REPORT message with "measurement identity" = '12'.

After step 25a the UE shall report cell 3's PCCPCH RSCP value by transmitting MEASUREMENT REPORT messages.

After step 27, UE shall send MEASUREMENT REPORT message with "measurement identity" = '1'.

<< END OF MODIFIED SECTION >>

CHANGE REQUEST

34.123-1 CR 1243 rev - Current version: 5.11.1

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the symbols.

Proposed change affects: | UICC apps ME Radio Access Network Core Network

Title:	Correction to Package 4 Inter system cell reselection test case 8.3.9.3		
Source:	3GPP TSG RAN WG5 (Testing)		
Work item code:	TEI	Date:	06/05/2005
Category:	F	Release:	Rel-5
Use <u>one</u> of the following categories: F (correction) A (corresponds to a correction in an earlier release) B (addition of feature), C (functional modification of feature) D (editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900 .		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6)	

Reason for change: The conformance requirement of this test case states that:

“If the inter-RAT cell reselection fails, the UE shall:

1> resume the connection to UTRAN using the resources used before initiating the inter-RAT cell reselection procedure.”

As per the test procedure at Step d)

“The SS increases $Q_{rxlevmin}$, so S will become negative”

This will cause UE to consider Serving UMTS cell as out of service area and initiate an Inter RAT cell reselection procedure.

However UE will not consider cell 9 (GPRS cell) suitable to camp as this cell is barred.

Later at Step f)

“The SS decreases $Q_{rxlevmin}$, so S will become positive”.

This will cause UE to consider UMTS cell suitable for cell reselection. UE will then initiate a cell updating procedure with the cause “Re-entered Service Area”.

Problem in Specification:	
1) Initiation of Cell Update procedure by the UE after step f is not mentioned. 2) As per the test procedure change in the system information (change in S value) triggers cell Reselection. This is not a real network scenario. In order to simulate a real network scenario, cell power level changed, which will trigger cell reselection procedure due to change in cell ranking.	
Summary of change:	☞ Cell power levels are changed instead of changing the S value. Due to this cell Reselection will be triggered, as GSM cell will rank better than the UMTS cell. At the same time S will be positive for the UMTS cell.
Consequences if not approved:	☞ Test case will not meet the test purpose.

Clauses affected:	☞ 8.3.9.3, 8.3.9.3.4, 8.3.9.3.5												
Other specs affected:	<table border="1"> <tr> <td>Y</td> <td>N</td> <td></td> </tr> <tr> <td></td> <td>X</td> <td>Other core specifications</td> </tr> <tr> <td>X</td> <td></td> <td>Test specifications</td> </tr> <tr> <td></td> <td>X</td> <td>O&M Specifications</td> </tr> </table>	Y	N			X	Other core specifications	X		Test specifications		X	O&M Specifications
Y	N												
	X	Other core specifications											
X		Test specifications											
	X	O&M Specifications											
Other comments:	☞ This CR will require change in TTCN.												

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <http://www.3gpp.org/specs/CR.htm>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked ☞ contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <ftp://ftp.3gpp.org/specs/> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

<< START OF MODIFIED SECTION >>

8.3.9.3 Cell reselection if ~~S<0~~[cell rank changes](#); UTRAN to GPRS (UE in CELL_FACH fails to complete an inter-RAT cell reselection)

8.3.9.3.1 Definition

8.3.9.3.2 Conformance requirement

If the inter-RAT cell reselection fails, the UE shall:

- 1> resume the connection to UTRAN using the resources used before initiating the inter-RAT cell reselection procedure.

References

TS 25.331, clause 8.3.9.4

8.3.9.3.3 Test purpose

To verify if the inter-RAT cell reselection fails before the UE in CELL_FACH succeeds in initiating the establishment of a connection to the GPRS cell, the UE shall:

- resume the connection to UTRAN using the resources used before initiating the inter-RAT cell reselection procedure.

8.3.9.3.4 Method of test

Initial conditions

System Simulator: 2 cells – Cell 1 is UTRAN FDD, Cell 9 is GPRS with PBCCH. 51.010 clauses 20.22 and 40.1.1 shall be referenced for the default parameters of cell 9.

All cells belong to the same PLMN.

The Inter-RAT Cell Info List of Cell 1 (UTRAN) refers to Cell 9 (GPRS).

The 3G Neighbour Cell Description of Cell 9 (GPRS) refers to Cell 1 (UTRAN)

UE: Power-Off (State 1) (UTRAN) as specified in clause 7.4 of TS 34.108.

[SYSTEM INFORMATION BLOCK TYPE 11 and 12 \(see specific message contents\).](#)

Related ICS/IXIT statement

- UE supports both GSM/GPRS and UTRAN Radio Access Technologies,
- UE supports Interactive/Background 32 kbps PS RAB + SRBs for CCCH + SRB for DCCH + SRB for BCCH,
- UE supports GSM-P, GSM-E, GSM-DCS, GSM-450, GSM-480.

Step a-c:

Parameter	Unit	Cell 1 (UTRAN)
Test Channel		1
CPICH Ec (FDD)	<u>dBm/</u> <u>3.84</u> <u>MHz</u>	-60
P-CCPCH RSCP (TDD)	<u>dBm</u>	-60
Qrxlevmin	dBm	-101
Treselection _s	s	5
Srxlev ^{R*}	dBm	41-58
CellBarred		Not barred

Parameter	Unit	Cell 9 (GPRS)
Test Channel		1
RF Signal Level	<u>dBm</u>	-80
GPRS_RXLEV_A CCESS_MIN	dBm	-100
C1*	dBm	20
C32*	dB	20
R*	dB	-80
CellBarred		Barred

Step d:

Parameter	Unit	Cell 1 (UTRAN)
Qrxlevmin	DB	-101 -> -41
Srxlev*	DB	41 -> -19

<u>Parameter</u>	<u>Unit</u>	<u>Cell 1 (UTRAN)</u>
<u>CPICH Ec (FDD)</u>	<u>dBm/</u> <u>3.84</u> <u>MHz</u>	<u>-60 -> -70</u>
<u>P-CCPCH RSCP (TDD)</u>	<u>dBm</u>	<u>-60 -> -70</u>
<u>R</u> ^{Srxlev*}	<u>dB</u>	<u>26-58 -> -68</u>

<u>Parameter</u>	<u>Unit</u>	<u>Cell 9 (GPRS)</u>
<u>RF Signal Level</u>	<u>dBm</u>	<u>-80 -> -50</u>
<u>C1*</u>	<u>dBm</u>	<u>20 -> 50</u>
<u>C32*</u>	<u>dB</u>	<u>20 -> 50</u>
<u>R*</u>	<u>dB</u>	<u>-80 -> -50</u>
<u>CellBarred</u>		<u>Barred</u>

Step f:

Parameter	Unit	Cell 1 (UTRAN)
Qrxlevmin	DB	-41 -> -101
Srxlev*	DB	-19 -> 41

Test procedure

- a) The SS activates cells 1 and 9. The SS monitors cells 1 and 9 for random access requests from the UE.
- b) The UE is switched on.
- c) The SS brings the UE to PS-DCCH+DTCH_FACH (State 6-11).

~~d) The SS increases $Q_{rxlevmin}$, so S will become negative.~~

~~e) The SS sends SYSTEM INFORMATION CHANGE INDICATION message to UE to inform UE of the modification in the system information.~~

~~f) The SS decreases $Q_{rxlevmin}$, so S will become positive (After the expiry of the timer $T_{reselection}$). The SS sends SYSTEM INFORMATION CHANGE INDICATION message to UE to inform UE of this change in the system information~~

d) The SS increases RF Signal Level of the GPRS Cell to -50 dBm and decreases the CPICH of the UMTS cell to -70 dBm so that the ranking of the UMTS cell goes lower than the GPRS cell.

e) After the expiry of the timer $T_{reselection}$, SS waits for 5 seconds to allow UE to read the system information for a GPRS cell and finds that the cell is barred. Later SS calls for generic procedure C.2 in cell 1 (UTRAN) to check that UE is in CELL_FACH state. The UE resumes the connection to UTRAN using the resources used before initiating the inter-RAT cell reselection procedure.

f) The UE is switched off.

~~g) SS calls for generic procedure C.2 in cell 1 (UTRAN) to check that UE is in CELL_FACH state. The UE resumes the connection to UTRAN using the resources used before initiating the inter-RAT cell reselection procedure~~

Specific Message Contents

Contents of System Information Block type 11 (FDD)

Use the same message type found in clause 6.1.0b of TS 34.108, with the following exceptions:

- FACH measurement occasion info		
- <u>FACH Measurement occasion cycle length coefficient</u> fACH-meas-occasion-coeff		3
- <u>Inter-frequency FDD measurement indicator</u> inter-freq-FDD-meas-ind		FALSE
- <u>Inter-frequency TDD measurement indicator</u> inter-freq-TDD-meas-ind		FALSE
- <u>Inter-RAT measurement indicators</u> inter-RAT-meas-ind		
- RAT-Type		GSM

Contents of System Information Block type 12 (FDD)

Use the message type reference found in clause 8.3.9 of TS 34.123-1, with the following exceptions:

FACH measurement occasion info

- <u>FACH Measurement occasion cycle length coefficient</u>	<u>3</u>
- <u>Inter-frequency FDD measurement indicator</u>	<u>FALSE</u>
- <u>Inter-frequency TDD measurement indicator</u>	<u>FALSE</u>
- <u>Inter-RAT measurement indicators</u>	
- <u>RAT-Type</u>	<u>GSM</u>

8.3.9.3.5 Test Requirements

In ~~After~~ step ~~fe~~, the UE remains in CELL_FACH in cell 1.

<< END OF MODIFIED SECTION >>

CR-Form-v7

CHANGE REQUEST

34.123-1 CR 1244 rev - Current version: **5.11.1**

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the symbols.

Proposed change affects: | UICC apps ME Radio Access Network Core Network

Title:	Correction to GCF WI-014 MAC-HS test case 7.1.5.1		
Source:	3GPP TSG RAN WG5 (Testing)		
Work item code:	TEI	Date:	28/04/2005
Category:	F	Release:	Rel-5
	Use <u>one</u> of the following categories: F (correction) A (corresponds to a correction in an earlier release) B (addition of feature), C (functional modification of feature) D (editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900 .		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6)

Reason for change:	<ol style="list-style-type: none"> RB 25 mapped on HS-DSCH/DCH is established in AM mode. With default RLC parameters specified in 34.108 clause 9 for Radio Bearer Setup Message, UE RLC layer will set poll bits, and on not reception of proper Acknowledgements, will retransmit PDU's in uplink. This polling mechanism makes expected sequence complex. To reduce the complexity and make expected sequence more deterministic, it is proposed to modify polling mechanism and hence retransmission of PDU's in uplink is avoided. Inconsistency between test procedure steps a to c and expected sequence steps 1 to 4. In Initial conditions reference to 34.108 class specified TBD
Summary of change:	<ol style="list-style-type: none"> Timer Poll Prohibit set to 1000(Max value) and Timer Poll disabled In test procedure added new step before step b, to receive loop backed PDU from UE Initial conditions reference to 34.108 clause 6.10.2.4.5.1 added.
Consequences if not approved:	Test procedure very complex to implement

Clauses affected:	7.1.5.1						
Other specs	<table border="1" style="display: inline-table; vertical-align: middle;"> <tr> <td style="text-align: center;">Y</td> <td style="text-align: center;">N</td> </tr> <tr> <td style="text-align: center;"> </td> <td style="text-align: center;">X</td> </tr> </table> Other core specifications	Y	N		X		
Y	N						
	X						

affected:

<input checked="" type="checkbox"/>	Test specifications
<input checked="" type="checkbox"/>	O&M Specifications

Other comments: ⓘ This CR affects Rel-5 and later releases

How to create CRs using this form:

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Below is a brief summary:

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- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

7.1.5 HS-DSCH MAC-hs

7.1.5.1 MAC-hs reordering and stall avoidance

7.1.5.1.1 Definition and applicability

All UEs which support HS-PDSCH.

7.1.5.1.2 Conformance requirement

When a MAC-hs PDU with $TSN = SN$ is received:

- If SN is within the receiver window:
 - if $SN < next_expected_TSN$, or this MAC-hs PDU has previously been received:
 - the MAC-hs PDU shall be discarded.
 - else:
 - the MAC-hs PDU is placed in the reordering buffer at the place indicated by the TSN.
- If SN is outside the receiver window:
 - the received MAC-hs PDU shall be placed above the highest received TSN in the reordering buffer, at the position indicated by SN;
 - $RcvWindow_UpperEdge$ shall be set to SN thus advancing the receiver window;
 - any MAC-hs PDUs with $TSN \leq RcvWindow_UpperEdge - RECEIVE_WINDOW_SIZE$, i.e. outside the receiver window after its position is updated, shall be removed from the reordering buffer and be delivered to the disassembly entity;
 - $next_expected_TSN$ shall be set to $RcvWindow_UpperEdge - RECEIVE_WINDOW_SIZE + 1$;
 - All received MAC-hs PDUs with consecutive TSNs from $next_expected_TSN$ (included) up to the first not received MAC-hs PDU are delivered to the disassembly entity.
 - $next_expected_TSN$ shall be advanced to the TSN of this first not received MAC-hs PDU.

[...]

If no timer T1 is active:

- the timer T1 shall be started when a MAC-hs PDU with $TSN > next_expected_TSN$ is correctly received.
- $T1_TSN$ shall be set to the TSN of this MAC-hs PDU.

If a timer T1 is already active:

- no additional timer shall be started, i.e. only one timer T1 may be active at a given time.

The timer T1 shall be stopped if:

- the MAC-hs PDU with $TSN = T1_TSN$ can be delivered to the disassembly entity before the timer expires.

When the timer T1 expires and $T1_TSN > next_expected_TSN$:

- all correctly received MAC-hs PDUs with $TSN > next_expected_TSN$ up to and including $T1_TSN-1$ shall be delivered to the disassembly entity;
- all correctly received MAC-hs PDUs up to the next not received MAC-hs PDU shall be delivered to the disassembly entity.
- $next_expected_TSN$ shall be set to the TSN of the next not received MAC-hs PDU.

When the timer T1 is stopped or expires, and there still exist some received MAC-hs PDUs that can not be delivered to higher layer:

- timer T1 is started
- set T1_TSN to the highest TSN among those of the MAC-hs PDUs that can not be delivered.

[...]

Reference(s)

TS 25.321 clauses 11.6.2.3.1, 11.6.2.3.2

7.1.5.1.3 Test purpose

1. To confirm that the UE performs MAC-hs reordering and delivers RLC PDUs in order to RLC.
2. To confirm that the UE performs stall avoidance in case of missing MAC-hs PDUs based on a) window based stall avoidance and b) timer based stall avoidance.

7.1.5.1.4 Method of test

Initial conditions

System Simulator:

1 cell, default parameters, Ciphering Off.

User Equipment:

The SS follows the procedure in TS 34.108 7.4.2.6 (Mobile Terminated) so that the UE shall be in state BGP 6-17 (PS-DCCH + DTCH HS-DSCH). During the procedure the radio bearer defined in TS 34.108 clause [\[6.10.2.4.5.1TBD\]](#) shall be established. The following parameters are specific for this test case:

Parameter	Value
MAC-hs receiver window size	32
MAC-hs reordering timer T1	400 ms
Polling Info	
- Timer poll periodic	1000(max value)
- Timer poll	Not Present

The radio bearer is placed into UE test loop mode 1 with the UL SDU size set to 39 octets.

Let T be the value of MAC-hs reordering timer T1 parameter.

Test procedure

In this test procedure each MAC-hs PDU contains one RLC PDU carrying one SDU of size 39 octets and one length indicator indicating the end of the SDU.

a) The SS transmits a MAC-hs PDU with Transmission Sequence Number (TSN) = 0 containing an RLC PDU with SN=0.

[b\) The SS checks that the RLC PDU with SN=0 is looped back](#)

~~b~~c) The SS transmits a MAC-hs PDU with TSN = 1 containing an RLC PDU with SN=1.

~~e~~d) The SS checks that the RLC PDUs with SN=~~0~~,1 ~~are is~~ looped back

~~d~~e) The SS repeats the transmission of the MAC-hs PDUs in step a) and ~~b~~c) with identical content except that the RLC PDUs have SN 2,3

~~e~~f) The SS checks that no data is looped back (the data is discarded in the UE)

~~f~~g) The SS transmits a MAC-hs PDU with TSN = 3 containing an RLC PDU with SN=3

g) The SS waits 400 ms and checks that no data is looped back and no RLC status report is received during that time

h) The SS transmits a MAC-hs PDU with TSN = 2 containing an RLC PDU with SN=2

i) The SS checks that the RLC PDUs with SN = 2,3 are looped back

j) The SS transmits a MAC-hs PDU with TSN = 6 containing an RLC PDU with SN=4

k) The SS transmits a MAC-hs PDU with TSN = 7 containing an RLC PDU with SN=5

l) The SS transmits a MAC-hs PDU with TSN = 38 containing an RLC PDU with SN=6

m) The SS checks that the RLC PDU with SN = 4 and 5 is looped back but the RLC PDU with SN = 6 is not looped back

n) The SS waits 400 ms and checks that the RLC PDU with SN = 6 is looped back after this time

Expected sequence

Step	Direction		Message	Comments
	UE	SS		
1	←		MAC-hs PDU with TSN = 0, containing RLC PDU with SN = 0	
2	→		RLC PDU with SN 0	
3	←		MAC-hs PDU with TSN = 1, containing RLC PDU with SN = 1	
4	→		RLC PDU with SN 1	
5	←		MAC-hs PDU with TSN = 0, containing RLC PDU with SN = 2	The duplicated data is discarded in the UE
6	←		MAC-hs PDU with TSN = 1, containing RLC PDU with SN = 3	The duplicated data is discarded in the UE
7	←		MAC-hs PDU with TSN = 3, containing RLC PDU with SN = 3	
8			SS waits T ms and checks that no data is looped back and no RLC status report is received	The waiting time may need to be adjusted to assure that T1 has not expired in the UE
9	←		MAC-hs PDU with TSN = 2, containing RLC PDU with SN = 2	
10	→		RLC PDUs with SN 2,3	
11	←		MAC-hs PDU with TSN = 6, containing RLC PDU with SN = 4	
12	←		MAC-hs PDU with TSN = 7, containing RLC PDU with SN = 5	
13	←		MAC-hs PDU with TSN = 38, containing RLC PDU with SN = 6	SS need to transmit this PDU before timer T1 in UE expires (400 ms after reception of MAC-hs PDU with TSN=6). Note: T _A
14	→		RLC PDUs with SN 4,5	The RLC PDUs with SN = 4,5 is looped back after reception of the MAC_hs PDU in step 13, i.e. before timer T1 expires
15			SS waits T ms and checks that the RLC PDU with SN = 6 is not looped back during this time	
16	→		RLC PDU with SN 6	The RLC PDU with SN = 6 is looped back after expiry of T1. Note: T _B
<p>NOTE 1: The RLC SN in step 5,6 is increased since otherwise the data would be discarded by RLC even if the MAC-hs reordering does not work correctly. Since the data is discarded the same RLC SN can be reused later in the test sequence.</p> <p>NOTE 2: In step 8 the absence of an RLC status report is used to check that the RLC PDU with SN = 3 is not delivered to RLC. If the RLC PDU was delivered to RLC the gap in the SN would trigger a status report (detection of missing PDUs).</p> <p>NOTE3: In step13, the timer T1 is restarted in the UE since the PDU with TSN = 38 can not be delivered to higher layers.</p> <p>NOTE 4: General timer tolerance as defined by 34.108 sub-clause 4.2.3 applies.</p>				

Specific Message Contents

None

7.1.5.1.5 Test requirements

1. After step 1, the RLC PDU with SN = 0 shall be looped back
2. After step 3, the RLC PDU with SN = 1 shall be looped back
3. After step 5 and 6 , no data shall be looped back
4. After step 7, no data shall be looped back and no RLC status report shall be received
5. After step 9, the RLC PDUs with SN = 2,3 shall be looped back
6. After step 13, the RLC PDUs with SN = 4,5 shall be looped back

7. In step 16, the RLC PDU with SN = 6 shall be looped back and $T_B - T_A$ shall be equal to T ms .

CHANGE REQUEST





34.123-1 CR 1245 rev - Current version: 5.11.1

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the symbols.

Proposed change affects: | UICC apps ME Radio Access Network Core Network


Title:	Correction to GCF WI-014 MAC-HS test case 7.1.5.4		
Source:	3GPP TSG RAN WG5 (Testing)		
Work item code:	TEI	Date:	28/04/2005
Category:	F	Release:	Rel-5
	<p>Use <u>one</u> of the following categories:</p> <p>F (correction)</p> <p>A (corresponds to a correction in an earlier release)</p> <p>B (addition of feature),</p> <p>C (functional modification of feature)</p> <p>D (editorial modification)</p> <p>Detailed explanations of the above categories can be found in 3GPP TR 21.900.</p>		<p>Use <u>one</u> of the following releases:</p> <p>2 (GSM Phase 2)</p> <p>R96 (Release 1996)</p> <p>R97 (Release 1997)</p> <p>R98 (Release 1998)</p> <p>R99 (Release 1999)</p> <p>Rel-4 (Release 4)</p> <p>Rel-5 (Release 5)</p> <p>Rel-6 (Release 6)</p>

Reason for change:	<ol style="list-style-type: none"> 1. RB 25 mapped on HS-DSCH/DCH is established in AM mode. With default RLC parameters specified in 34.108 clause 9 for Radio Bearer Setup Message, UE RLC layer will set poll bits, and on not reception of proper Acknowledgements, will retransmit PDU's in uplink. This polling mechanism makes expected sequence complex. To reduce the complexity and make expected sequence more deterministic, it is proposed to modify polling mechanism and hence retransmission of PDU's in uplink is avoided. 2. In Initial conditions reference to 34.108 class specified TBD 3. Check for 'no loop back PDU' after step 2 and reception of loop back PDU after step 4 missing. 4. Editorial correction
Summary of change:	<ol style="list-style-type: none"> 1. Timer Poll Prohibit set to 1000(Max value) and Timer Poll disabled 2. In initial conditions reference to 34.108 clause 6.10.2.4.5.1 added. 3. In test procedure b, added that UE will not loop back the RLC PDU 4. In test procedure d, added that UE will loop back the RLC PDU 5. Editorial correction in test procedure step 'e' 6. In expected sequence added steps 2a and 5
Consequences if not approved:	Test procedure very complex to implement

Clauses affected:		7.1.5.4										
Other specs affected:		<table border="1"><tr><td>Y</td><td>N</td></tr><tr><td></td><td>X</td></tr><tr><td></td><td>X</td></tr><tr><td></td><td>X</td></tr></table>	Y	N		X		X		X	Other core specifications	
		Y	N									
			X									
	X											
	X											
	Test specifications											
	O&M Specifications											
Other comments:		This CR affects Rel-5 & later releases.										

How to create CRs using this form:

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- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

7.1.5.4 MAC-hs retransmissions

7.1.5.4.1 Definition and applicability

All UEs which support HS-PDSCH.

7.1.5.4.2 Conformance requirement

[...]

The UE shall:

- if the New Data Indicator has been incremented compared to the value in the previous received transmission in this HARQ process or this is the first received transmission in the HARQ process:
 - replace the data currently in the soft buffer for this HARQ process with the received data.
- if the Transport Block Size index value is equal to 111111 (FDD only):
 - generate a positive acknowledgement (ACK) of the data in this HARQ process;
 - discard the received data;
 - assume that the data has been successfully decoded.
- if the New Data Indicator is identical to the value used in the previous received transmission in the HARQ process:
 - if the Transport Block Size index value is equal to 111111 (FDD only):
 - assume that the transport block size is identical to the last valid transport block size signalled for this HARQ process.
 - if the data has not yet been successfully decoded:
 - combine the received data with the data currently in the soft buffer for this HARQ process.
- if the data in the soft buffer has been successfully decoded and no error was detected:
 - deliver the decoded MAC-hs PDU to the reordering entity;
 - generate a positive acknowledgement (ACK) of the data in this HARQ process.
- else:
 - generate a negative acknowledgement (NAK) of the data in this HARQ process;
- schedule the generated positive or negative acknowledgement for transmission and the time of transmission relative to the reception of data in a HARQ process is configured by upper layer.

[...]

Reference(s)

TS 25.321 clauses 11.6.22

7.1.5.4.3 Test purpose

1. To confirm that the UE correctly transmit positive and negative acknowledgements when receiving MAC-hs PDUs

7.1.5.4.4 Method of test

Initial conditions

System Simulator:

1 cell, default parameters, Ciphering Off.

User Equipment:

The SS follows the procedure in TS 34.108 7.4.2.6 (Mobile Terminated) so that the UE shall be in state BGP 6-17 (PS-DCCH + DTCH HS-DSCH). During the procedure the radio bearer defined in TS 34.108 clause [6.10.2.4.5.1 ~~TBD~~] shall be established.

<u>Parameter</u>	<u>Value</u>
<u>Polling info</u>	
<u>- Timer Poll Periodic</u>	<u>1000(Max Value)</u>
<u>- Timer_poll</u>	<u>Not Present</u>

The radio bearer is placed into UE test loop mode 1 with the UL SDU size set to 39 octets.

Test procedure

In this test procedure each MAC-hs PDU contains one RLC PDU carrying one SDU of size 39 octets and one length indicator indicating the end of the SDU.

- a) The SS transmits a MAC-hs PDU where:
 1. The TSN = 0
 2. The HARQ process id = 0
 3. The Queue ID = 0
 4. The MAC-hs PDU contains an RLC PDU with SN=0.
 5. The physical layer CRC is modified such that the CRC check in the UE will fail
- b) The SS checks that a negative acknowledgement is received for the correct HARQ process and no RLC PDU loop backed by UE
- c) The SS transmits a MAC-hs PDU with the same content as in step a) but where the CRC is correct
- d) The SS checks that a positive acknowledgement is received for the correct HARQ process and RLC PDU is loop backed by UE.
- e) The SS repeats steps a), b), ~~c)~~ & d) with the HARQ process, TSN and RLC SN set as follows for iteration 2 to ~~7~~8:

Iteration	HARQ process	TSN	RLC SN
1	0	0	0
2	1	1	1
3	2	2	2
4	3	3	3
5	4	4	4
6	5	5	5
7	6	6	6
8	7	7	7

Expected sequence

Step	Direction		Message	Comments
	UE	SS		
1		←	MAC-hs PDU sent in process N	Erroneous CRC
2		→	MAC-hs negative acknowledgement with process id = N	
2a				SS checks for 5 sec that UE does not send loop backed PDU
3		←	MAC-hs PDU sent in process N	
4		→	MAC-hs positive acknowledgement with process id = N	
5		→	RLC Loop Backed PDU	
NOTE: The process id N in step 1-4 is taken from the table in the Test procedure description above.				

Steps 1 to [5](#)4 of the expected sequence are repeated for iteration 2-8.

7.1.5.4.5 Test requirements

1. After step 1, a MAC-hs negative acknowledgement shall be received for the correct HARQ process
2. After step 3, a MAC-hs positive acknowledgement shall be received for the correct HARQ process

CR-Form-v7

CHANGE REQUEST

34.123-1 CR 1246 rev - Current version: **5.11.1**

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Proposed change affects: | UICC apps ME Radio Access Network Core Network


Title:	Correction to GCF WI-014 MAC-HS test case 7.1.5.5		
Source:	3GPP TSG RAN WG5 (Testing)		
Work item code:	TEI	Date:	28/04/2005
Category:	F	Release:	Rel-5
	Use <u>one</u> of the following categories: F (correction) A (corresponds to a correction in an earlier release) B (addition of feature), C (functional modification of feature) D (editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900 .		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6)

Reason for change:	RB 25 mapped on HS-DSCH/DCH is established in AM mode. With default RLC parameters specified in 34.108 clause 9 for Radio Bearer Setup Message, UE RLC layer will set poll bits, and on not reception of proper Acknowledgements, will retransmit PDU's in uplink. This polling mechanism makes expected sequence complex. To reduce the complexity and make expected sequence more deterministic, it is proposed to modify polling mechanism and hence retransmission of PDU's in uplink is avoided.
Summary of change:	<ol style="list-style-type: none"> 1. Timer Poll Prohibit set to 1000(Max value) and Timer Poll disabled 2. Editorial correction in expected sequence step 10 3. Test requirement 5, 'after step 5' is changed to 'after step 9'
Consequences if not approved:	Test procedure very complex to implement

Clauses affected:	7.1.5.5						
Other specs affected:	<table border="1" style="display: inline-table; border-collapse: collapse; text-align: center;"> <tr> <td style="padding: 2px 5px;">Y</td> <td style="padding: 2px 5px;">N</td> </tr> <tr> <td style="padding: 2px 5px;"><input type="checkbox"/></td> <td style="padding: 2px 5px;"><input checked="" type="checkbox"/></td> </tr> </table> Other core specifications	Y	N	<input type="checkbox"/>	<input checked="" type="checkbox"/>		
Y	N						
<input type="checkbox"/>	<input checked="" type="checkbox"/>						
	<table border="1" style="display: inline-table; border-collapse: collapse; text-align: center;"> <tr> <td style="padding: 2px 5px;"><input checked="" type="checkbox"/></td> <td style="padding: 2px 5px;"><input checked="" type="checkbox"/></td> </tr> <tr> <td style="padding: 2px 5px;"><input checked="" type="checkbox"/></td> <td style="padding: 2px 5px;"><input checked="" type="checkbox"/></td> </tr> </table> Test specifications	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>		
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>						
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>						
	<table border="1" style="display: inline-table; border-collapse: collapse; text-align: center;"> <tr> <td style="padding: 2px 5px;"><input checked="" type="checkbox"/></td> <td style="padding: 2px 5px;"><input checked="" type="checkbox"/></td> </tr> </table> O&M Specifications	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>				
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>						
Other comments:	This CR affects Rel-5 and later releases						

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- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

7.1.5.5 MAC-hs reset

7.1.5.5.1 Definition and applicability

All UEs which support HS-PDSCH.

7.1.5.5.2 Conformance requirement

If a reset of the MAC-hs entity is requested by upper layers, the UE shall:

- flush soft buffer for all configured HARQ processes;
- stop all active re-ordering release timer (T1) and set all timer T1 to their initial value;
- start TSN with value 0 for the next transmission on every configured HARQ process;
- initialise the variables RcvWindow_UpperEdge and next_expected_TSN to their initial values;
- disassemble all MAC-hs PDUs in the re-ordering buffer and deliver all MAC-d PDUs to the MAC-d entity;
- flush the re-ordering buffer.

and then:

- indicate to all AM RLC entities mapped on HS-DSCH to generate a status report.

[...]

Reference(s)

TS 25.321 clause 11.6.2.5

7.1.5.5.3 Test purpose

1. To confirm that the UE flushes the reordering buffer and delivers all MAC-d PDUs in the buffer to higher layers upon reset.
2. To confirm that the UE initializes the TSN and next_expected_TSN to their initial values.
3. To confirm that the UE sends an RLC status report after the reset.

7.1.5.5.4 Method of test

Initial conditions

System Simulator:

1 cell, default parameters, Ciphering Off

User Equipment:

The SS follows the procedure in TS 34.108 7.4.2.6 (Mobile Terminated) so that the UE shall be in state BGP 6-17 (PS-DCCH + DTCH HS-DSCH). The following parameters are specific for this test case:

Parameter	Value
MAC-hs receiver window size	32
MAC-hs reordering timer T1	400 ms
Polling Info	
- Timer poll prohibit	1000 (max value)
- Timer poll	Disabled

The radio bearer is placed into UE test loop mode 1 with the UL SDU size set to 39 octets.

Test procedure

In this test procedure each MAC-hs PDU contains one RLC PDU carrying one SDU of size 39 octets and one length indicator indicating the end of the SDU.

- a) The SS transmits a MAC-hs PDU with Transmission Sequence Number (TSN) = 0 containing an RLC PDU with SN=0
- b) The SS checks that the RLC PDU with SN=0 is looped back
- b) The SS transmits 2 MAC-hs PDUs with TSN = 2,3 containing the RLC PDUs with SN=1,2
- c) The SS initiates a MAC-hs reset by transmitting a PHYSICAL CHANNEL RECONFIGURATION message
- d) The SS checks that the RLC PDUs with SN=1,2 are looped back
- e) The SS checks that an RLC status report is transmitted by the UE
- d) The SS transmits a MAC-hs PDU with TSN = 0 containing an RLC PDU with SN=3
- e) The SS checks that the RLC PDU with SN=3 is looped back

Expected sequence

Step	Direction		Message	Comments
	UE	SS		
1		←	MAC-hs PDU with TSN = 0, containing RLC PDU with SN = 0	
2		→	RLC PDU with SN 0	
3		←	MAC-hs PDU with TSN = 2, containing RLC PDU with SN = 1	
4		←	MAC-hs PDU with TSN = 3, containing RLC PDU with SN = 2	
5		←	SS transmits a PHYSICAL CHANNEL RECONFIGURATION message to trigger a MAC-hs reset	Note: Time Ta
6		→	PHYSICAL CHANNEL RECONFIGURATION COMPLETE	
7		→	RLC PDUs with SN 1,2	The RLC PDUs are delivered directly after the MAC-hs reset i.e. before T1 expires. Note: time Tb
8		→	RLC status report	
9		←	MAC-hs PDU with TSN = 0, containing RLC PDU with SN = 3	
10		→	RLC PDU with SN 3	

NOTE : Steps 6-8 may occur in different order.

Specific Message Contents

PHYSICAL CHANNEL RECONFIGURATION (Step 5)

Use the same message as specified for "Packet to CELL_DCH from CELL_DCH in PS" in 34.108 except for the following:

Information Element	Value/remark
Downlink information common for all radio links - MAC-hs reset indicator	TRUE

7.1.5.5.5 Test requirements

1. After step 1, the RLC PDU with SN = 0 shall be looped back
2. After step 5, the RLC PDUs with SN = 1,2 shall be looped back

3. The time $T_b - T_a$ shall be less than $T1/2$
3. After step 5 an RLC status report shall be received
4. After step ~~9~~⁵, the RLC PDU with SN=3 shall be looped back

3GPP RAN WG5 Meeting #27
 Bath, England, 25-29 April, 2005

Tdoc **R5-050985**

CR-Form-v7
CHANGE REQUEST
⌘ 34.123-1 CR 1247 ⌘ rev - ⌘ Current version: 5.11.1 ⌘

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the ⌘ symbols.

Proposed change affects: | UICC apps ME Radio Access Network Core Network

Title:	⌘ Correction to GCF WI-10 NAS Test Cases 12.2.1.2, 12.2.1.5a Proc 1, 12.2.1.5a Proc 2, 12.3.2.7, 12.4.1.2 and 12.6.1.2		
Source:	⌘ 3GPP TSG RAN WG5 (Testing)		
Work item code:	⌘ TEI Date: ⌘ 10/04/2005		
Category:	<table style="width: 100%; border: none;"> <tr> <td style="width: 50%; vertical-align: top;"> ⌘ F Use <u>one</u> of the following categories: F (correction) A (corresponds to a correction in an earlier release) B (addition of feature), C (functional modification of feature) D (editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900. </td> <td style="width: 50%; vertical-align: top;"> Release: ⌘ Rel-5 Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6) </td> </tr> </table>	⌘ F Use <u>one</u> of the following categories: F (correction) A (corresponds to a correction in an earlier release) B (addition of feature), C (functional modification of feature) D (editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900 .	Release: ⌘ Rel-5 Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6)
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Reason for change:	⌘ In response to action item AP26.10 raised in T1#26, this CR is raised to complete this action. <ol style="list-style-type: none"> 1. The presence of the statement “Parameter mobile identity is ...” implies that the mobile identity (i.e. IMSI or TMSI) shall be checked against an expected value. The current TTCN implementation does this checking in some of the test cases, whereas in some of the test cases this mobile identity is allocated to the UE. As the checking of this IE is not part of the test purpose. It is felt that the statement should be removed in order to align the prose with the TTCN. 2. Where the checking of the mobile parameter is required, the wording has been changed to provide clarity. 3. Where applicable in CS registration, a statement of the TMSI value allocated by the SS is added to provide clarity.
Summary of change:	⌘ <ol style="list-style-type: none"> 1. The statement “Parameter mobile identity is ...” has been removed and added check for Mobile Identity in case it is checked in TTCN. Also added a comment indicating which Mobile Identity to be assigned to UE. 2. Where the checking is required, the statement “Parameter mobile identity is ...” has been replaced by “SS checks Mobile identity ...”. 3. Where applicable in CS registration, a statement to state the SS allocating a TMSI value have been added.
Consequences if not approved:	⌘ The prose will remian unclear.

Clauses affected:	⌘	12.2.1.2.4, 12.2.1.5a.1.4, 12.2.1.5a.2.4, 12.3.2.7.4, 12.4.1.4 and 12.6.1.2.4								
Other specs affected:	⌘	<table border="1"> <tr> <td>Y</td> <td>N</td> </tr> <tr> <td></td> <td>X</td> </tr> <tr> <td></td> <td>X</td> </tr> </table>	Y	N		X		X	Other core specifications	⌘
		Y	N							
			X							
	X									
	X	Test specifications								
	X	O&M Specifications								
Other comments:	⌘	No impact to TTCN as the TTCN is already implemented this way.								

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12.2.1.2 PS attach / rejected / IMSI invalid / illegal UE

12.2.1.2.1 Definition

12.2.1.2.2 Conformance requirement

- 1) If the network rejects a PS attach procedure from the User Equipment with the cause 'Illegal MS, the User Equipment shall consider USIM invalid for PS services until power is switched off or USIM is removed.
- 2) If the network rejects a PS attach procedure from the User Equipment with the cause 'Illegal MS the User Equipment shall delete the stored RAI, PS-CKSN, P-TMSI and P-TMSI signature.
- 3) If the network rejects a PS attach procedure from the User Equipment with the cause 'Illegal MS, the User Equipment shall delete the LAI.

Reference

3GPP TS 24.008 clause 4.7.3.1.

12.2.1.2.3 Test purpose

To test the behaviour of the UE if the network rejects the PS attach procedure of the UE with the cause 'illegal MS.

12.2.1.2.4 Method of test

Initial condition

System Simulator:

Three cells (not simultaneously activated), cell A with MCC1/MNC1/LAC1/RAC1 (RAI-1), cell B in MCC1/MNC1/LAC2/RAC1 (RAI-3), cell C in MCC2/MNC1/LAC1/RAC1 (RAI-2).

All three cells are operating in network operation mode II (in case of UE operation mode A).

The SIB1 IE "CN domain specific NAS system information", for the CS Domain, is set to value "00 00" (T3212 value is set to 0 and ATT flag is set to FALSE in all cells.

NB: i) Cell C will be mapped to Cell 4 as found in TS 34.108 clause 6.1.4.1.

User Equipment:

The UE has a valid P-TMSI-1 and RAI-1.

Related ICS/IXIT statements

Support of PS service Yes/No

UE operation mode C Yes/No

UE operation mode A Yes/No (only if mode C not supported)

Switch off on button Yes/No

Automatic PS attach procedure at switch on or power on Yes/No

Test procedure

The SS rejects a PS attach with the cause value 'Illegal MS'. The SS checks that the UE does not perform PS attach in the same or another PLMN.

Expected Sequence

Step	Direction		Message	Comments
	UE	SS		
1	UE			The following messages are sent and shall be received on cell A. The UE is set in UE operation mode C (see ICS).
2	SS			The SIB1 IE "CN domain specific NAS system information", for the CS Domain, is set to value "00 00" (T3212 value is set to 0 and ATT flag is set to FALSE) in all cells. The SS is set in network operation mode II. Set the cell type of cell A to the "Serving cell". Set the cell type of cell B to the "Non-Suitable cell". Set the cell type of cell C to the "Non-Suitable cell". (see note)
3	UE			The UE is powered up or switched on and initiates an attach (see ICS). Cell A is preferred by the UE.
3a			Void	
4	->		ATTACH REQUEST	Attach type = 'GPRS attach' Mobile identity = P-TMSI-1 Old Routing area identity = RAI-1
5	<-		ATTACH REJECT	GMM cause = 'Illegal MS'.
6	SS			The following messages are sent and shall be received on cell B. Set the cell type of cell A to the "Non-Suitable cell". Set the cell type of cell B to the "Serving cell". (see note)
7	UE			Cell B is preferred by the UE.
8	UE			No ATTACH REQUEST sent to the SS (SS waits 30 seconds).
9	UE			The UE initiates an attach by MMI or by AT command.
10	UE			No ATTACH REQUEST sent to the SS (SS waits 30 seconds).
11	SS			The following messages are sent and shall be received on cell C. Set the cell type of cell B to the "Non-Suitable cell". Set the cell type of cell C to the "Serving cell". (see note)
12	UE			Cell C is preferred by the UE.
13	UE			No ATTACH REQUEST sent to the SS (SS waits 30 seconds).
14	UE			The UE initiates an attach by MMI or by AT command.
15	UE			No ATTACH REQUEST sent to the SS (SS waits 30 seconds).
16	UE			If possible (see ICS) switch off is performed. Otherwise the power is removed.
17	UE		Registration on CS	The UE is powered up or switched on. See TS 34.108
18	UE			This is applied only for UE in UE operation mode A. Parameter mobile identity is IMSI. SS allocates Mobile identity = TMSI-1.
19	UE			The UE initiates an attach (see ICS).
20	->		ATTACH REQUEST	Attach type = 'GPRS attach' Mobile identity = IMSI
20a	<-		AUTHENTICATION AND CIPHERING REQUEST	
20b	->		AUTHENTICATION AND CIPHERING RESPONSE	

20c 21	SS -<	ATTACH ACCEPT	The SS starts integrity protection. Attach result = 'GPRS only attached' Allocated P-TMSI = P-TMSI-1 P-TMSI Signature = P-TMSI-1 signature Routing area identity = RAI-2
22	->	ATTACH COMPLETE	
NOTE: The definitions for "Non-Suitable cell" and "Serving cell" are specified in TS34.108 clause 6.1 "Reference Radio Conditions for signalling test cases only".			

Specific message contents

None.

12.2.1.2.5 Test requirements

At step4, when the UE is powered up or switched on, UE shall:

- initiate the PS attach procedure with the information elements specified in the above Expected Sequence.

At step8, 10, 13 and 15, UE shall:

- not send the ATTACH REQUEST message to SS, even if there is an instruction of attach request from MMI or from AT command.

At step20, UE shall:

- initiate the PS attach procedure with the information elements specified in the above Expected Sequence.

12.2.1.5a PS attach / rejected / roaming not allowed in this location area

12.2.1.5a.1 Definition

12.2.1.5a.2 Conformance requirement

- 1) If the network rejects a PS attach procedure from the User Equipment with the cause 'roaming not allowed in this location area' the User Equipment shall:
 - 1.1 not perform PS attach when in the same location area.
 - 1.2 delete the stored RAI, PS-CKSN, P-TMSI and P-TMSI signature.
 - 1.3 store the LA in the 'forbidden location areas for roaming' list.
 - 1.4 perform PS attach when a new location area is entered.
 - 1.5 Periodically search for its HPLMN.
- 2) The User Equipment shall reset the list of 'Forbidden location areas for roaming' when switched off or when the USIM is removed.
- 3) The UE shall be capable of storing at least 10 entries in the list of 'Forbidden location areas for roaming'.

Reference

3GPP TS 24.008 clause 4.7.3.1.

12.2.1.5a.3 Test purpose

Test purpose 1

To test that on receipt of a rejection using the 'roaming not allowed in this location area' cause code, the UE ceases trying to attach on that location area. Successful PS attach procedure is possible in other location areas.

Test purpose 2

To test that if the UE is switched off or the USIM is removed the list of 'forbidden location areas for roaming' is cleared.

Test purpose 3

To test that at least 6 entries can be held in the list of 'forbidden location areas for roaming' (the requirement in 3GPP TS 24.008 is to store at least 10 entries. This is not fully tested by the third procedure).

Test purpose 4

To test that if a cell of the Home PLMN is available then the UE returns to it in preference to any other available cell.

12.2.1.5a.4 Method of test**12.2.1.5a.4.1 Test procedure 1****Initial condition****System Simulator:**

Three cells (not simultaneously activated), cell A in MCC2/MNC1/LAC1/RAC1 (RAI-2, Not HPLMN), cell B in

MCC2/MNC1/LAC2/RAC1 (RAI-6, Not HPLMN) and cell C in MCC2/MNC1/LAC1/RAC2 (RAI-7, Not HPLMN).

All three cells are operating in network operation mode II.

The SIB1 IE "CN domain specific NAS system information", for the CS Domain, is set to value "00 00" (T3212 value is set to 0 and ATT flag is set to FALSE) in all cells.

User Equipment:

The UE has a valid P-TMSI-1 and RAI-2.

Related ICS/IXIT statements

Support of PS service	Yes/No	
UE operation mode C	Yes/No	
UE operation mode A	Yes/No	
Switch off on button	Yes/No	
Automatic PS attach procedure at switch on or power on		Yes/No

Test procedure

The SS rejects a PS attach with the cause value 'Roaming not allowed in this location area'. A new attempt for a PS attach is not possible. Successful PS attach / detach procedures are performed in another location area. A new attempt for a PS attach is performed in the 1st location area. This attempt shall not succeed, as the LA is on the forbidden list.

Expected Sequence

Step	Direction		Message	Comments
	UE	SS		
1		SS		The following messages are sent and shall be received on cell A. The UE is set in UE operation mode C (see ICS). If UE operation mode C not supported, goto step 19. Set the cell type of cell A to the "Serving cell". Set the cell type of cell B to the "Non-Suitable cell". Set the cell type of cell C to the "Non-Suitable cell". (see note) The UE is powered up or switched on and initiates an attach (see ICS). Cell A is preferred by the UE.
2	UE			
3		SS		
3a			Void	
3b		SS		
4	->		ATTACH REQUEST	
5	<-		ATTACH REJECT	SS checks that the IE "Establishment cause" in the received RRC CONNECTION REQUEST message is set to "Registration". Attach type = 'GPRS attach' Mobile identity = P-TMSI-1 Old Routing area identity = RAI-2 GMM cause = 'Roaming not allowed in this location area'
6	UE			No ATTACH REQUEST sent to SS (SS waits 30 seconds).
6a		SS		The SS releases the RRC connection.
7		SS		The following messages are sent and shall be received on cell B. Set the cell type of cell A to the "Non-Suitable cell". Set the cell type of cell B to the "Serving cell". (see note) Cell B is preferred by the UE. See TS 34.108 This is applied only for UE in UE operation mode A. Parameter mobile identity is IMSI. SS checks Mobile identity = IMSI SS allocates Mobile identity = TMSI-1.
8	UE		Registration on CS	
9	UE			
10	UE			
10a		SS		
11	->		ATTACH REQUEST	
11a	<-		AUTHENTICATION AND CIPHERING REQUEST	
11b	->		AUTHENTICATION AND CIPHERING RESPONSE	
11c		SS		
12	<-		ATTACH ACCEPT	
13	->		ATTACH COMPLETE	
13a		SS		
14	UE			
14a		SS		
15	->		DETACH REQUEST	
16	<-		DETACH ACCEPT	

16a	SS		The SS releases the RRC connection.
17	SS		The following messages are sent and shall be received on cell C. Set the cell type of cell B to the "Non-Suitable cell". Set the cell type of cell C to the "Serving cell". (see note)
18	UE		Cell C is preferred by the UE.
19	UE		No ATTACH REQUEST sent to SS (SS waits 30 seconds). The UE is switched off or power is removed (see ICS)
20	UE		UE is switched off.
21	SS		Set the cell type of cell C to the "Non-Suitable cell". (see note)
22	UE		The UE is set in UE operation mode A if supported (see ICS) and the test is repeated from step 2 to step 20.
NOTE: The definitions for "Non-Suitable cell" and "Serving cell" are specified in TS34.108 clause 6.1 "Reference Radio Conditions for signalling test cases only".			

12.2.1.5a.4.2 Test procedure 2

Initial condition

System Simulator:

One cell in MCC2/MNC1/LAC1/RAC1 (RAI-2, Not HPLMN) operating in network operation mode II. The SIB1 IE "CN domain specific NAS system information", for the CS Domain, is set to value "00 00" (T3212 value is set to 0 and ATT flag is set to FALSE).

User Equipment:

The UE has a valid P-TMSI-1 and RAI-2.

Related ICS/IXIT statements

Support of PS service	Yes/No
UE operation mode C	Yes/No
UE operation mode A	Yes/No (only if mode C not supported)
Switch off on button	Yes/No
Automatic PS attach procedure at switch on or power on	Yes/No

Test procedure

The SS rejects a PS attach updating with the cause value 'Roaming not allowed in this location area'. The UE is switched off for 10 s and switched on again. The SS check that a PS attach is possible on the cell on which the PS attach had been rejected.

If USIM removal is possible without switching off: The SS rejects a PS attach with the cause value 'Roaming not allowed in this location area'. The USIM is removed and inserted in the UE. The SS check that a PS attach is possible on the cell on which the PS attach had been rejected.

Expected Sequence

Step	Direction		Message	Comments
	UE	SS		
1	UE			If UE operation mode C is supported, the UE is set in UE operation mode C (see ICS). If UE operation mode C is not supported, the UE is set in UE operation mode A.
2	UE			The UE is powered up or switched on and initiates an attach (see ICS).
2a			Void	
2b		SS		SS checks that the IE "Establishment cause" in the received RRC CONNECTION REQUEST message is set to "Registration".
3		->	ATTACH REQUEST	Attach type = 'GPRS attach' Mobile identity = P-TMSI-1 Old Routing area identity = RAI-2
4		<-	ATTACH REJECT	GMM cause = 'Roaming not allowed in this location area'
5	UE			No ATTACH REQUEST sent to the SS (SS waits 30 seconds).
5a		SS		The SS releases the RRC connection.
6	UE			If possible (see ICS) switch off is performed. Otherwise the power is removed.
7	UE			The UE is powered up or switched on and initiates an attach (see ICS).
8				
8a		SS		SS checks that the IE "Establishment cause" in the received RRC CONNECTION REQUEST message is set to "Registration".
8b	UE		Registration on CS	See TS 34.108 This is applied only for UE in UE operation mode A. Parameter mobile identity is IMSI- SS allocates Mobile identity = TMSI-1.
9		->	ATTACH REQUEST	Attach type = 'GPRS attach' Mobile identity = IMSI
9a		<-	AUTHENTICATION AND CIPHERING REQUEST	
9b		->	AUTHENTICATION AND CIPHERING RESPONSE	
9c		SS		The SS starts integrity protection.
10		<-	ATTACH ACCEPT	Attach result = 'GPRS only attached' Allocated P-TMSI = P-TMSI-1 P-TMSI Signature = P-TMSI-1 signature Routing area identity = RAI-2
11		->	ATTACH COMPLETE	
11a		SS		The SS releases the RRC connection.

12.3.2.7 PS detach / rejected / Roaming not allowed in this location area

12.3.2.7.1 Definition

12.3.2.7.2 Conformance requirement

- 1) If the network performs a PS detach procedure with the cause 'Roaming not allowed in this location area' the User Equipment shall:
 - 1.1 delete any RAI, P-TMSI, P-TMSI signature and PS ciphering key sequence number.
 - 1.2 set the GPRS update status to GU3 ROAMING NOT ALLOWED.
 - 1.3 reset the attach attempt counter.
 - 1.4 store the LAI in the list of "forbidden location areas for roaming".

- 1.5 perform a PLMN selection.
- 2) If the UE is IMSI attached via MM procedures, the UE shall in addition:
 - 2.1 delete any TMSI, LAI and ciphering key sequence number.
 - 2.2 reset the location update attempt counter.

Reference

3GPP TS 24.008 clauses 4.7.4.2.

12.3.2.7.3 Test purpose

To test the behaviour of the UE if the network orders the PS detach procedure with the cause ' Roaming not allowed in this location area '.

12.3.2.7.4 Method of test

Initial condition

System Simulator:

Three cells (not simultaneously activated), cell A in MCC2/MNC1/LAC1/RAC2 (RAI-2, Not HPLMN), cell B in MCC2/MNC1/LAC1/RAC2 (RAI-7, Not HPLMN), cell C in MCC2/MNC1/LAC2/RAC1 (RAI-6, Not HPLMN).

All cells are operating in network operation mode I.

User Equipment:

The UE has a valid IMSI.

Related ICS/IXIT statements

Support of PS service	Yes/No	
UE operation mode A	Yes/No	
Switch off on button	Yes/No	
Automatic PS attach procedure at switch on or power on		Yes/No

Test procedure

The SS orders a PS detach with the cause value ' Roaming not allowed in this location area '. The SS checks that the UE does not perform combined PS attach while in the location area, performs PS attach when a new location area is entered and deletes the list of forbidden LAs when switched off. CS services are not possible unless an IMSI attach procedure is performed.

Expected Sequence

Step	Direction		Message	Comments
	UE	SS		
1		SS		The following messages are sent and shall be received on cell A. Set the cell type of cell A to the "Serving cell". Set the cell type of cell B to the "Non-Suitable cell". Set the cell type of cell C to the "Non-Suitable cell". (see note)
2		SS		The UE is set in UE operation mode A (see ICS).
3		UE		The UE is powered up or switched on and initiates an attach (see ICS). Cell A is preferred by the UE.
4		->	ATTACH REQUEST	Attach type = 'Combined GPRS/IMSI attach' Mobile identity = IMSI TMSI status = no valid TMSI available
4a		<-	AUTHENTICATION AND CIPHERING REQUEST	
4b		->	AUTHENTICATION AND CIPHERING RESPONSE	
4c		SS		The SS starts integrity protection.
5		<-	ATTACH ACCEPT	Attach result = 'Combined GPRS/IMSI attached' Allocated P-TMSI = P-TMSI-1 P-TMSI Signature = P-TMSI-1 signature MS identity = TMSI-1 Routing area identity = RAI-2
6		->	ATTACH COMPLETE	
7		<-	DETACH REQUEST	Detach type = 're-attach not required' Cause 'Roaming not allowed in this location area'
8		->	DETACH ACCEPT	
9		UE		No LOCATION UPDATING REQ with type 'IMSI attach' is sent to the SS (SS waits 30 seconds).
10		<-	PAGING TYPE1	Mobile identity = TMSI-1 Paging order is for CS services.
11		UE		The UE shall not initiate an RRC connection. This is checked during 3 seconds.
12		<-	PAGING TYPE1	Mobile identity = P-TMSI-1 Paging order is for PS services.
13		UE		No response from the UE to the request. This is checked for 10 seconds
14		SS		The following messages are sent and shall be received on cell B. Set the cell type of cell A to the "Non-Suitable cell". Set the cell type of cell B to the "Serving cell". (see note)
15		UE		Cell B is preferred by the UE.
16		UE		The UE initiates an attach automatically, by MMI or by AT command.
17		UE		No ATTACH REQUEST sent to SS (SS waits 30 seconds)
18		UE		No LOCATION UPDATING REQ with type 'IMSI attach' is sent to the SS (SS waits 30 seconds).
19		<-	PAGING TYPE1	Mobile identity = TMSI-1 Paging order is for CS services.
20		UE		The UE shall not initiate an RRC connection. This is checked during 3 seconds.
21		<-	PAGING TYPE1	Mobile identity = P-TMSI-1 Paging order is for PS services.
22		UE		No response from the UE to the request. This is checked for 10 seconds

Step	Direction		Message	Comments
	UE	SS		
23		SS		The following messages are sent and shall be received on cell C. Set the cell type of cell B to the "Non-Suitable cell". Set the cell type of cell C to the "Serving cell". (see note)
24	UE			Cell C is preferred by the UE. Step 25 is only performed for non-auto attach UE.
25	UE		Registration on CS	See TS34.108
26	UE			Parameter mobile identity is IMSI.
27	->		ATTACH REQUEST	The UE initiates an attach automatically (See ICS), by MMI or AT command. Attach type = 'Combined GPRS/IMSI attach' Mobile identity = IMSI TMSI status = no valid TMSI available
28		<-	ATTACH ACCEPT	Attach result = 'Combined GPRS/IMSI attached' Allocated P-TMSI = P-TMSI1 P-TMSI Signature = P-TMSI-1 signature MS identity = TMSI-1 Routing area identity = RAI-6
29		->	ATTACH COMPLETE	
30		<-	PAGING TYPE1	Mobile identity = TMSI-1 Paging order is for CS services.
31		->	RRC CONNECTION REQUEST	
32		<-	RRC CONNECTION SETUP	
33		->	RRC CONNECTION SETUP COMPLETE	
34		->	PAGING RESPONSE	Mobile identity = TMSI-1
35		<-	RRC CONNECTION RELEASE	After sending of this message, the SS waits for disconnection of the CS signalling link.
36		->	RRC CONNECTION RELEASE COMPLETE	
37		<-	PAGING TYPE1	Mobile identity = P-TMSI-1 Paging order is for PS services.
38		->	RRC CONNECTION REQUEST	
39		<-	RRC CONNECTION SETUP	
40		->	RRC CONNECTION SETUP COMPLETE	
41		->	SERVICE REQUEST	service type = "paging response"
42		<-	RRC CONNECTION RELEASE	
43		->	RRC CONNECTION RELEASE COMPLETE	
44	UE			The UE is switched off or power is removed (see ICS).
45		->	DETACH REQUEST	Message not sent if power is removed. Detach type = 'power switched off, combined GPRS / IMSI detach'
45a		SS		The SS releases the RRC connection. If no RRC CONNECTION RELEASE COMPLETE message have been received within 1 second then the SS shall consider the UE as switched off.
46	UE			The following messages are sent and shall be received on cell B. Set the cell type of cell B to the "Serving cell". Set the cell type of cell C to the "Non-Suitable cell". (see note)
47	UE			Cell B is preferred by the UE. The UE is powered up or switched on and initiates an attach (see ICS). Step 48 is only performed for non-auto attach UE.

Step	Direction		Message	Comments
	UE	SS		
48	UE		Registration on CS	See TS34.108 Parameter mobile identity is TMSI-1 <u>SS allocates Mobile identity = TMSI-1.</u>
49	UE			UE initiates an attach automatically (see ICS), by MMI or AT commands.
50	->		ATTACH REQUEST	Attach type = 'Combined GPRS/IMSI attach' Mobile identity = P-TMSI-1 Old Routing area identity = RAI-6 TMSI status = valid TMSI available or IE not present
51	<-		ATTACH ACCEPT	Attach result = 'Combined GPRS/IMSI attached' Allocated P-TMSI = P-TMSI-2 P-TMSI Signature = P-TMSI-2 signature MS identity = TMSI-2 Routing area identity = RAI-7
52	->		ATTACH COMPLETE	
53	<-		PAGING TYPE1	Mobile identity = TMSI-2 Paging order is for CS services.
54	->		RRC CONNECTION REQUEST	
55	<-		RRC CONNECTION SETUP	
56	->		RRC CONNECTION SETUP COMPLETE	
57	->		PAGING RESPONSE	Mobile identity = TMSI-2
58	<-		RRC CONNECTION RELEASE	After sending of this message, the SS waits for disconnection of the CS signalling link.
59	->		RRC CONNECTION RELEASE COMPLETE	
60	<-		PAGING TYPE1	Mobile identity = P-TMSI-2 Paging order is for PS services.
61	->		RRC CONNECTION REQUEST	
62	<-		RRC CONNECTION SETUP	
63	->		RRC CONNECTION SETUP COMPLETE	
64	->		SERVICE REQUEST	service type = "paging response"
65	<-		RRC CONNECTION RELEASE	
66	->		RRC CONNECTION RELEASE COMPLETE	
67	UE			The UE is switched off or power is removed (see ICS).
68	->		DETACH REQUEST	Message not sent if power is removed. Detach type = 'power switched off, combined GPRS / IMSI detach'
69	SS			The SS releases the RRC connection. If no RRC CONNECTION RELEASE COMPLETE message have been received within 1 second then the SS shall consider the UE as switched off.
NOTE: The definitions for "Non-Suitable cell" and "Serving cell" are specified in TS34.108 clause 6.1 "Reference Radio Conditions for signalling test cases only".				

Specific message contents

None.

12.3.2.7.5 Test requirements

At step4, when the UE is powered up or switched on, UE shall:

- initiate the PS attach procedure with the information elements specified in the above Expected Sequence.

At step8, when the UE receive the DETACH REQUEST message (Detach type = 're-attach not required', Cause = 'Roaming not allowed in this location area') from SS, UE shall:

- send the DETACH ACCEPT message.

UE shall perform the following action depending on UE location.

1) UE is in the same location area.

At step9 and 18, UE shall:

- not perform location updating procedure.

At step11 and 20, when the UE receives the paging message for CS domain, UE shall:

- not respond to the paging message for PS domain.

At step13 and 22, when the UE receives the paging message for PS domain, UE shall:

- not respond to the paging message for PS domain.

At step17, UE shall;

- not perform PS attach procedure.

2) UE is in the new location area.

At step27, UE shall;

- perform the combined PS attach procedure.

At step34, when the UE receives the paging message for CS domain with Mobile identity = IMSI, UE shall;

- respond to the paging message for CS domain by sending the PAGING RESPONSE message.

At step41, when the UE receives the paging message for PS domain with Mobile identity = P-TMSI-1, UE shall:

- respond to the paging message for PS domain by sending the SERVICE REQUEST message.

At step50, when the UE is powered up or switched on, UE shall:

- initiate the PS attach procedure with the information elements specified in the above Expected Sequence

At step57, when the UE receives the paging message for CS domain with Mobile identity = IMSI, UE shall;

- respond to the paging message for CS domain by sending the PAGING RESPONSE message.

At step64, when the UE receives the paging message for PS domain with Mobile identity = P-TMSI-1, UE shall:

- respond to the paging message for PS domain by sending the SERVICE REQUEST message.

12.4.1.2 Routing area updating / rejected / IMSI invalid / illegal ME

12.4.1.2.1 Definition

12.4.1.2.2 Conformance requirement

- 1) If the network rejects a routing area updating procedure from the User Equipment with the cause 'Illegal ME', the User Equipment shall consider USIM invalid for PS services until power is switched off or USIM is removed.
- 2) If the network rejects a routing area updating procedure from the User Equipment with the cause 'Illegal ME', the User Equipment shall delete the stored RAI, PS-CKSN, P-TMSI and P-TMSI signature.

Reference

3GPP TS 24.008 clause 4.7.5.1.

12.4.1.2.3 Test purpose

To test the behaviour of the UE if the network rejects the routing area updating procedure of the UE with the cause 'Illegal ME'.

12.4.1.2.4 Method of test

Initial condition

System Simulator:

Three cells (not simultaneously activated), cell A in MCC1/MNC1/LAC1/RAC1 (RAI-1), cell B in MCC1/MNC1/LAC1/RAC2 (RAI-4), cell C in MCC2/MNC1/LAC1/RAC1 (RAI-2).

All three cells are operating in network operation mode II (in case of UE operation mode A)

The SIB1 IE "CN domain specific NAS system information", for the CS Domain, is set to value "00 00" (T3212 value is set to 0 and ATT flag is set to FALSE) in all cells.

NB: i) Cell C will be mapped to Cell 4 as found in TS 34.108 clause 6.1.4.1.

User Equipment:

The UE has a valid P-TMSI-1 and RAI-1.

Related ICS/IXIT statements

Support of PS service	Yes/No
UE operation mode C	Yes/No
UE operation mode A	Yes/No (only if mode C not supported)
USIM removal possible without powering down	Yes/No
Switch off on button	Yes/No
Automatic PS attach procedure at switch on or power on	Yes/No

Test procedure

The SS rejects a routing area updating with the cause value 'Illegal ME'. The SS checks that the UE does not perform PS attach in the same or another PLMN.

Expected Sequence

Step	Direction		Message	Comments
	UE	SS		
1	UE			<p>The following messages are sent and shall be received on cell A. The UE is set in UE operation mode C (see ICS). The SS is set in network operation mode II. Set the cell type of cell A to the "Serving cell". Set the cell type of cell B to the "Non-Suitable cell". Set the cell type of cell C to the "Non-Suitable cell". (see note) The UE is powered up or switched on and initiates an attach (see ICS). Cell A is preferred by the UE.</p> <p>Attach type = 'GPRS attach' Mobile identity = P-TMSI-1 Old Routing area identity = RAI-1</p> <p>AUTHENTICATION AND CIPHERING REQUEST AUTHENTICATION AND CIPHERING RESPONSE</p> <p>The SS starts integrity protection. No new mobile identity assigned.P-TMSI and P-TMSI signature not included. Attach result = 'GPRS only attached' Routing area identity = RAI-1</p>
2	SS			
3	UE			
3a			Void	
4	->		ATTACH REQUEST	
4a	<-		AUTHENTICATION AND CIPHERING REQUEST	
4b	->		AUTHENTICATION AND CIPHERING RESPONSE	
4c	SS			
5	<-		ATTACH ACCEPT	
6	SS			
7	UE			
8	->		ROUTING AREA UPDATE REQUEST	
9	<-		ROUTING AREA UPDATE REJECT	
10	<-		PAGING TYPE1	
11	UE			
12	SS			<p>The following messages are sent and shall be received on cell C. Set the cell type of cell B to the "Non-Suitable cell". Set the cell type of cell A to the "Non-Suitable cell". Set the cell type of cell C to the "Serving cell". (see note) Cell C is preferred by the UE. No ATTACH REQUEST sent to the SS (SS waits 30 seconds). If possible (see ICS) USIM removal is performed. Otherwise if possible (see ICS) switch off is performed. Otherwise the power is removed. The UE gets the USIM replaced, is powered up or switched on and initiates an attach (see ICS). Step 16b is only performed by UE in operation mode A</p>
13	UE			
14	UE			
15	UE			
16	UE			
16a				

16b	UE	Registration on CS	See TS 34.108 Parameter mobile identity is IMSI. <u>SS allocates Mobile identity = TMSI-1.</u>
17	->	ATTACH REQUEST	Attach type = 'GPRS attach' Mobile identity = IMSI
17a	<-	AUTHENTICATION AND CIPHERING REQUEST	
17b	->	AUTHENTICATION AND CIPHERING RESPONSE	
17c	SS		The SS starts integrity protection.
18	<-	ATTACH ACCEPT	Attach result = 'GPRS only attached' Allocated P-TMSI = P-TMSI-1 P-TMSI Signature = P-TMSI-1 signature Routing area identity = RAI-2
19	->	ATTACH COMPLETE	
NOTE: The definitions for "Non-Suitable cell", "Suitable neighbour cell" and "Serving cell" are specified in TS34.108 clause 6.1 "Reference Radio Conditions for signalling test cases only".			

Specific message contents

None.

12.4.1.2.5 Test requirements

At step4, when the UE is powered up or switched on, UE shall:

- initiate the PS attach procedure with the information elements specified in the above Expected Sequence.

At step8, UE shall;

- initiate the routing area updating procedure with the information elements specified in the above Expected Sequence.

At step11, after the routing area updating procedure is rejected with GMM cause = 'Illegal ME', UE shall;

- not respond to the paging message for PS domain.

At step14, UE shall,

- not initiate PS attach procedure.

At step17, after the UE is powered up or USIM is replaced, UE shall;

- initiate the PS attach procedure.

12.6.1.2 Authentication rejected by the network

12.6.1.2.1 Definition

12.6.1.2.2 Conformance requirement

Upon receipt of an AUTHENTICATION AND CIPHERING REJECT message, the UE shall set the PS update status to GU3 ROAMING NOT ALLOWED and shall delete the P-TMSI, P-TMSI signature, RAI and PS ciphering key sequence number stored.

The USIM shall be considered as invalid until switching off or the USIM is removed.

If the AUTHENTICATION AND CIPHERING REJECT message is received, the UE shall abort any GMM procedure, shall stop the timers T3310 and T3330 (if running) and shall enter state GMM-DEREGISTERED.

Reference

3GPP TS 24.008 clauses 4.7.7.5.

12.6.1.2.3 Test purpose

To test the behaviour of the UE if the network rejects the authentication and ciphering procedure.

12.6.1.2.4 Method of test

Initial condition

System Simulator:

Two cells (not simultaneously activated), cell A in MCC1/MNC1/LAC1/RAC1 (RAI-1), cell B in MCC1/MNC1/LAC1/RAC2 (RAI-4).
Both cells are operating in network operation mode II.
The SIB1 IE "CN domain specific NAS system information", for the CS Domain, is set to value "00 00" (T3212 value is set to 0 and ATT flag is set to FALSE) in both cells.

User Equipment:

The UE has a valid IMSI.

Related ICS/IXIT statements

Support of PS service	Yes/No	
UE operation mode A	Yes/No	
UE operation mode C	Yes/No	
Switch off on button	Yes/No	
Automatic PS attach procedure at switch on or power on		Yes/No

Test procedure

The test sequence is repeated for $K = 1, 2$.

A complete PS attach procedure is performed. The SS rejects the following authentication and ciphering procedure. The UE is paged with its IMSI and shall not respond.
The Cell is changed into a new Routing Area.

The SS checks that the UE does not perform normal routing area updating.

The SS then checks that the UE does not perform a PS detach.

The SS checks that the UE does not perform a PS Attach procedure.

Expected Sequence

The test sequence is repeated for $k = 1, 2$

For $k=1$, the UE is set in UE operation mode C. If MS operation mode C not supported then $k = 2$.

For $k = 2$ the UE is set in UE operation mode A.

Step	Direction		Message	Comments
	UE	SS		
1		SS		The following messages are sent and shall be received on cell A. Set the cell type of cell A to the "Serving cell". Set the cell type of cell B to the "Non-Suitable cell". (see note)
2	UE			The UE is powered up or switched on and initiates an attach (see ICS).
2a			Void	
2b		SS		SS checks that the IE "Establishment cause" in the received RRC CONNECTION REQUEST message is set to "Registration".
3		->	ATTACH REQUEST	Attach type = 'GPRS attach' Mobile identity = IMSI
4			Void	
5			Void	
6		<-	AUTHENTICATION AND CIPHERING REQUEST	Request authentication. Set PS-CKSN-1
7		->	AUTHENTICATION AND CIPHERING RESPONSE	RES
8		<-	AUTHENTICATION AND CIPHERING REJECT	
8a		SS		The SS releases the RRC connection and waits 5s to allow the UE to read system information. Mobile identity = IMSI
9		<-	PAGING TYPE1	Paging order is for PS services.
10	UE			No response from the UE to the request. This is checked for 10 seconds.
11		SS		The following messages are sent and shall be received on cell B. Set the cell type of cell A to the "Non-Suitable cell". Set the cell type of cell B to the "Serving cell". (see note)
12	UE			Cell B is preferred by the MS.
13	UE			No ROUTING AREA UPDATE REQUEST sent to the SS (SS waits 30 seconds).
14	UE			The UE initiates an attach by MMI or by AT command.
15	UE			No ATTACH REQUEST sent to the SS (SS waits 30 seconds).
16	UE			The UE is switched off (see ICS).
17	SS			No DETACH REQUEST sent to the SS (SS waits 30 seconds).
18				The UE is powered up or switched on and initiates an attach (see ICS). Step 19 is only performed for k =2
19	UE		Registration on CS	Parameter mobile identity is IMSI See TS 34.108 SS checks Mobile identity = IMSI. SS allocates Mobile identity = TMSI-1.
19a		SS		SS checks that the IE "Establishment cause" in the received RRC CONNECTION REQUEST message is set to "Registration".
20		->	ATTACH REQUEST	Attach type = 'GPRS attach' Mobile identity = IMSI
20a		<-	AUTHENTICATION AND CIPHERING REQUEST	
20b		->	AUTHENTICATION AND CIPHERING RESPONSE	
20c		SS		The SS starts integrity protection.

21	<-	ATTACH ACCEPT	Attach result = 'GPRS only attached' Allocated P-TMSI = P-TMSI-1 P-TMSI Signature = P-TMSI-1 signature Routing area identity = RAI-4
22	->	ATTACH COMPLETE	
22a	SS		The SS releases the RRC connection.
23	UE		The UE is switched off or power is removed. (see ICS)
23a	SS		SS checks that the IE "Establishment cause" in any received RRC CONNECTION REQUEST message is set to "Detach".
24	->	DETACH REQUEST	Message not sent if power is removed.
24a	SS		If the power was not removed, the SS releases the RRC connection. If no RRC CONNECTION RELEASE COMPLETE message have been received within 1 second then the SS shall consider the UE as switched off .
25	UE		If k=1 then the test is repeated for k=2.
NOTE: The definitions for "Non-Suitable cell" and "Serving cell" are specified in TS34.108 clause 6.1 "Reference Radio Conditions for signalling test cases only".			

Specific message contents

None.

12.6.1.2.5 Test requirements

At step3, when the UE is powered on or switched on, UE shall:

- initiate the PS attach procedure with information elements specified in the above Expected Sequence.

At step9, when the UE receives the AUTHENTICATION AND CIPHERING REJECT message, UE shall:

- not respond paging message for PS domain.

At step13, when the RF level of the attached cell is lower than the RF level of the new cell, UE shall:

- not perform normal routing area updating.

At step17, when the UE is switched off, UE shall:

- not perform PS detach procedure.

3GPP TSG-R5 WG1 Meeting #27
 Bath, England, 25th Apr – 29th Apr 2005

Tdoc **R5-050876**

CR-Form-v7

CHANGE REQUEST

34.123-1 CR 1248 rev - Current version: **5.11.1**

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the symbols.

Proposed change affects: | UICC apps ME Radio Access Network Core Network


Title:	CR to 34.123-1 Rel-5: Correction to WI-012 RLC test case 7.2.3.28		
Source:	3GPP TSG RAN WG5 (Testing)		
Work item code:	TEI	Date:	18/05/2005
Category:	F	Release:	Rel-5
	Use <u>one</u> of the following categories: F (correction) A (corresponds to a correction in an earlier release) B (addition of feature), C (functional modification of feature) D (editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900 .		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6)

Reason for change:	<ul style="list-style-type: none"> In the RLC test case 7.2.3.28 the SS is checking that the UE does not retransmit any PDUs. The current prose use a downlink SDU size fitting into one PDU, which is expanded in uplink such that the UE transmit the necessary number of PDUs in uplink. In TTCN the downlink and uplink SDU size have been chosen to be equal to enable the use of the RLC standard mechanism for checking PDU content to verify that no retransmission is done by the UE. As the TTCN implementation still achieves the test purpose it is proposed that the prose is changed to align with TTCN.
Summary of change:	<ul style="list-style-type: none"> The DL and UL SDU sizes have been chosen to be the same: (2 * Poll_PDU * AM_7_PayloadSize) – 1.
Consequences if not approved:	Misalignment between TTCN and prose

Clauses affected:	7.2.3.28.4										
Other specs affected:	<table border="1" style="display: inline-table; border-collapse: collapse;"> <tr> <td style="text-align: center;">Y</td> <td style="text-align: center;">N</td> </tr> <tr> <td style="text-align: center;"><input type="checkbox"/></td> <td style="text-align: center;"><input checked="" type="checkbox"/></td> </tr> <tr> <td style="text-align: center;"><input type="checkbox"/></td> <td style="text-align: center;"><input checked="" type="checkbox"/></td> </tr> <tr> <td style="text-align: center;"><input type="checkbox"/></td> <td style="text-align: center;"><input checked="" type="checkbox"/></td> </tr> </table>	Y	N	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	Other core specifications Test specifications O&M Specifications	
Y	N										
<input type="checkbox"/>	<input checked="" type="checkbox"/>										
<input type="checkbox"/>	<input checked="" type="checkbox"/>										
<input type="checkbox"/>	<input checked="" type="checkbox"/>										
Other comments:	Affects R99, Rel-4 and Rel-5; This CR has been approved on the RAN5 e-mail reflector as R5-050501r1.doc										

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at <http://www.3gpp.org/specs/CR.htm>. Below is a brief summary:

- 1) Fill out the above form. The symbols above marked  contain pop-up help information about the field that they are closest to .
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <ftp://ftp.3gpp.org/specs/> For the latest version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.
- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

<START OF MODIFIED SECTION>

7.2.3.28 Status reporting / Abnormal conditions / Reception of LIST SUFI with Length set to zero

7.2.3.28.1 Definition

Peer RLCs use STATUS PDUs to manage flow control and retransmission. On a STATUS report PDU with an invalid LIST SUFI the RLC must behave as specified. Incorrect behaviour may result in degradation of QoS, or failure of the UE to communicate.

7.2.3.28.2 Conformance requirement

The List super-field

The List Super-Field consists of a type identifier field (LIST), a list length field (LENGTH) and a list of LENGTH number of pairs as shown in figure 9.11 below:

Type = LIST
LENGTH
SN ₁
L ₁
SN ₂
L ₂
...
SN _{LENGTH}
L _{LENGTH}

Figure 9.11: The List fields in a STATUS PDU

LENGTH

Length: 4 bits

The number of (SN_{*i*}, L_{*i*})-pairs in the super-field of type LIST. The value "0000" is invalid and the list is discarded.

Reference

TS 25.322 clause 9.2.2.11.4.

7.2.3.28.3 Test purpose

To verify that if a STATUS PDU is received with a LIST SUFI and the LENGTH field is set to "0000" that the list is discarded.

7.2.3.28.4 Method of test

Initial conditions

The generic procedure for Radio Bearer establishment (clause 7.1.3 of TS 34.108) is executed, with all the parameters as specified in the procedure, with the exception that the default Radio Access Bearer is replaced with the RAB defined for AM 7-bit "Length Indicator" tests in clause 7.2.3.1.

The following RLC parameter values are used in place of the values in clause 7.2.3.1:

Uplink RLC Polling info Poll_PDU	4
--	---

These settings apply to both the uplink and downlink DTCH.

The Radio Bearer is placed in UE test loop mode 1 with the UL SDU size set to $(2 * \text{Poll_PDU} * \text{AM_7_PayloadSize}) - 1$ bytes.

Test procedure

- a) The SS sends an SDU of size $(2 * \text{Poll_PDU} * \text{AM_7_PayloadSize}) - 1$. [See note 1.](#)
- b) The SS monitors the received (looped back) PDUs for a poll request.
- c) The SS responds to the poll request by transmitting a STATUS PDU with a LIST SUFI. The list contains an indication that two PDUs were not received, but has the length field set to "0000".
- d) The SS continues to monitor the received PDUs to verify that none are retransmitted.
- e) The SS may optionally release the radio bearer.

NOTE 1. The DL SDU size has been chosen to simplify TTCN implementation. If the number of PDUs in downlink and uplink is the same then the RLC standard mechanism for checking PDU content can be used to verify that no retransmission is done by the UE.

Expected sequence

Step	Direction		Message	Comments
	UE	SS		
1	←		DOWNLINK RLC PDU	SDU 1 (start)
2	←		...	SS continues to transmit RLC PDUs
3	←		DOWNLINK RLC PDU	SDU 1 (end)
4	→		UPLINK RLC PDU	SDU 1 (start)
5	→		UPLINK RLC PDU	
6	→		...	SS continues to receive RLC PDUs
7	→		UPLINK RLC PDU	SN = Poll_PDU - 1, Poll
8	←		STATUS PDU	LIST(LENGTH = "0000", SN = 1, SN = 2)
9	→		...	SS continues to receive RLC PDUs
10	→		UPLINK RLC PDU	Poll
11	←		STATUS PDU	Normal reply
12	→		...	SS continues to receive RLC PDUs
13	→		UPLINK RLC PDU	SDU 1 (end)
14			RB RELEASE	Optional step

NOTE 1: The Expected Sequence shown is infomative.
 The UPLINK and DOWNLINK PDU flows may overlap, but are shown separate for clarity.
 Information such as SDU, PDU or Sequence numbers given in the comments column shall be considered informative only, for test case development purposes.

7.2.3.28.5 Test requirements

No RLC PDUs shall be retransmitted by the UE.

<END OF MODIFIED SECTION>

3GPP TSG-R5 Meeting #27
 Bath, UK, 25th – 29th April 2005

Tdoc **R5-050878**
 Agenda 8.8.7

CR-Form-v7

CHANGE REQUEST

34.123-1 CR 1249 rev - Current version: 5.11.1

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the symbols.

Proposed change affects: | UICC apps ME Radio Access Network Core Network

Title:	Corrections to WI-10 P4 approved GMM test case 12.2.1.5a Test Procedures 1 & 2		
Source:	3GPP TSG RAN WG5 (Testing)		
Work item code:	TEI	Date:	16/05/2005
Category:	F	Release:	Rel-5
	Use <u>one</u> of the following categories: F (correction) A (corresponds to a correction in an earlier release) B (addition of feature), C (functional modification of feature) D (editorial modification) Detailed explanations of the above categories can be found in 3GPP TR 21.900 .		Use <u>one</u> of the following releases: 2 (GSM Phase 2) R96 (Release 1996) R97 (Release 1997) R98 (Release 1998) R99 (Release 1999) Rel-4 (Release 4) Rel-5 (Release 5) Rel-6 (Release 6)

Reason for change: As per TS 24.008 section 4.7.3.1.4:

13 (Roaming not allowed in this location area).

The MS shall delete any RAI, P-TMSI, P-TMSI signature and GPRS ciphering key sequence number, shall set the GPRS update status to GU3 ROAMING NOT ALLOWED (and shall store it according to clause 4.1.3.2) and shall reset the attach attempt counter. The state is changed to GMM-DEREGISTERED.LIMITED-SERVICE or optionally to GMM-DEREGISTERED.PLMN-SEARCH

The MS shall store the LAI in the list of "forbidden location areas for roaming".

If no RR connection exists, the MS shall perform the following additional actions immediately. If the MS is operating in MS operation mode A and an RR connection exists, the MS shall perform these actions when the RR connection is subsequently released:

- If the MS is IMSI attached, the MS shall set the update status to U3 ROAMING NOT ALLOWED, shall delete any TMSI, LAI and ciphering key sequence number and shall reset the location update attempt counter. The new MM state is MM IDLE.
- The MS shall perform a PLMN selection according to 3GPP TS 23.122.

In this test case when Attach Request is rejected with cause 'Roaming not allowed in this location area' no RR connection (physical circuit switched domain connection – see definition in TS 24.008 section 2.2.2) exists between the UE and network.

Changes in R5-05988r1

Following Attach Reject, SS is waiting for 30 seconds to verify UE doesn't reinitiate Attach Request before releasing RRC connection. TS 24.008 is

	ambiguous about UE behaviour when PS Signalling connection or RRC Connection is not released by the network after Attach Reject with cause #13. UE may perform PLMN selection after Attach reject and hence won't respond to RRC Connection Release Request received from the network. Test sequence expects RRC Connection Release Request Complete message, thus incorrectly failing a conformant UE.									
Summary of change:	⌘	Revised order of test steps 6 and 6a (Test Procedure 1) and test steps 5 and 5a (Test Procedure 2) to avoid UE locally releasing RRC connection following initiation of PLMN search. Editorial corrections.								
Consequences if not approved:	⌘	Test case will incorrectly fail a conformant mobile								
Clauses affected:	⌘	12.2.1.5a.4.1 and 12.2.1.5a.4.2								
Other specs affected:	⌘	<table border="1"> <thead> <tr> <th>Y</th> <th>N</th> </tr> </thead> <tbody> <tr> <td><input checked="" type="checkbox"/></td> <td><input type="checkbox"/></td> </tr> <tr> <td><input checked="" type="checkbox"/></td> <td><input type="checkbox"/></td> </tr> <tr> <td><input checked="" type="checkbox"/></td> <td><input type="checkbox"/></td> </tr> </tbody> </table> Other core specifications Test specifications O&M Specifications	Y	N	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>
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<input checked="" type="checkbox"/>	<input type="checkbox"/>									
Other comments:	⌘	This CR affects R99 & later releases. TTCN change required. Revision of R5-050973 & R5-050988; The CR has been approved on the RAN5 e-mail reflector as R5-050988r1.doc								

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- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

12.2.1.5a PS attach / rejected / roaming not allowed in this location area

12.2.1.5a.1 Definition

12.2.1.5a.2 Conformance requirement

- 1) If the network rejects a PS attach procedure from the User Equipment with the cause 'roaming not allowed in this location area' the User Equipment shall:
 - 1.1 not perform PS attach when in the same location area.
 - 1.2 delete the stored RAI, PS-CKSN, P-TMSI and P-TMSI signature.
 - 1.3 store the LA in the 'forbidden location areas for roaming' list.
 - 1.4 perform PS attach when a new location area is entered.
 - 1.5 Periodically search for its HPLMN.
- 2) The User Equipment shall reset the list of 'Forbidden location areas for roaming' when switched off or when the USIM is removed.
- 3) The UE shall be capable of storing at least 10 entries in the list of 'Forbidden location areas for roaming'.

Reference

3GPP TS 24.008 clause 4.7.3.1.

12.2.1.5a.3 Test purpose

Test purpose 1

To test that on receipt of a rejection using the 'roaming not allowed in this location area' cause code, the UE ceases trying to attach on that location area. Successful PS attach procedure is possible in other location areas.

Test purpose 2

To test that if the UE is switched off or the USIM is removed the list of 'forbidden location areas for roaming' is cleared.

Test purpose 3

To test that at least 6 entries can be held in the list of 'forbidden location areas for roaming' (the requirement in 3GPP TS 24.008 is to store at least 10 entries. This is not fully tested by the third procedure).

Test purpose 4

To test that if a cell of the Home PLMN is available then the UE returns to it in preference to any other available cell.

12.2.1.5a.4 Method of test

12.2.1.5a.4.1 Test procedure 1

Initial condition

System Simulator:

Three cells (not simultaneously activated), cell A in MCC2/MNC1/LAC1/RAC1 (RAI-2, Not HPLMN), cell B in

MCC2/MNC1/LAC2/RAC1 (RAI-6, Not HPLMN) and cell C in MCC2/MNC1/LAC1/RAC2 (RAI-7, Not HPLMN).

All three cells are operating in network operation mode II.

The SIB1 IE "CN domain specific NAS system information", for the CS Domain, is set to value "00 00" (T3212 value is set to 0 and ATT flag is set to FALSE) in all cells.

User Equipment:

The UE has a valid P-TMSI-1 and RAI-2.

Related ICS/IXIT statements

Support of PS service	Yes/No	
UE operation mode C	Yes/No	
UE operation mode A	Yes/No	
Switch off on button	Yes/No	
Automatic PS attach procedure at switch on or power on		Yes/No

Test procedure

The SS rejects a PS attach with the cause value 'Roaming not allowed in this location area'. A new attempt for a PS attach is not possible. Successful PS attach / detach procedures are performed in another location area. A new attempt for a PS attach is performed in the 1st location area. This attempt shall not succeed, as the LA is on the forbidden list.

Expected Sequence

Step	Direction		Message	Comments
	UE	SS		
1		SS		The following messages are sent and shall be received on cell A.
2	UE			The UE is set in UE operation mode C (see ICS). If UE operation mode C not supported, goto step 2249.
3		SS		Set the cell type of cell A to the "Serving cell". Set the cell type of cell B to the "Non-Suitable cell". Set the cell type of cell C to the "Non-Suitable cell". (see note)
3a	UE			The UE is powered up or switched on and initiates an attach (see ICS). Cell A is preferred by the UE.
3b		SS	Void	SS checks that the IE "Establishment cause" in the received RRC CONNECTION REQUEST message is set to "Registration".
4		->	ATTACH REQUEST	Attach type = 'GPRS attach' Mobile identity = P-TMSI-1 Old Routing area identity = RAI-2
5		<-	ATTACH REJECT	GMM cause = 'Roaming not allowed in this location area'
6		UE SS		The SS releases the RRC connection No ATTACH REQUEST sent to SS (SS waits 30 seconds).
6a		UE SS		The SS releases the RRC connection. No ATTACH REQUEST sent to SS (SS waits 30 seconds)
7		SS		The following messages are sent and shall be received on cell B. Set the cell type of cell A to the "Non-Suitable cell". Set the cell type of cell B to the "Serving cell". (see note)
8	UE			Cell B is preferred by the UE.
9	UE		Registration on CS	See TS 34.108 This is applied only for UE in UE operation mode A. Parameter mobile identity is IMSI.
10	UE			The UE initiates an attach automatically, by MMI or by AT command.
10a		SS		SS checks that the IE "Establishment cause" in the received RRC CONNECTION REQUEST message is set to "Registration".
11		->	ATTACH REQUEST	Attach type = 'GPRS attach' Mobile identity = IMSI
11a		<-	AUTHENTICATION AND CIPHERING REQUEST	
11b		->	AUTHENTICATION AND CIPHERING RESPONSE	
11c		SS		The SS starts integrity protection.
12		<-	ATTACH ACCEPT	Attach result = 'GPRS only attached' Allocated P-TMSI = P-TMSI-1 P-TMSI Signature = P-TMSI-1 signature Routing area identity = RAI-6
13		->	ATTACH COMPLETE	
13a		SS		The SS releases the RRC connection.

14	UE		The UE initiates a PS detach (without power off) by MMI or by AT command . SS checks that the IE "Establishment cause" in any received RRC CONNECTION REQUEST message is set to "Detach". Detach type = 'normal detach, GPRS detach'
14a	SS		
15	->	DETACH REQUEST	Detach type = 'normal detach, GPRS detach'
16	<-	DETACH ACCEPT	
16a	SS		The SS releases the RRC connection.
17	SS		The following messages are sent and shall be received on cell C. Set the cell type of cell B to the "Non-Suitable cell". Set the cell type of cell C to the "Serving cell". (see note) Cell C is preferred by the UE. No ATTACH REQUEST sent to SS (SS waits 30 seconds). The UE is switched off or power is removed (see ICS)
18	UE		
19	UE		
20	UE		UE is switched off. Set the cell type of cell C to the "Non-Suitable cell". (see note)
21	SS		
22	UE		The UE is set in UE operation mode A if supported (see ICS) and the test is repeated from step 2 to step 20.
NOTE: The definitions for "Non-Suitable cell" and "Serving cell" are specified in TS34.108 clause 6.1 "Reference Radio Conditions for signalling test cases only".			

12.2.1.5a.4.2 Test procedure 2

Initial condition

System Simulator:

One cell in MCC2/MNC1/LAC1/RAC1 (RAI-2, Not HPLMN) operating in network operation mode II.

The SIB1 IE "CN domain specific NAS system information", for the CS Domain, is set to value "00 00" (T3212 value is set to 0 and ATT flag is set to FALSE).

User Equipment:

The UE has a valid P-TMSI-1 and RAI-2.

Related ICS/IXIT statements

Support of PS service	Yes/No
UE operation mode C	Yes/No
UE operation mode A	Yes/No (only if mode C not supported)
Switch off on button	Yes/No
Automatic PS attach procedure at switch on or power on	Yes/No

Test procedure

The SS rejects a PS attach updating with the cause value 'Roaming not allowed in this location area'. The UE is switched off for 10 s and switched on again. The SS check that a PS attach is possible on the cell on which the PS attach had been rejected.

If USIM removal is possible without switching off: The SS rejects a PS attach with the cause value 'Roaming not allowed in this location area'. The USIM is removed and inserted in the UE. The SS check that a PS attach is possible on the cell on which the PS attach had been rejected.

Expected Sequence

Step	Direction		Message	Comments
	UE	SS		
1	UE			If UE operation mode C is supported, the UE is set in UE operation mode C (see ICS). If UE operation mode C is not supported, the UE is set in UE operation mode A.
2	UE			The UE is powered up or switched on and initiates an attach (see ICS).
2a			Void	
2b	SS			SS checks that the IE "Establishment cause" in the received RRC CONNECTION REQUEST message is set to "Registration".
3	->		ATTACH REQUEST	Attach type = 'GPRS attach' Mobile identity = P-TMSI-1 Old Routing area identity = RAI-2
4	<-		ATTACH REJECT	GMM cause = 'Roaming not allowed in this location area'
5	SS UE			The SS releases the RRC connection No ATTACH REQUEST sent to the SS (SS waits 30 seconds).
5a	UE SS			No ATTACH REQUEST sent to the SS (SS waits 30 seconds). The SS releases the RRC connection.
6	UE			If possible (see ICS) switch off is performed. Otherwise the power is removed.
7	UE			The UE is powered up or switched on and initiates an attach (see ICS).
8			<u>Void</u>	
8a	SS			SS checks that the IE "Establishment cause" in the received RRC CONNECTION REQUEST message is set to "Registration".
8b	UE		Registration on CS	See TS 34.108 This is applied only for UE in UE operation mode A.
9	->		ATTACH REQUEST	Parameter mobile identity is IMSI. Attach type = 'GPRS attach' Mobile identity = IMSI
9a	<-		AUTHENTICATION AND CIPHERING REQUEST	
9b	->		AUTHENTICATION AND CIPHERING RESPONSE	
9c	SS			The SS starts integrity protection.
10	<-		ATTACH ACCEPT	Attach result = 'GPRS only attached' Allocated P-TMSI = P-TMSI-1 P-TMSI Signature = P-TMSI-1 signature Routing area identity = RAI-2
11	->		ATTACH COMPLETE	
11a	SS			The SS releases the RRC connection.