3GPP TSG RAN Meeting #19 Birmingham, United Kingdom, 11 - 14 March 2003

Title: CRs (Rel-5) to TS 25.212

Source: TSG-RAN WG1

Agenda item: 8.1.5

TS 25.212 (RP-030134)

Doc-1st-	Doc-2nd-	Spec	CR	Rev	Subject F		Ca	Versio	Versio	Workitem
RP-030134	R1-030294	25.212	165	1	Correction of CQI index to bit mapping	Rel-5	F	5.3.0	5.4.0	HSDPA-Phys
RP-030134	R1-030340	25.212	166	3	Correction of bit scrambling of HS-DSCH	Rel-5	F	5.3.0	5.4.0	HSDPA-Phys
RP-030134	R1-030240	25.212	168	-	Correction of subscript for modulation scheme	Rel-5	F	5.3.0	5.4.0	HSDPA-Phys

CHANGE REQUEST										
*	25.212 CR	165	жrev	1	æ	Current version:	5.3.0	¥		
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Proposed change affects: UICC apps# ME X Radio Access Network X Core Network

Title: ★ Correction of CQI index to bit mapping Source: **™ TSG RAN WG1** Work item code: 第 HSDPA-Phys Date: 第 18 Feb, 2003 ₩ F Category: Release: # Rel-5 Use one of the following categories: Use one of the following releases: F (correction) (GSM Phase 2) 2 A (corresponds to a correction in an earlier release) R96 (Release 1996) (Release 1997) B (addition of feature), R97 **C** (functional modification of feature) R98 (Release 1998) **D** (editorial modification) R99 (Release 1999) Detailed explanations of the above categories can Rel-4 (Release 4) be found in 3GPP TR 21.900. Rel-5 (Release 5) Rel-6 (Release 6)

Reason for change: # The mapping between the 31 decimal CQI values and the 32 binary CQI codewords is modified according to RAN2 LS to enable flow control efficiently in future release(R1-030029).

Summary of change: ₩

Previous version of the spec is following

Binary CQI code word	CQI value	Explanation
$(a_0, a_1, a_2, a_3, a_4)$		
(0 0 0 0 0)	0	Out of range
(1 0 0 0 0)	1	Transport Block Size=137
~	~	~
(0 1 1 1 1)	30	Transport Block Size=25558 in
		category10
(1 1 1 1 1)	Undefined	Not used

This CR proposes following modification

This ert proposes renewing meanication								
Binary CQI code word	CQI value	Explanation						
$(a_0, a_1, a_2, a_3, a_4)$								
(0 0 0 0 0)	Undefined	Not used in release 5. This patten is						
		used for flow control in future release.						
(1 0 0 0 0)	0	Out of range						
(0 1 0 0 0)	1	Transport Block Size=137						
~	~	~						
(1 1 1 1 1)	30	Transport Block Size=25558 in						
		category10						

The difference from Rev 0 is "Information bits pattern" is updated to "information bit pattern".

Consequences if not approved:

** The error behaviour when flow control is introduced in future release is not so good if CQI bit mapping is common between release 5 and release 6.

Clauses affected:	% 4.7.1.2							
Other specs affected:	Y N X Other core specifications X Test specifications O&M Specifications							
Other comments:	x							

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at http://www.3gpp.org/specs/CR.htm. Below is a brief summary:

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- 3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request.

4.7.1.2 Channel coding for HS-DPCCH channel quality information

The channel quality information is coded using a (20,5) code. The code words of the (20,5) code are a linear combination of the 5 basis sequences denoted $M_{i,n}$ defined in the table below.

Table 14: Basis sequences for (20,5) code

i	M _{i,0}	M _{i,1}	$M_{i,2}$	M _{i,3}	$M_{i,4}$
0	1	0	0	0	1
1	0	1	0	0	1
3	1	1	0	0	1
	0	0	1	0	1
4	1	0	1	0	1
5	0	1	1	0	1
6	1	1	1	0	1
7	0	0	0	1	1
8	1	0	0	1	1
9	0	1	0	1	1
10	1	1	0	1	1
11	0	0	1	1	1
12	1	0	1	1	1
13	0	1	1	1	1
14	1	1	1	1	1
15	0	0	0	0	1
16	0	0	0	0	1
17	0	0	0	0	1
18	0	0	0	0	1
19	0	0	0	0	1

The CQI values 0 .. 30 as defined in [4] are converted from decimal to binary to map them to the channel quality information bits ($\frac{10}{9}$ 0 0 0 0) to ($\frac{10}{9}$ 1 1 1 1) respectively. The information bit pattern (0 0 0 0 0) shall not be used in this release. The channel quality information bits are a_0 , a_1 , a_2 , a_3 , a_4 (where a_0 is LSB and a_4 is MSB). The output code word bits b_i are given by:

$$b_i = \sum_{n=0}^4 (\boldsymbol{a}_n \times \boldsymbol{M}_{i,n}) \bmod 2$$

where i = 0, ..., 19.

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Consequences if

not approved:

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ME X Radio Access Network X Core Network UICC apps# Proposed change affects: Title: Correction of bit scrambling of HS-DSCH Source: ₩ TSG RAN WG1 Date: 第 20 Jan, 2003 ж **F** Category: Release: # Rel-5 Use one of the following releases: Use one of the following categories: F (correction) 2 (GSM Phase 2) **A** (corresponds to a correction in an earlier release) R96 (Release 1996) **B** (addition of feature), R97 (Release 1997) **C** (functional modification of feature) R98 (Release 1998) **D** (editorial modification) R99 (Release 1999) Detailed explanations of the above categories can Rel-4 (Release 4) Rel-5 be found in 3GPP TR 21.900. (Release 5) Rel-6 (Release 6)

- The variable y'_m of bit scrambling for HS-DSCH is not defined at m = B although Reason for change: # m takes value B. <Addition at Rev 1> - The subindex 'm' for variable in section 4.5.1a should be avoided because of subindex 'm' is used for transport block number in whole TS25.212. - In section 4.5.1, although transport block subindex is shown as 'm' in TS25.212, there is misleading description that 'i' looks transport block subindex. - It was clarified that i=1 and m=1 in HS-DSCH section. <Addition at Rev 2> - Bit order of vector g was clarified. <Addition at Rev 3> - Temporal variable 'i' was substituated to 'x' because 'i' isused for TrCH number. 'x' is temporary variable in TS25.212. Summary of change: ₩ The range of the difinition of y'_m is extended from m < B to $m \le B$. - The subindex 'm' for variable is substituted to ' γ '. ' γ ' was chosen as the character which is not yet used in TS25.212. - Misleading description is removed in section 4.5.1 - It was clarified that i=1 and m=1 in HS-DSCH section.

Some index related text may bring confusion.

The variable y'_m of bit scrambling for HS-DSCH remains undefined at m = B.

Clauses affected: Other specs affected:	# 4.5, 4.5.1, 4.5.1a Y N X Other core specifications X Test specifications O&M Specifications
Other comments:	lpha

How to create CRs using this form:

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4.5 Coding for HS-DSCH

Data arrives to the coding unit in form of a maximum of one transport block once every transmission time interval. The transmission time interval is 2 ms which is mapped to a radio sub-frame of 3 slots.

The following coding steps can be identified:

- add CRC to each transport block (see subclause 4.5.1);
- bit scrambling (see subclause 4.5.1a);
- code block segmentation (see subclause 4.5.2);
- channel coding (see subclause 4.5.3);
- hybrid ARQ (see subclause 4.5.4);
- physical channel segmentation (see subclause 4.5.5);
- interleaving for HS-DSCH (see subclause 4.5.6);
- constellation re-arrangement for 16 QAM (see subclause 4.5.7);
- mapping to physical channels (see subclause 4.5.8).

The coding steps for HS-DSCH are shown in the figure below.

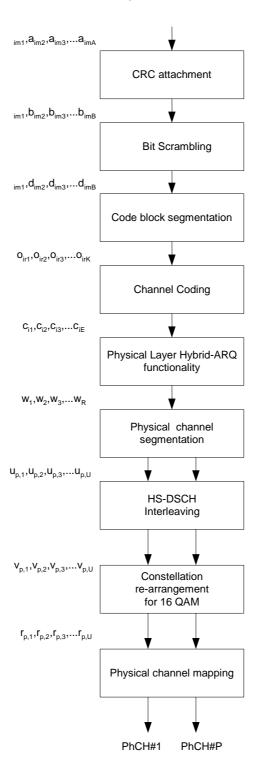


Figure 16: Coding chain for HS-DSCH

In the following the number of transport blocks and the number of transport channels is always one <u>i.e. m=1, i=1</u>. When referencing non HS-DSCH formulae which are used in correspondence with HS-DSCH formulae the convention is used that transport block subscripts may be omitted (e.g. X_1 may be written X).

4.5.1 CRC attachment for HS-DSCH

CRC attachment for the HS-DSCH transport channel shall be done using the general method described in 4.2.1 above with the following specific parameters.

There will be a maximum of one transport block, i=1. The CRC length shall always be $L_1 = 24$ bits.

Bit scrambling for HS-DSCH 4.5.1a

The bits output from the HS-DSCH CRC attachment are scrambled in the bit scrambler. The bits input to the bit scrambler are denoted by $b_{im,1}, b_{im,2}, b_{im,3}, ..., b_{im,B}$, where B is the number of bits input to the HS-DSCH bit scrambler The bits after bit scrambling are denoted $d_{\mathit{im},1}, d_{\mathit{im},2}, d_{\mathit{im},3}, ..., d_{\mathit{im},B}$.

Bit scrambling is defined by the following relation:

$$d_{im,k} = (b_{im,k} + y_k) \mod 2$$
 $k = 1,2,...,B$

and y_k results from the following operation:

$$y'_{m} = 0$$
 $y'_{\gamma} = 0$ $-15 < m_{\gamma} < 1$

$$\frac{y'_{m}=1}{y_{m}} = 1 \qquad y'_{\gamma} = 1 \underline{m}_{\gamma} = 1$$

$$\begin{aligned} & \underline{y'_{m}} = 0 \quad \underline{y'_{\gamma}} = 0 \quad -15 < \underline{m_{\gamma}} < 1 \\ & \underline{y'_{m}} = 1 \quad \underline{y'_{\gamma}} = 1 \quad \underline{m_{\gamma}} = 1 \\ & \underline{y'_{m}} = \left(\sum_{i=1}^{16} g_{i} \cdot \underline{y'_{m-i}}\right) \bmod 2 \quad \underline{y'_{\gamma}} = \left(\sum_{x=1}^{16} g_{x} \cdot \underline{y'_{\gamma-x}}\right) \bmod 2 \quad \underline{1 < m < B} \quad \underline{1} < \gamma \le B, \\ & \text{where } \quad \underline{g} = \left\{0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 1, 0, 1, 1, 0, 1\right\} \quad \underline{g} = \left\{g_{1}, g_{2}, \dots, g_{16}\right\} = \left\{0, 0, 0, 0, 0, 0, 0, 0, 0, 1, 0, 1, 1, 0, 1\right\}, \end{aligned}$$

$$y_{k} = y'_{k}$$
 $k = 1, 2, ..., B$.

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Proposed change a		IICC appsЖ <mark></mark>			ess Network	X Core Ne	etwork			
Title: ₩	Correction	of subscript for n	nodulation sch	eme infor	mation					
Source: #	TSG RAN	WG1								
Work item code: ₩	HSDPA-P	hys			Date: 第	06/02/2003				
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Reason for change:	: Ж <mark>The s</mark>	subscript for the m	nodulation sch	eme infor	mation is inco	orrect.				
Summary of change	e: 郑 <mark>The s</mark>	ubscript is correc	ted.							
Consequences if not approved:	₩ A nor	n-existent term is	used in the sp	ecification	1.					
Clauses affected:										
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4.6.3 Multiplexing of HS-SCCH information

The channelization-code-set information $x_{ccs,1}$, $x_{ccs,2}$, ..., $x_{ccs,7}$ and modulation-scheme information $x_{m\underline{s},1}$ are multiplexed together. This gives a sequence of bits $x_{1,1}$, $x_{1,2}$, ..., $x_{1,8}$ where

$$x_{1,i} = x_{ccs,i}$$
 $i=1,2,...,7$

$$x_{1,i} = x_{ms,i-7} \qquad i=8$$

The transport-block-size information $x_{tbs,1}$, $x_{tbs,2}$, ..., $x_{tbs,6}$, Hybrid-ARQ-process information $x_{hap,1}$, $x_{hap,2}$, $x_{hap,3}$, redundancy-version information $x_{rv,1}$, $x_{rv,2}$, $x_{rv,3}$ and new-data indicator $x_{nd,1}$ are multiplexed together. This gives a sequence of bits $x_{2,1}$, $x_{2,2}$, ..., $x_{2,1,3}$ where

$$x_{2,i} = x_{tbs,i}$$
 $i=1,2,...,6$

$$x_{2,i} = x_{hap,i-6}$$
 $i=7,8,9$

$$x_{2,i} = x_{rv,i-9}$$
 $i=10,11,12$

$$x_{2,i} = x_{nd,i-12}$$
 $i=13$