Technical Specification Group Radio Access Network Marco Island, USA 4 - 7 June 2002

RP#16(02) 0425

TSG_Doc_Num	Specification	CR_Num	Revision_Num	3G_Release	CR_Subject	CR_Category	Cur_Ver_Num	New_Ver_Num	Tdoc_Num	WorkItem
RP-020425	25.878	001	1		Correction of reference to a RAN4 specification	F	5.0.0	5.1.0		RANimp- RLTA

3GPP TSG-RAN3 Meeting #29 Gyeongju, Korea, 13th – 18th May, 2002

ж	25.8	<mark>78</mark> CR <mark>001</mark>	ж	ev 1	# Current vers	ion: 5.0.0 [#]				
For <u>HELP</u> on u	sing this	s form, see bottom	of this pag	e or look a	at the pop-up text	over the # symbols.				
Proposed change a	affects:	₩ (U)SIM	ME/UE	Radio	o Access Networl	k X Core Network				
Title: #	Corre	ction of reference t	o a RAN4 s	specificatio	on					
Source: ¥	RR-W	/G3								
Work item code: 郑	RANir	mp-RLTA			<i>Date:</i> ສ	May, 2002				
Category: ₩	F A B C D Detailed	e of the following cate (correction) (corresponds to a co (addition of feature), (functional modification (editorial modification d explanations of the d in 3GPP <u>TR 21.900</u>	rrection in a ion of feature n) above categ	e)	2	REL-5 the following releases: (GSM Phase 2) (Release 1996) (Release 1997) (Release 1998) (Release 1999) (Release 4) (Release 5)				
Reason for change	e: #	order to cope wi quoted in the tec an out-of-date p solution RAN3 a	th an updat chnical repo art of 25.13 dopted reg	te occurre ort. Althou 33 has no i arding to t	d in TS 25.133, w gh the assumptio impact and no co the WI 'RL Timing	clarify TR 25.878 in which appears to be in made by referencing nsequence on the g Adjustment' it was est version of 25.133.				
Summary of chang	ie:	was added. R1: u	pdate to th ot to 5.1.0	e cover sh	neet's reason for	reference to 25.133 change to indicate that 25.133(the affected				
Consequences if not approved:	Ħ					sent change does not ed in RAN3/RAN2 TSs.				
Clauses affected:	Ħ	2, 6.3								
Other specs affected:	ж	Other core sp Test specifica O&M Specific	tions	5 ¥						
Other comments:	ж									

How to create CRs using this form:

Comprehensive information and tips about how to create CRs can be found at: <u>http://www.3gpp.org/3G_Specs/CRs.htm</u>. Below is a brief summary:

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3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not

2 References

The following documents contain provisions which, through reference in this text, constitute provisions of the present document.

- References are either specific (identified by date of publication, edition number, version number, etc.) or non-specific.
- For a specific reference, subsequent revisions do not apply.
- For a non-specific reference, the latest version applies.
- A non-specific reference to an ETS shall also be taken to refer to later versions published as an EN with the same number.

[1.] Work Item Description: "Radio Link Timing Adjustment" RP-010261, submitted at TSG RAN#11.

[2.] 3GPP TS 25.133: "Requirements for support of Radio Resource management (FDD)".

6.3 Description of timing mechanisms

Fig. 1 shows the timing relationship between DL DPCH and UL DPCH at UE side when UE moves from Cell 1 area to Cell 2 through the handover region. As it is shown in Fig. 1, it can be assumed that the time difference between DL DPCH and UL DPCH is T0 (1024chips). When the UE moves from Cell 1 area to the handover region, the time when DL DPCH arrives at UE is delayed as shown in Fig. 1. UE may adjust the UL DPCH transmission time to keep the T0 time offset but the requirement described in [2]TS25.133 should be fulfilled. The requirement in TS25.133 is as follows:

----- TS 25.133 v3.5.0/v4.0.0

2002.2.^ .~ UE Transmit Timing

2002.2.1.~ Requirements

[partially omitted]

The UE shall be capable of changing the transmission timing according the received downlink DPCCH/DPDCH frame. The maximum amount of the timing change in one adjustment shall be ¼ Chip.

The minimum adjustment rate shall be 233ns per second. The maximum adjustment rate shall be $\frac{1}{4}$ chip per 200ms. In particular, within any given 200 ms period, the UE transmit timing shall not change in excess of $\frac{1}{4}$ chip from the timing at the beginning of this 200ms period.