TSG-RAN Meeting No. 16 June 4-7, 2002, Marco Island, Florida, USA

TSGRP-020374

Status Report for SI to TSG

Study Item Name: Improvement of Inter-frequency and inter-system measurement for 1.28 Mcps TDD

SOURCE: Rapporteur

TSG: RAN WG: WG1

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Ref. to SI sheet: RAN_Study_Items_after_RAN_15.doc

Progress Report since the last TSG (for all involved WGs):

RAN1: RAN1 is the leading working group of this SI. The purpose of this SI is firstly to evaluate and quantify some possible problems from current inter-frequency and inter-system measurement for 1.28 Mcps TDD and then propose some set of solutions in order to resolve them, particulary in terms of enlarging the measurement window and avoiding the possible measurement failures. Evaluation and verification of gains brought by each solution are also needed. In RAN1 #26 meeting, revised TR 25.888 [1] containing the overall structure and scope was approved. Other related two contributions [6][7] including the text proposal for this study item, which describes the use of asymmetric time slot allocation pattern and pattern combination scheme, were not presented yet due to the lack of time. Hence, they will be treated in the next RAN1 #27 meeting.

RAN2: There is no progress since the last TSG RAN meeting. It is required that improved signalling support can be treated after the necessity of specific algorithm is completed in the RAN1.

RAN3: For feasibility study in Iur/Iub areas, related contribution was submitted as [10] at RAN3#29. But it was not treated due to the lack of time.

RAN4: There is no need of further progress.

List of Completed elements (for complex work items):

RAN1: Overall structure of TR, Scope

RAN2: Clarification of the SI.

RAN3: No

RAN4: Agreement on the impact to the WG4 related specifications

List of open issues:

- Use of asymmetric time slot allocation pattern, evaluation of concrete gains and its impact on DCA, power control, beam-forming and uplink synchronization function.
- Use of the combination of different time slot allocation pattern, evaluation of concrete gains and its impact on DCA, power control, beam-forming and uplink synchronization function.
- Improved signalling support for the specific measurement

Estimates of the level of completion (when possible):

30 %

SI completion date review resulting from the discussion at the working group:

TSG RAN #17 (September 2002)

References to WG's internal documentation and/or TRs:

- [1] 3GPP TSGR1-02-0516: "Revised draft TR 25.888 on Improvement of Inter-frequency and inter-system measurement for 1.28Mcps TDD (revision of R1-02-0468)"
- [2] 3GPP TSGR1-02-0663: "Improvement of monitoring GSM from 1.28Mcps TDD"
- [3] 3GPP TSGR1-02-0664: "Improvement of monitoring FDD from 1.28Mcps TDD"
- [4] 3GPP TSGR1-02-0665: "Improvement of monitoring 3.84Mcps TDD from 1.28Mcps TDD"
- [5] 3GPP TSGR1-02-0666: "Improvement of monitoring 1.28Mcps TDD from 1.28Mcps TDD"
- [6] 3GPP TSGR1-02-0780: "Inter-RAT/Frequency study overview : asymmetric pattern for time slot allocation"
- [7] 3GPP TSGR1-02-0781: "Inter-RAT/Frequency study overview : Combination of different time slot allocation pattern"
- [8] 3GPP TSGR3-020256, "Skeleton TR3-001 v001", Samsung Electronics, April. 2002
- [9] 3GPP TSGR3-020981, "Signalling support for improvement of inter-RAT measurements", Samsung Electronics, April. 2002
- [10] 3GPP TSGR3-021595, "Skeleton TR3-001 v001", Samsung Electronics, May. 2002