

**3GPP TSG-RAN Meeting #16**  
**Marco Island, FL, U.S.A., 4 – 7, June, 2002**

**RP-020307**

**Title:** Agreed CRs (R99 and Rel-4/Rel-5 Category A) to TS 25.211

**Source:** TSG-RAN WG1

**Agenda item:** 7.1.3

| No. | Spec   | CR  | Rev | R1 T-doc   | Subject                             | Phase | Cat | Work Item | V_old  | V_new  |
|-----|--------|-----|-----|------------|-------------------------------------|-------|-----|-----------|--------|--------|
| 1   | 25.211 | 143 | 1   | R1-02-0539 | SCCPCH structure with STTD encoding | R99   | F   | TEI       | 3.10.0 | 3.11.0 |
| 2   | 25.211 | 144 | 1   | R1-02-0539 | SCCPCH structure with STTD encoding | Rel-4 | A   | TEI       | 4.4.0  | 4.5.0  |
| 3   | 25.211 | 149 | 1   | R1-02-0539 | SCCPCH structure with STTD encoding | Rel-5 | A   | TEI       | 5.0.0  | 5.1.0  |
| 4   | 25.211 | 151 | -   | R1-02-0596 | Downlink bit mapping                | R99   | F   | TEI       | 3.10.0 | 3.11.0 |
| 5   | 25.211 | 152 | -   | R1-02-0596 | Downlink bit mapping                | Rel-4 | A   | TEI       | 4.4.0  | 4.5.0  |
| 6   | 25.211 | 153 | -   | R1-02-0596 | Downlink bit mapping                | Rel-5 | A   | TEI       | 5.0.0  | 5.1.0  |

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**R1-02-0539**

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| CR-Form-v5                                 |  |
| <h2 style="margin: 0;">CHANGE REQUEST</h2> |  |
| z  | <div style="background-color: yellow; padding: 2px; display: inline-block;"><b>25.211 CR 143</b></div>             |
| z  | <div style="background-color: yellow; padding: 2px; display: inline-block;">rev <b>1</b></div>                     |
| z  | Current version: <div style="background-color: yellow; padding: 2px; display: inline-block;"><b>3.10.0</b></div> z |

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**Proposed change affects:** z (U)SIM  ME/UE  Radio Access Network  Core Network

|                        |   |   |
|------------------------|---|---|
| <b>Title:</b>          | z | SCCPCH structure with STTD encoding   |
| <b>Source:</b>         | z | TSG RAN WG1   |
| <b>Work item code:</b> | z | TEI   |
|                        |   | <b>Date:</b> z <span style="background-color: yellow;">2.05.2002</span>   |
| <b>Category:</b>       | z | <b>F</b>  |
|                        |   | Use <u>one</u> of the following categories:<br><b>F</b> (correction)<br><b>A</b> (corresponds to a correction in an earlier release)<br><b>B</b> (addition of feature),<br><b>C</b> (functional modification of feature)<br><b>D</b> (editorial modification)<br>Detailed explanations of the above categories can be found in 3GPP <a href="#">TR 21.900</a> . |
|                        |   | <b>Release:</b> z <span style="background-color: yellow;">R99</span><br>Use <u>one</u> of the following releases:<br>2 (GSM Phase 2)<br>R96 (Release 1996)<br>R97 (Release 1997)<br>R98 (Release 1998)<br>R99 (Release 1999)<br>REL-4 (Release 4)<br>REL-5 (Release 5)  |

|                                      |   |  |
|--------------------------------------|---|--|
| <b>Reason for change:</b>            | z | The usage of word, "data" is not consistent between sections 5.3.3.4 and 5.3.3.4.1. In 5.3.3.4, 'data' corresponds to field where FACH and PCH is transmitted. In 5.3.3.4.1, 'data symbols' refers to whole structure of SCCPCH. It is proposed to have consistent usage of data aligned with the usage of section 5.3.3.4. Hence, when doing STTD encoding for SCCPCH also TFCI bits has to be included in encoding in addition to data bits. |
| <b>Summary of change:</b>            | z | TFCI is included in STTD encoding with SCCPCH  |
| <b>Consequences if not approved:</b> | z | STTD may not be used with SCCPCH   |

|  |   |   |  |   |  |  |  |  |   |  |  |
|--|---|---|--|---|--|--|--|--|---|--|--|
| <b>Clauses affected:</b>                           | z | 5.3.3.4.1   |  |   |  |  |  |  |   |  |  |
| <b>Other specs affected:</b>                       | z | <table style="width: 100%; border: none;"> <tr> <td style="width: 50%;"><input type="checkbox"/> Other core specifications</td> <td style="width: 5%;">z</td> <td style="background-color: yellow;"></td> </tr> <tr> <td><input type="checkbox"/> Test specifications</td> <td></td> <td></td> </tr> <tr> <td><input type="checkbox"/> O&amp;M Specifications</td> <td></td> <td></td> </tr> </table> | <input type="checkbox"/> Other core specifications | z |  | <input type="checkbox"/> Test specifications |  |  | <input type="checkbox"/> O&M Specifications |  |  |
| <input type="checkbox"/> Other core specifications | z |   |  |   |  |  |  |  |   |  |  |
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| <b>Other comments:</b>                             | z | This CR is considered to have isolated impact: Would not affect existing UE or UTRAN implementations behaving like indicated in the CR, would affect implementations supporting the corrected functionality otherwise.  |  |   |  |  |  |  |   |  |  |

**How to create CRs using this form:**

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**Table 19: Pilot Symbol Pattern**

| Symbol # | N <sub>pilot</sub> = 8 |    |    |    | N <sub>pilot</sub> = 16 |    |    |    |    |    |    |    |
|----------|------------------------|----|----|----|-------------------------|----|----|----|----|----|----|----|
|          | 0                      | 1  | 2  | 3  | 0                       | 1  | 2  | 3  | 4  | 5  | 6  | 7  |
| Slot #0  | 11                     | 11 | 11 | 10 | 11                      | 11 | 11 | 10 | 11 | 11 | 11 | 10 |
| 1        | 11                     | 00 | 11 | 10 | 11                      | 00 | 11 | 10 | 11 | 11 | 11 | 00 |
| 2        | 11                     | 01 | 11 | 01 | 11                      | 01 | 11 | 01 | 11 | 10 | 11 | 00 |
| 3        | 11                     | 00 | 11 | 00 | 11                      | 00 | 11 | 00 | 11 | 01 | 11 | 10 |
| 4        | 11                     | 10 | 11 | 01 | 11                      | 10 | 11 | 01 | 11 | 11 | 11 | 11 |
| 5        | 11                     | 11 | 11 | 10 | 11                      | 11 | 11 | 10 | 11 | 01 | 11 | 01 |
| 6        | 11                     | 11 | 11 | 00 | 11                      | 11 | 11 | 00 | 11 | 10 | 11 | 11 |
| 7        | 11                     | 10 | 11 | 00 | 11                      | 10 | 11 | 00 | 11 | 10 | 11 | 00 |
| 8        | 11                     | 01 | 11 | 10 | 11                      | 01 | 11 | 10 | 11 | 00 | 11 | 11 |
| 9        | 11                     | 11 | 11 | 11 | 11                      | 11 | 11 | 11 | 11 | 00 | 11 | 11 |
| 10       | 11                     | 01 | 11 | 01 | 11                      | 01 | 11 | 01 | 11 | 11 | 11 | 10 |
| 11       | 11                     | 10 | 11 | 11 | 11                      | 10 | 11 | 11 | 11 | 00 | 11 | 10 |
| 12       | 11                     | 10 | 11 | 00 | 11                      | 10 | 11 | 00 | 11 | 01 | 11 | 01 |
| 13       | 11                     | 00 | 11 | 11 | 11                      | 00 | 11 | 11 | 11 | 00 | 11 | 00 |
| 14       | 11                     | 00 | 11 | 11 | 11                      | 00 | 11 | 11 | 11 | 10 | 11 | 01 |

For slot formats using TFCI, the TFCI value in each radio frame corresponds to a certain transport format combination of the FACHs and/or PCHs currently in use. This correspondence is (re-)negotiated at each FACH/PCH addition/removal. The mapping of the TFCI bits onto slots is described in [3].

#### 5.3.3.4.1 Secondary CCPCH structure with STTD encoding

In case the diversity antenna is present in UTRAN and the S-CCPCH is to be transmitted using open loop transmit diversity, the data **and TFCI bits symbols** of the S-CCPCH are STTD encoded as given in subclause 5.3.1.1.1. The pilot symbol pattern for antenna 2 for the S-CCPCH given in table 20 is not supported in this release.

**Table 20: Pilot symbol pattern for antenna 2 when STTD encoding is used on the S-CCPCH**

| Symbol # | N <sub>pilot</sub> = 8 |    |    |    | N <sub>pilot</sub> = 16 |    |    |    |    |    |    |    |
|----------|------------------------|----|----|----|-------------------------|----|----|----|----|----|----|----|
|          | 0                      | 1  | 2  | 3  | 0                       | 1  | 2  | 3  | 4  | 5  | 6  | 7  |
| Slot #0  | 11                     | 00 | 00 | 10 | 11                      | 00 | 00 | 10 | 11 | 00 | 00 | 10 |
| 1        | 11                     | 00 | 00 | 01 | 11                      | 00 | 00 | 01 | 11 | 10 | 00 | 10 |
| 2        | 11                     | 11 | 00 | 00 | 11                      | 11 | 00 | 00 | 11 | 10 | 00 | 11 |
| 3        | 11                     | 10 | 00 | 01 | 11                      | 10 | 00 | 01 | 11 | 00 | 00 | 00 |
| 4        | 11                     | 11 | 00 | 11 | 11                      | 11 | 00 | 11 | 11 | 01 | 00 | 10 |
| 5        | 11                     | 00 | 00 | 10 | 11                      | 00 | 00 | 10 | 11 | 11 | 00 | 00 |
| 6        | 11                     | 10 | 00 | 10 | 11                      | 10 | 00 | 10 | 11 | 01 | 00 | 11 |
| 7        | 11                     | 10 | 00 | 11 | 11                      | 10 | 00 | 11 | 11 | 10 | 00 | 11 |
| 8        | 11                     | 00 | 00 | 00 | 11                      | 00 | 00 | 00 | 11 | 01 | 00 | 01 |
| 9        | 11                     | 01 | 00 | 10 | 11                      | 01 | 00 | 10 | 11 | 01 | 00 | 01 |
| 10       | 11                     | 11 | 00 | 00 | 11                      | 11 | 00 | 00 | 11 | 00 | 00 | 10 |
| 11       | 11                     | 01 | 00 | 11 | 11                      | 01 | 00 | 11 | 11 | 00 | 00 | 01 |
| 12       | 11                     | 10 | 00 | 11 | 11                      | 10 | 00 | 11 | 11 | 11 | 00 | 00 |
| 13       | 11                     | 01 | 00 | 01 | 11                      | 01 | 00 | 01 | 11 | 10 | 00 | 01 |
| 14       | 11                     | 01 | 00 | 01 | 11                      | 01 | 00 | 01 | 11 | 11 | 00 | 11 |

#### 5.3.3.5 Synchronisation Channel (SCH)

The Synchronisation Channel (SCH) is a downlink signal used for cell search. The SCH consists of two sub channels, the Primary and Secondary SCH. The 10 ms radio frames of the Primary and Secondary SCH are divided into 15 slots, each of length 2560 chips. Figure 18 illustrates the structure of the SCH radio frame.

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|  |   |
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| <h2 style="margin: 0;">CHANGE REQUEST</h2> |   |
| z  | <div style="background-color: yellow; padding: 2px; display: inline-block;"><b>25.211 CR 144</b></div>            |
| z  | <div style="background-color: yellow; padding: 2px; display: inline-block;">rev <b>1</b></div>                    |
| z  | Current version: <div style="background-color: yellow; padding: 2px; display: inline-block;"><b>4.4.0</b></div> z |

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|                        |   |   |
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| <b>Title:</b>          | z | SCCPCH structure with STTD encoding   |
| <b>Source:</b>         | z | TSG RAN WG1   |
| <b>Work item code:</b> | z | TEI   |
|                        |   | <b>Date:</b> z <span style="background-color: yellow;">2.05.2002</span>   |
| <b>Category:</b>       | z | <b>A</b>  |
|                        |   | Use <u>one</u> of the following categories:<br><b>F</b> (correction)<br><b>A</b> (corresponds to a correction in an earlier release)<br><b>B</b> (addition of feature),<br><b>C</b> (functional modification of feature)<br><b>D</b> (editorial modification)<br>Detailed explanations of the above categories can be found in 3GPP <a href="#">TR 21.900</a> . |
|                        |   | <b>Release:</b> z <span style="background-color: yellow;">REL-4</span><br>Use <u>one</u> of the following releases:<br>2 (GSM Phase 2)<br>R96 (Release 1996)<br>R97 (Release 1997)<br>R98 (Release 1998)<br>R99 (Release 1999)<br>REL-4 (Release 4)<br>REL-5 (Release 5)  |

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|  |   |   |  |   |  |  |  |  |   |  |  |
|--|---|---|--|---|--|--|--|--|---|--|--|
| <b>Clauses affected:</b>                           | z | 5.3.3.4.1   |  |   |  |  |  |  |   |  |  |
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| 1        | 11                     | 00 | 11 | 10 | 11                      | 00 | 11 | 10 | 11 | 11 | 11 | 00 |
| 2        | 11                     | 01 | 11 | 01 | 11                      | 01 | 11 | 01 | 11 | 10 | 11 | 00 |
| 3        | 11                     | 00 | 11 | 00 | 11                      | 00 | 11 | 00 | 11 | 01 | 11 | 10 |
| 4        | 11                     | 10 | 11 | 01 | 11                      | 10 | 11 | 01 | 11 | 11 | 11 | 11 |
| 5        | 11                     | 11 | 11 | 10 | 11                      | 11 | 11 | 10 | 11 | 01 | 11 | 01 |
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| 8        | 11                     | 01 | 11 | 10 | 11                      | 01 | 11 | 10 | 11 | 00 | 11 | 11 |
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| 2        | 11                     | 11 | 00 | 00 | 11                      | 11 | 00 | 00 | 11 | 10 | 00 | 11 |
| 3        | 11                     | 10 | 00 | 01 | 11                      | 10 | 00 | 01 | 11 | 00 | 00 | 00 |
| 4        | 11                     | 11 | 00 | 11 | 11                      | 11 | 00 | 11 | 11 | 01 | 00 | 10 |
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| 12       | 11                     | 10 | 00 | 11 | 11                      | 10 | 00 | 11 | 11 | 11 | 00 | 00 |
| 13       | 11                     | 01 | 00 | 01 | 11                      | 01 | 00 | 01 | 11 | 10 | 00 | 01 |
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|  |   |
|--|---|
| CR-Form-v5                                 |   |
| <h2 style="margin: 0;">CHANGE REQUEST</h2> |   |
| z  | <div style="background-color: yellow; padding: 2px; display: inline-block;"><b>25.211 CR 149</b></div>            |
| z  | <div style="background-color: yellow; padding: 2px; display: inline-block;">rev <b>1</b></div>                    |
| z  | Current version: <div style="background-color: yellow; padding: 2px; display: inline-block;"><b>5.0.0</b></div> z |

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|                        |   |   |
|------------------------|---|---|
| <b>Title:</b>          | z | SCCPCH structure with STTD encoding   |
| <b>Source:</b>         | z | TSG RAN WG1   |
| <b>Work item code:</b> | z | TEI   |
|                        |   | <b>Date:</b> z <span style="background-color: yellow;">2.05.2002</span>   |
| <b>Category:</b>       | z | <b>A</b>  |
|                        |   | Use <u>one</u> of the following categories:<br><b>F</b> (correction)<br><b>A</b> (corresponds to a correction in an earlier release)<br><b>B</b> (addition of feature),<br><b>C</b> (functional modification of feature)<br><b>D</b> (editorial modification)<br>Detailed explanations of the above categories can be found in 3GPP <a href="#">TR 21.900</a> . |
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|                                      |   |  |
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|  |   |   |  |   |  |  |  |  |   |  |  |
|--|---|---|--|---|--|--|--|--|---|--|--|
| <b>Clauses affected:</b>                           | z | 5.3.3.4.1   |  |   |  |  |  |  |   |  |  |
| <b>Other specs affected:</b>                       | z | <table style="width: 100%; border: none;"> <tr> <td style="width: 50%;"><input type="checkbox"/> Other core specifications</td> <td style="width: 5%;">z</td> <td style="background-color: yellow;"></td> </tr> <tr> <td><input type="checkbox"/> Test specifications</td> <td></td> <td></td> </tr> <tr> <td><input type="checkbox"/> O&amp;M Specifications</td> <td></td> <td></td> </tr> </table> | <input type="checkbox"/> Other core specifications | z |  | <input type="checkbox"/> Test specifications |  |  | <input type="checkbox"/> O&M Specifications |  |  |
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| <input type="checkbox"/> Test specifications       |   |   |  |   |  |  |  |  |   |  |  |
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**How to create CRs using this form:**

Comprehensive information and tips about how to create CRs can be found at: [http://www.3gpp.org/3G\\_Specs/CRs.htm](http://www.3gpp.org/3G_Specs/CRs.htm). Below is a brief summary:

- 1) Fill out the above form. The symbols above marked z contain pop-up help information about the field that they are closest to.
- 2) Obtain the latest version for the release of the specification to which the change is proposed. Use the MS Word "revision marks" feature (also known as "track changes") when making the changes. All 3GPP specifications can be downloaded from the 3GPP server under <ftp://ftp.3gpp.org/specs/> For the latest



version, look for the directory name with the latest date e.g. 2001-03 contains the specifications resulting from the March 2001 TSG meetings.

3) With "track changes" disabled, paste the entire CR form (use CTRL-A to select it) into the specification just in front of the clause containing the first piece of changed text. Delete those parts of the specification which are not relevant to the change request

**Table 20: Pilot Symbol Pattern**

| Symbol # | N <sub>pilot</sub> = 8 |    |    |    | N <sub>pilot</sub> = 16 |    |    |    |    |    |    |    |
|----------|------------------------|----|----|----|-------------------------|----|----|----|----|----|----|----|
|          | 0                      | 1  | 2  | 3  | 0                       | 1  | 2  | 3  | 4  | 5  | 6  | 7  |
| Slot #0  | 11                     | 11 | 11 | 10 | 11                      | 11 | 11 | 10 | 11 | 11 | 11 | 10 |
| 1        | 11                     | 00 | 11 | 10 | 11                      | 00 | 11 | 10 | 11 | 11 | 11 | 00 |
| 2        | 11                     | 01 | 11 | 01 | 11                      | 01 | 11 | 01 | 11 | 10 | 11 | 00 |
| 3        | 11                     | 00 | 11 | 00 | 11                      | 00 | 11 | 00 | 11 | 01 | 11 | 10 |
| 4        | 11                     | 10 | 11 | 01 | 11                      | 10 | 11 | 01 | 11 | 11 | 11 | 11 |
| 5        | 11                     | 11 | 11 | 10 | 11                      | 11 | 11 | 10 | 11 | 01 | 11 | 01 |
| 6        | 11                     | 11 | 11 | 00 | 11                      | 11 | 11 | 00 | 11 | 10 | 11 | 11 |
| 7        | 11                     | 10 | 11 | 00 | 11                      | 10 | 11 | 00 | 11 | 10 | 11 | 00 |
| 8        | 11                     | 01 | 11 | 10 | 11                      | 01 | 11 | 10 | 11 | 00 | 11 | 11 |
| 9        | 11                     | 11 | 11 | 11 | 11                      | 11 | 11 | 11 | 11 | 00 | 11 | 11 |
| 10       | 11                     | 01 | 11 | 01 | 11                      | 01 | 11 | 01 | 11 | 11 | 11 | 10 |
| 11       | 11                     | 10 | 11 | 11 | 11                      | 10 | 11 | 11 | 11 | 00 | 11 | 10 |
| 12       | 11                     | 10 | 11 | 00 | 11                      | 10 | 11 | 00 | 11 | 01 | 11 | 01 |
| 13       | 11                     | 00 | 11 | 11 | 11                      | 00 | 11 | 11 | 11 | 00 | 11 | 00 |
| 14       | 11                     | 00 | 11 | 11 | 11                      | 00 | 11 | 11 | 11 | 10 | 11 | 01 |

For slot formats using TFCI, the TFCI value in each radio frame corresponds to a certain transport format combination of the FACHs and/or PCHs currently in use. This correspondence is (re-)negotiated at each FACH/PCH addition/removal. The mapping of the TFCI bits onto slots is described in [3].

#### 5.3.3.4.1 Secondary CCPCH structure with STTD encoding

In case the diversity antenna is present in UTRAN and the S-CCPCH is to be transmitted using open loop transmit diversity, the data **and TFCI bits symbols** of the S-CCPCH are STTD encoded as given in subclause 5.3.1.1.1. The pilot symbol pattern for antenna 2 for the S-CCPCH given in Table 21 is not supported in this release.

**Table 21: Pilot symbol pattern for antenna 2 when STTD encoding is used on the S-CCPCH**

| Symbol # | N <sub>pilot</sub> = 8 |    |    |    | N <sub>pilot</sub> = 16 |    |    |    |    |    |    |    |
|----------|------------------------|----|----|----|-------------------------|----|----|----|----|----|----|----|
|          | 0                      | 1  | 2  | 3  | 0                       | 1  | 2  | 3  | 4  | 5  | 6  | 7  |
| Slot #0  | 11                     | 00 | 00 | 10 | 11                      | 00 | 00 | 10 | 11 | 00 | 00 | 10 |
| 1        | 11                     | 00 | 00 | 01 | 11                      | 00 | 00 | 01 | 11 | 10 | 00 | 10 |
| 2        | 11                     | 11 | 00 | 00 | 11                      | 11 | 00 | 00 | 11 | 10 | 00 | 11 |
| 3        | 11                     | 10 | 00 | 01 | 11                      | 10 | 00 | 01 | 11 | 00 | 00 | 00 |
| 4        | 11                     | 11 | 00 | 11 | 11                      | 11 | 00 | 11 | 11 | 01 | 00 | 10 |
| 5        | 11                     | 00 | 00 | 10 | 11                      | 00 | 00 | 10 | 11 | 11 | 00 | 00 |
| 6        | 11                     | 10 | 00 | 10 | 11                      | 10 | 00 | 10 | 11 | 01 | 00 | 11 |
| 7        | 11                     | 10 | 00 | 11 | 11                      | 10 | 00 | 11 | 11 | 10 | 00 | 11 |
| 8        | 11                     | 00 | 00 | 00 | 11                      | 00 | 00 | 00 | 11 | 01 | 00 | 01 |
| 9        | 11                     | 01 | 00 | 10 | 11                      | 01 | 00 | 10 | 11 | 01 | 00 | 01 |
| 10       | 11                     | 11 | 00 | 00 | 11                      | 11 | 00 | 00 | 11 | 00 | 00 | 10 |
| 11       | 11                     | 01 | 00 | 11 | 11                      | 01 | 00 | 11 | 11 | 00 | 00 | 01 |
| 12       | 11                     | 10 | 00 | 11 | 11                      | 10 | 00 | 11 | 11 | 11 | 00 | 00 |
| 13       | 11                     | 01 | 00 | 01 | 11                      | 01 | 00 | 01 | 11 | 10 | 00 | 01 |
| 14       | 11                     | 01 | 00 | 01 | 11                      | 01 | 00 | 01 | 11 | 11 | 00 | 11 |

#### 5.3.3.5 Synchronisation Channel (SCH)

The Synchronisation Channel (SCH) is a downlink signal used for cell search. The SCH consists of two sub channels, the Primary and Secondary SCH. The 10 ms radio frames of the Primary and Secondary SCH are divided into 15 slots, each of length 2560 chips. Figure 19 illustrates the structure of the SCH radio frame.

## CHANGE REQUEST

z **25.211 CR 151** z rev **-** z Current version: **3.10.0** z

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the z symbols.

**Proposed change affects:** z (U)SIM  ME/UE  Radio Access Network  Core Network

|                        |   |  |   |
|------------------------|---|--|---|
| <b>Title:</b>          | z | Downlink bit mapping   |   |
| <b>Source:</b>         | z | TSG RAN WG1  |   |
| <b>Work item code:</b> | z | TEI  | <b>Date:</b> z 2002-04-05                 |
| <b>Category:</b>       | z | <b>F</b>   | <b>Release:</b> z R99                     |
|                        |   | Use <u>one</u> of the following categories:  | Use <u>one</u> of the following releases: |
|                        |   | <b>F</b> (correction)  | 2 (GSM Phase 2)                           |
|                        |   | <b>A</b> (corresponds to a correction in an earlier release)                                   | R96 (Release 1996)                        |
|                        |   | <b>B</b> (addition of feature),  | R97 (Release 1997)                        |
|                        |   | <b>C</b> (functional modification of feature)  | R98 (Release 1998)                        |
|                        |   | <b>D</b> (editorial modification)  | R99 (Release 1999)                        |
|                        |   | Detailed explanations of the above categories can be found in 3GPP <a href="#">TR 21.900</a> . | REL-4 (Release 4)                         |
|                        |   |  | REL-5 (Release 5)                         |

|                                      |   |  |
|--------------------------------------|---|--|
| <b>Reason for change:</b>            | z | The current description of signal formats for the interfaces between different RAN1 specifications does not consider all types of signals. The signal formats output from 25.211 and 25.212 are formally not compatible with the spreading operation in 25.213. In particular, there is ambiguity about binary, real-valued and DTX signals and their mapping. |
| <b>Summary of change:</b>            | z | For all channels, except AICH-type channels, the output format from 25.211 is specified as 0, 1 and "DTX". STTD description, CPICH, PICH and CSICH definitions are updated accordingly.  |
| <b>Consequences if not approved:</b> | z | Misleading information regarding signal formats on the interfaces between RAN1 specifications.   |

|                              |   |  |
|------------------------------|---|--|
| <b>Clauses affected:</b>     | z | 5.3.1.1.1, 5.3.3.1, 5.3.3.10, 5.3.3.11   |
| <b>Other specs affected:</b> | z | <input type="checkbox"/> Other core specifications      z<br><input type="checkbox"/> Test specifications<br><input type="checkbox"/> O&M Specifications |
| <b>Other comments:</b>       | z | Isolated impact analysis: The implementation of this CR does not impact existing UE or UTRAN implementations.  |

### 5.3.1.1.1 Space time block coding based transmit antenna diversity (STTD)

The open loop downlink transmit diversity employs a space time block coding based transmit diversity (STTD).

The STTD encoding is optional in UTRAN. STTD support is mandatory at the UE.

If higher layers signal that neither P-CPICH nor S-CPICH can be used as phase reference for the downlink DPCH for a radio link in a cell, the UE shall assume that STTD is not used for the downlink DPCH (and the associated PDSCH if applicable) in that cell.

STTD encoding is applied on blocks of 4 consecutive channel bits. A block diagram of a generic STTD encoder for channel bits  $b_0, b_1, b_2, b_3$  is shown in the figure 8 below. Channel coding, rate matching and interleaving is done as in the non-diversity mode. The STTD encoder operates on 4 symbols  $b_0, b_1, b_2, b_3$  as shown in figure 8. For AICH, AP-AICH and CD/CA-ICH, the bit  $b_i$  are real valued signals {0} for DTX bits and {1, -1} for all other channel bits, and  $\bar{b}_i$  is defined as  $-b_i$ . For channels other than AICH, AP-AICH and CD/CA-ICH, the  $b_i$  are 3-valued digits, taking the values 0, 1, "DTX", and  $\bar{b}_i$  is defined as follows: if  $b_i = 0$  then  $\bar{b}_i = 1$ , if  $b_i = 1$  then  $\bar{b}_i = 0$ , otherwise  $\bar{b}_i = b_i$ .

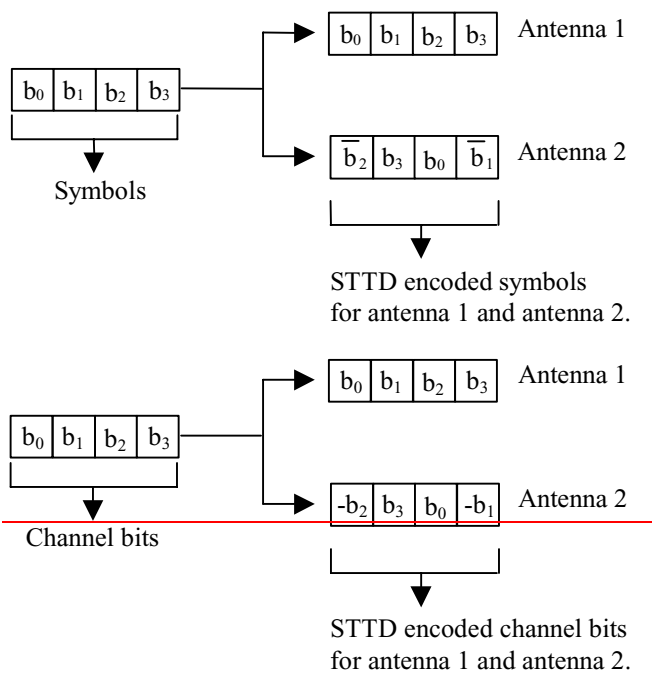
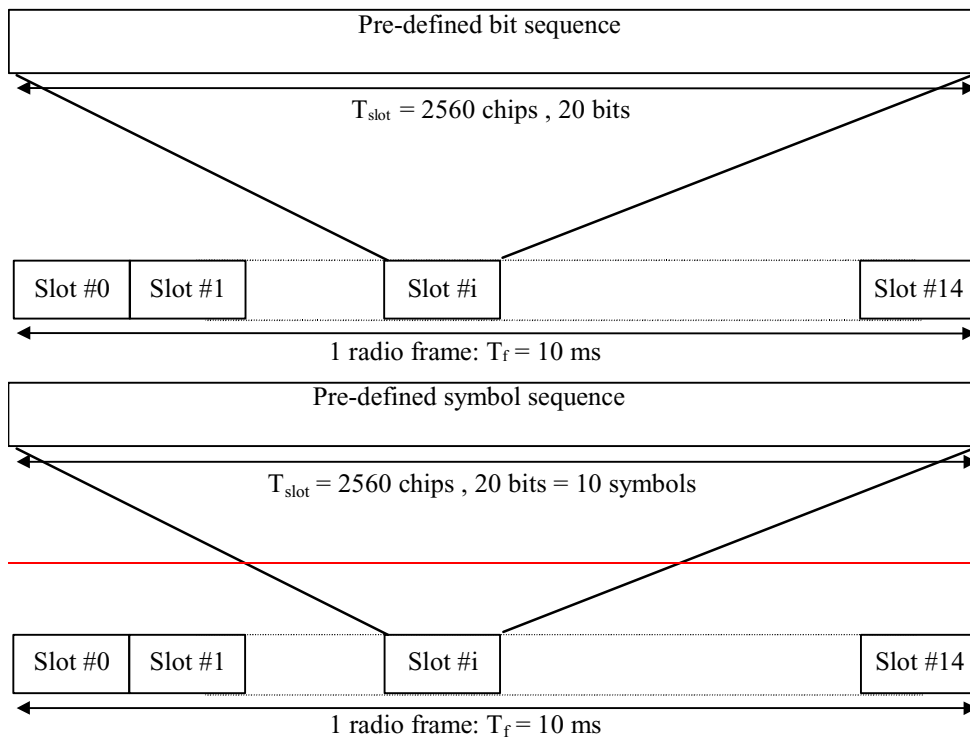


Figure 8: Generic block diagram of the STTD encoder

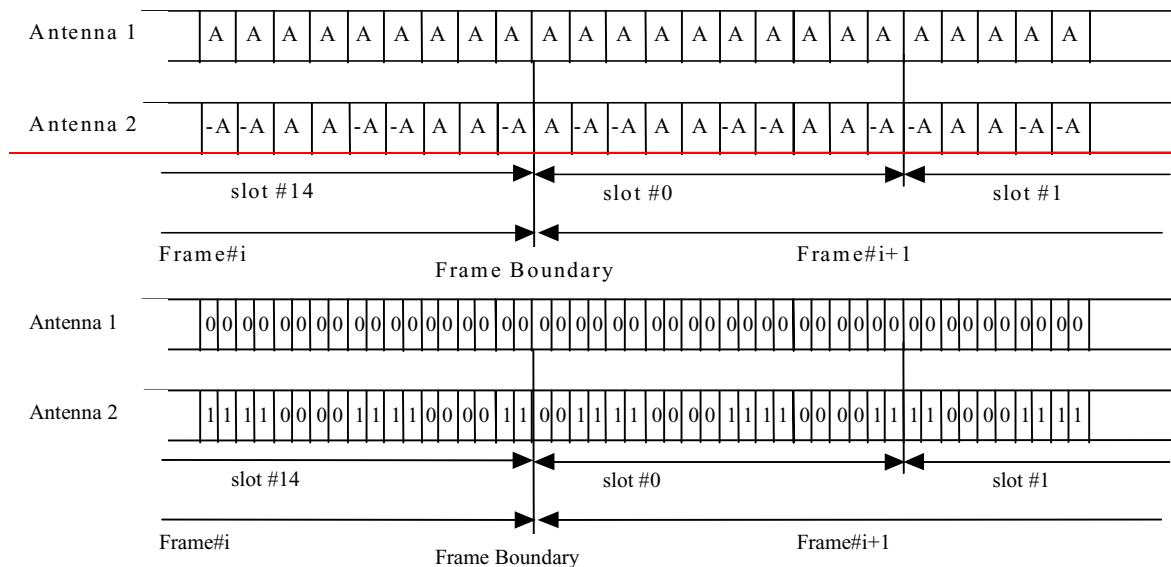
### 5.3.3.1 Common Pilot Channel (CPICH)

The CPICH is a fixed rate (30 kbps, SF=256) downlink physical channel that carries a pre-defined bit/symbol sequence. Figure 13 shows the frame structure of the CPICH.



**Figure 13: Frame structure for Common Pilot Channel**

In case transmit diversity (open or closed loop) is used on any downlink channel in the cell, the CPICH shall be transmitted from both antennas using the same channelization and scrambling code. In this case, the pre-defined **symbol bit** sequence of the CPICH is different for Antenna 1 and Antenna 2, see figure 14. In case of no transmit diversity, the **symbol bit** sequence of Antenna 1 in figure 14 is used.



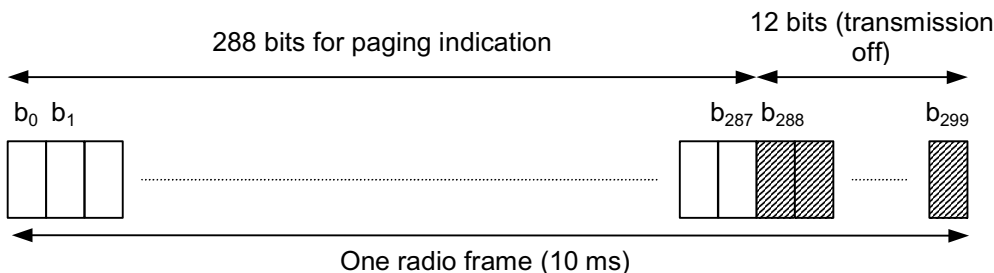
**Figure 14: Modulation pattern for Common Pilot Channel (with A = 1+j)**

There are two types of Common pilot channels, the Primary and Secondary CPICH. They differ in their use and the limitations placed on their physical features.

### 5.3.3.10 Paging Indicator Channel (PICH)

The Paging Indicator Channel (PICH) is a fixed rate (SF=256) physical channel used to carry the paging indicators. The PICH is always associated with an S-CCPCH to which a PCH transport channel is mapped.

Figure 24 illustrates the frame structure of the PICH. One PICH radio frame of length 10 ms consists of 300 bits ( $b_0, b_1, \dots, b_{299}$ ). Of these, 288 bits ( $b_0, b_1, \dots, b_{287}$ ) are used to carry paging indicators. The remaining 12 bits are not formally part of the PICH and shall not be transmitted (DTX). The part of the frame with no transmission is reserved for possible future use.



**Figure 24: Structure of Paging Indicator Channel (PICH)**

In each PICH frame,  $N_p$  paging indicators  $\{P_0, \dots, P_{N_p-1}\}$  are transmitted, where  $N_p=18, 36, 72, \text{ or } 144$ .

The PI calculated by higher layers for use for a certain UE, is associated to the paging indicator  $P_q$ , where  $q$  is computed as a function of the PI computed by higher layers, the SFN of the P-CCPCH radio frame during which the start of the PICH radio frame occurs, and the number of paging indicators per frame ( $N_p$ ):

$$q = \left( PI + \left[ \left( (18 \times (SFN + \lfloor SFN / 8 \rfloor + \lfloor SFN / 64 \rfloor + \lfloor SFN / 512 \rfloor)) \bmod 144 \right) \times \frac{N_p}{144} \right] \right) \bmod N_p$$

Further, the PI calculated by higher layers is associated with the value of the paging indicator  $P_q$ . If a paging indicator in a certain frame is set to "1" it is an indication that UEs associated with this paging indicator and PI should read the corresponding frame of the associated S-CCPCH.

The PI bitmap in the PCH data frames over Iub contains indication values for all higher layer PI values possible. Each bit in the bitmap indicates if the paging indicator associated with that particular PI shall be set to 0 or 1. Hence, the calculation in the formula above is to be performed in Node B to make the association between PI and  $P_q$ .

The mapping from  $\{P_0, \dots, P_{N_p-1}\}$  to the PICH bits  $\{b_0, \dots, b_{287}\}$  are according to table 24.

**Table 24: Mapping of paging indicators  $P_q$  to PICH bits**

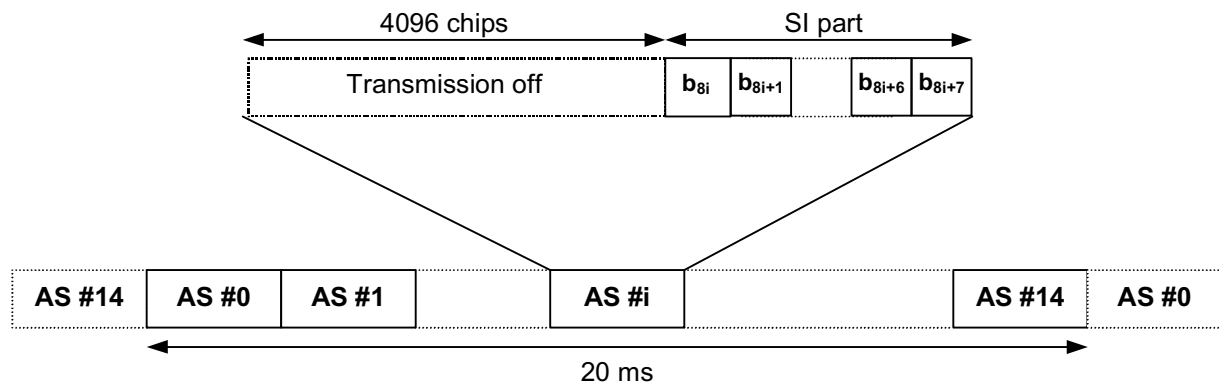
| Number of paging indicators per frame ( $N_p$ ) | $P_q = 1$  | $P_q = 0$   |
|---|--|---|
| $N_p=18$  | $\{b_{16q}, \dots, b_{16q+15}\} = \{-1, -1, \dots, -1\}$ | $\{b_{16q}, \dots, b_{16q+15}\} = \{+10, +10, \dots, +10\}$ |
| $N_p=36$  | $\{b_{8q}, \dots, b_{8q+7}\} = \{-1, -1, \dots, -1\}$    | $\{b_{8q}, \dots, b_{8q+7}\} = \{+10, +10, \dots, +10\}$    |
| $N_p=72$  | $\{b_{4q}, \dots, b_{4q+3}\} = \{-1, -1, \dots, -1\}$    | $\{b_{4q}, \dots, b_{4q+3}\} = \{+10, +10, \dots, +10\}$    |
| $N_p=144$                                       | $\{b_{2q}, b_{2q+1}\} = \{-1, -1\}$                      | $\{b_{2q}, b_{2q+1}\} = \{+10, +10\}$                       |

When transmit diversity is employed for the PICH, STTD encoding is used on the PICH bits as described in subclause 5.3.1.1.1.

### 5.3.3.11 CPCH Status Indicator Channel (CSICH)

The CPCH Status Indicator Channel (CSICH) is a fixed rate (SF=256) physical channel used to carry CPCH status information.

A CSICH is always associated with a physical channel used for transmission of CPCH AP-AICH and uses the same channelization and scrambling codes. Figure 25 illustrates the frame structure of the CSICH. The CSICH frame consists of 15 consecutive access slots (AS) each of length 40 bits. Each access slot consists of two parts, a part of duration 4096 chips with no transmission that is not formally part of the CSICH, and a Status Indicator (SI) part consisting of 8 bits  $b_{8i}, \dots, b_{8i+7}$ , where  $i$  is the access slot number. The part of the slot with no transmission is reserved for use by AICH, AP-AICH or CD/CA-ICH. The modulation used by the CSICH is the same as for the PICH. The phase reference for the CSICH is the Primary CPICH.



**Figure 25: Structure of CPCH Status Indicator Channel (CSICH)**

N Status Indicators  $\{SI_0, \dots, SI_{N-1}\}$  shall be transmitted in each CSICH frame. The mapping from  $\{SI_0, \dots, SI_{N-1}\}$  to the CSICH bits  $\{b_0, \dots, b_{119}\}$  is according to table 25. The Status Indicators shall be transmitted in all the access slots of the CSICH frame, even if some signatures and/or access slots are shared between CPCH and RACH.

**Table 25: Mapping of Status Indicators (SI) to CSICH bits**

| Number of SI per frame (N) | $SI_n = 1$   | $SI_n = 0$  |
|----------------------------|--|---|
| N=1                        | $\{b_0, \dots, b_{119}\} = \{-1, -1, \dots, -1\}$        | $\{b_0, \dots, b_{119}\} = \{+10, +10, \dots, +10\}$        |
| N=3                        | $\{b_{40n}, \dots, b_{40n+39}\} = \{-1, -1, \dots, -1\}$ | $\{b_{40n}, \dots, b_{40n+39}\} = \{+10, +10, \dots, +10\}$ |
| N=5                        | $\{b_{24n}, \dots, b_{24n+23}\} = \{-1, -1, \dots, -1\}$ | $\{b_{24n}, \dots, b_{24n+23}\} = \{+10, +10, \dots, +10\}$ |
| N=15                       | $\{b_{8n}, \dots, b_{8n+7}\} = \{-1, -1, \dots, -1\}$    | $\{b_{8n}, \dots, b_{8n+7}\} = \{+10, +10, \dots, +10\}$    |
| N=30                       | $\{b_{4n}, \dots, b_{4n+3}\} = \{-1, -1, -1, -1\}$       | $\{b_{4n}, \dots, b_{4n+3}\} = \{+10, +10, +10, +10\}$      |
| N=60                       | $\{b_{2n}, b_{2n+1}\} = \{-1, -1\}$                      | $\{b_{2n}, b_{2n+1}\} = \{+10, +10\}$                       |

When transmit diversity is employed for the CSICH, STTD encoding is used on the CSICH bits as described in subclause 5.3.1.1.1.

The CPCH Status Indicator mode (CSICH mode) defines the structure of the information carried on the CSICH. At the UTRAN the value of the CPCH Status Indicator mode is set by higher layers. There are two CSICH modes depending on whether Channel Assignment is active or not. The CSICH mode defines the number of status indicators per frame and the content of each status indicator. Layer 1 transmits the CSICH information according to the CSICH mode and the structures defined in the following paragraphs.

## CHANGE REQUEST

z **25.211 CR 152** z rev **-** z Current version: **4.4.0** z

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the z symbols.

**Proposed change affects:** z (U)SIM  ME/UE  Radio Access Network  Core Network

|                        |   |  |   |
|------------------------|---|--|---|
| <b>Title:</b>          | z | Downlink bit mapping   |   |
| <b>Source:</b>         | z | TSG RAN WG1  |   |
| <b>Work item code:</b> | z | TEI  | <b>Date:</b> z 2002-04-05                 |
| <b>Category:</b>       | z | <b>A</b>   | <b>Release:</b> z REL-4                   |
|                        |   | Use <u>one</u> of the following categories:  | Use <u>one</u> of the following releases: |
|                        |   | <b>F</b> (correction)  | 2 (GSM Phase 2)                           |
|                        |   | <b>A</b> (corresponds to a correction in an earlier release)                                   | R96 (Release 1996)                        |
|                        |   | <b>B</b> (addition of feature),  | R97 (Release 1997)                        |
|                        |   | <b>C</b> (functional modification of feature)  | R98 (Release 1998)                        |
|                        |   | <b>D</b> (editorial modification)  | R99 (Release 1999)                        |
|                        |   | Detailed explanations of the above categories can be found in 3GPP <a href="#">TR 21.900</a> . | REL-4 (Release 4)                         |
|                        |   |  | REL-5 (Release 5)                         |

|                                      |   |  |
|--------------------------------------|---|--|
| <b>Reason for change:</b>            | z | The current description of signal formats for the interfaces between different RAN1 specifications does not consider all types of signals. The signal formats output from 25.211 and 25.212 are formally not compatible with the spreading operation in 25.213. In particular, there is ambiguity about binary, real-valued and DTX signals and their mapping. |
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| <b>Consequences if not approved:</b> | z | Misleading information regarding signal formats on the interfaces between RAN1 specifications.   |

|                              |   |  |
|------------------------------|---|--|
| <b>Clauses affected:</b>     | z | 5.3.1.1.1, 5.3.3.1, 5.3.3.10, 5.3.3.11   |
| <b>Other specs affected:</b> | z | <input type="checkbox"/> Other core specifications      z<br><input type="checkbox"/> Test specifications<br><input type="checkbox"/> O&M Specifications |
| <b>Other comments:</b>       | z | Isolated impact analysis: The implementation of this CR does not impact existing UE or UTRAN implementations.  |



### 5.3.1.1.1 Space time block coding based transmit antenna diversity (STTD)

The open loop downlink transmit diversity employs a space time block coding based transmit diversity (STTD).

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STTD encoding is applied on blocks of 4 consecutive channel bits. A block diagram of a generic STTD encoder for channel bits  $b_0, b_1, b_2, b_3$  is shown in the figure 8 below. Channel coding, rate matching and interleaving is done as in the non-diversity mode. The STTD encoder operates on 4 symbols  $b_0, b_1, b_2, b_3$  as shown in figure 8. For AICH, AP-AICH and CD/CA-ICH, the bit  $b_i$  are real valued signals {0} for DTX bits and {1, -1} for all other channel bits, and  $\bar{b}_i$  is defined as  $-b_i$ . For channels other than AICH, AP-AICH and CD/CA-ICH, the  $b_i$  are 3-valued digits, taking the values 0, 1, "DTX", and  $\bar{b}_i$  is defined as follows: if  $b_i = 0$  then  $\bar{b}_i = 1$ , if  $b_i = 1$  then  $\bar{b}_i = 0$ , otherwise  $\bar{b}_i = -b_i$ .

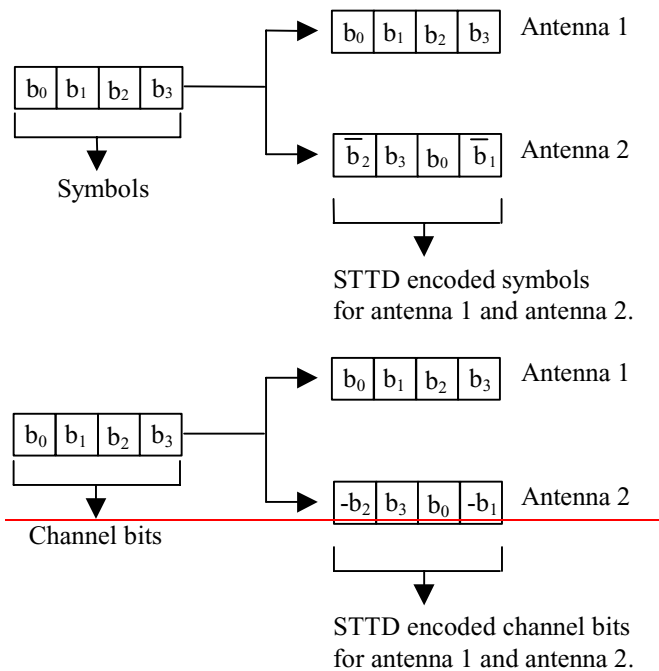
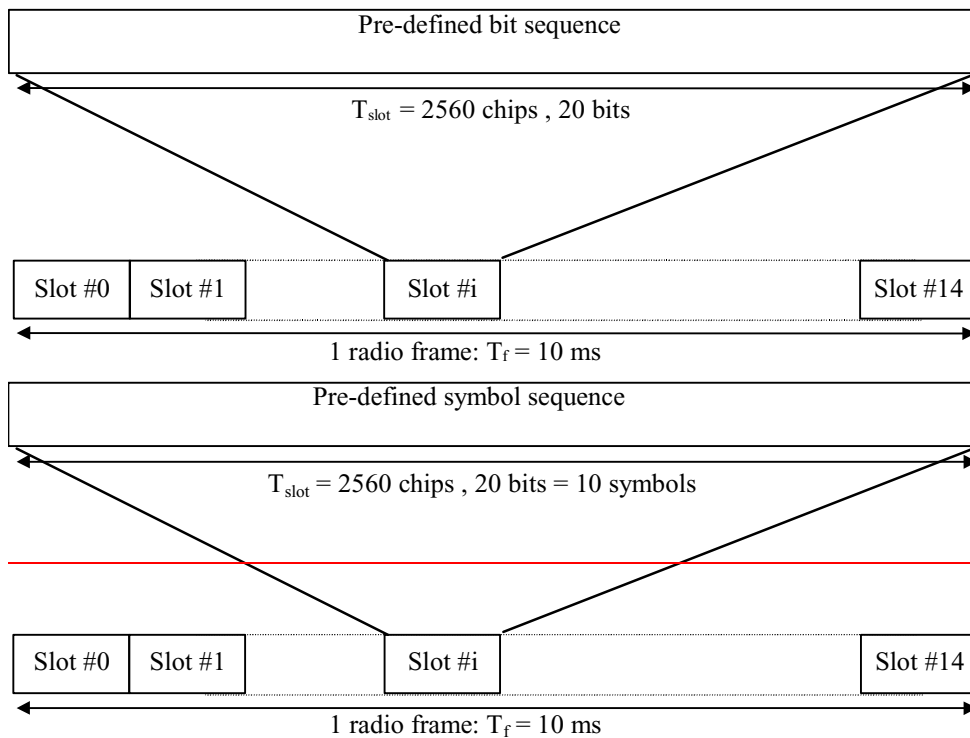


Figure 8: Generic block diagram of the STTD encoder

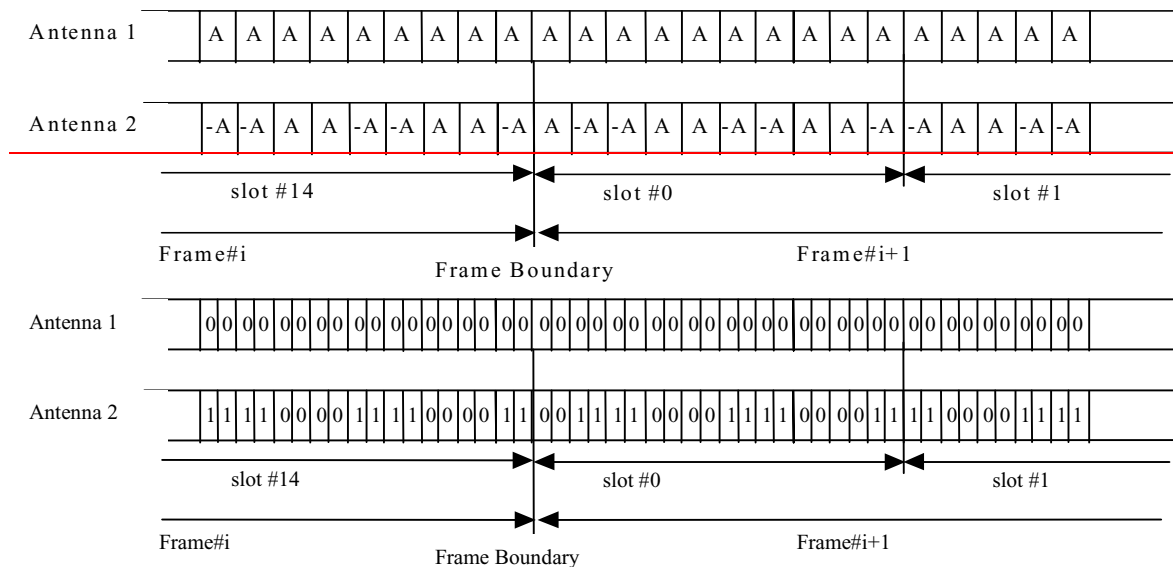
### 5.3.3.1 Common Pilot Channel (CPICH)

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**Figure 13: Frame structure for Common Pilot Channel**

In case transmit diversity (open or closed loop) is used on any downlink channel in the cell, the CPICH shall be transmitted from both antennas using the same channelization and scrambling code. In this case, the pre-defined **symbol bit** sequence of the CPICH is different for Antenna 1 and Antenna 2, see figure 14. In case of no transmit diversity, the **symbol bit** sequence of Antenna 1 in figure 14 is used.



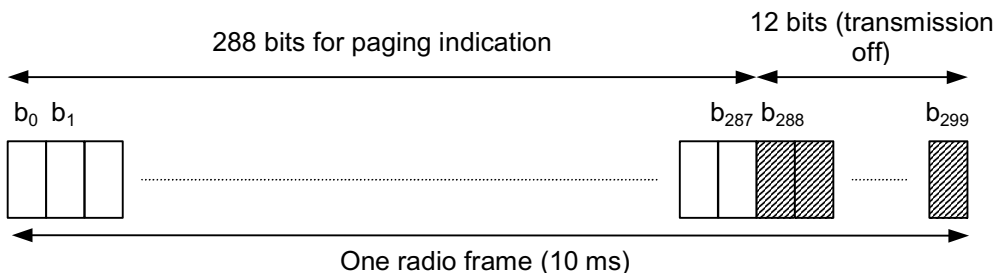
**Figure 14: Modulation pattern for Common Pilot Channel (with A = 1+j)**

There are two types of Common pilot channels, the Primary and Secondary CPICH. They differ in their use and the limitations placed on their physical features.

### 5.3.3.10 Paging Indicator Channel (PICH)

The Paging Indicator Channel (PICH) is a fixed rate (SF=256) physical channel used to carry the paging indicators. The PICH is always associated with an S-CCPCH to which a PCH transport channel is mapped.

Figure 24 illustrates the frame structure of the PICH. One PICH radio frame of length 10 ms consists of 300 bits ( $b_0, b_1, \dots, b_{299}$ ). Of these, 288 bits ( $b_0, b_1, \dots, b_{287}$ ) are used to carry paging indicators. The remaining 12 bits are not formally part of the PICH and shall not be transmitted (DTX). The part of the frame with no transmission is reserved for possible future use.



**Figure 24: Structure of Paging Indicator Channel (PICH)**

In each PICH frame,  $N_p$  paging indicators  $\{P_0, \dots, P_{N_p-1}\}$  are transmitted, where  $N_p=18, 36, 72, \text{ or } 144$ .

The PI calculated by higher layers for use for a certain UE, is associated to the paging indicator  $P_q$ , where  $q$  is computed as a function of the PI computed by higher layers, the SFN of the P-CCPCH radio frame during which the start of the PICH radio frame occurs, and the number of paging indicators per frame ( $N_p$ ):

$$q = \left( PI + \left[ \left( (18 \times (SFN + \lfloor SFN / 8 \rfloor + \lfloor SFN / 64 \rfloor + \lfloor SFN / 512 \rfloor)) \bmod 144 \right) \times \frac{N_p}{144} \right] \right) \bmod N_p$$

Further, the PI calculated by higher layers is associated with the value of the paging indicator  $P_q$ . If a paging indicator in a certain frame is set to "1" it is an indication that UEs associated with this paging indicator and PI should read the corresponding frame of the associated S-CCPCH.

The PI bitmap in the PCH data frames over Iub contains indication values for all higher layer PI values possible. Each bit in the bitmap indicates if the paging indicator associated with that particular PI shall be set to 0 or 1. Hence, the calculation in the formula above is to be performed in Node B to make the association between PI and  $P_q$ .

The mapping from  $\{P_0, \dots, P_{N_p-1}\}$  to the PICH bits  $\{b_0, \dots, b_{287}\}$  are according to table 24.

**Table 24: Mapping of paging indicators  $P_q$  to PICH bits**

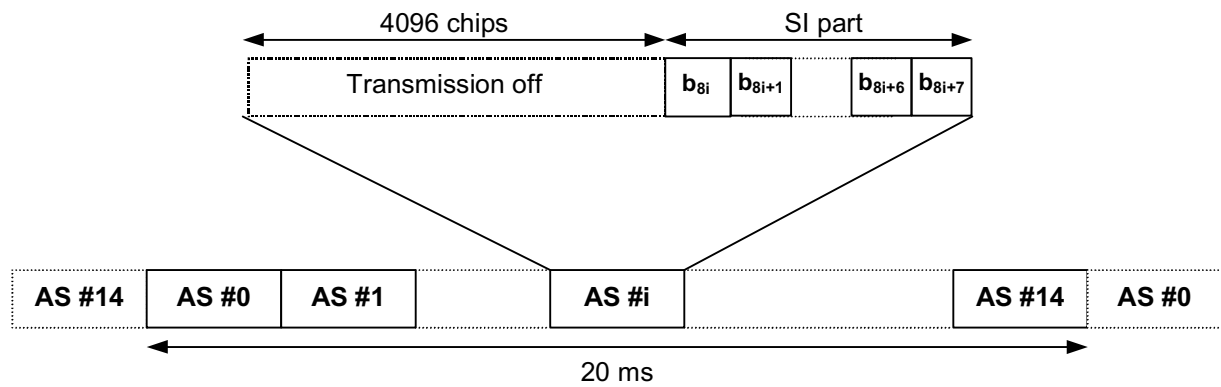
| Number of paging indicators per frame ( $N_p$ ) | $P_q = 1$  | $P_q = 0$   |
|---|--|---|
| $N_p=18$  | $\{b_{16q}, \dots, b_{16q+15}\} = \{-1, -1, \dots, -1\}$ | $\{b_{16q}, \dots, b_{16q+15}\} = \{+10, +10, \dots, +10\}$ |
| $N_p=36$  | $\{b_{8q}, \dots, b_{8q+7}\} = \{-1, -1, \dots, -1\}$    | $\{b_{8q}, \dots, b_{8q+7}\} = \{+10, +10, \dots, +10\}$    |
| $N_p=72$  | $\{b_{4q}, \dots, b_{4q+3}\} = \{-1, -1, \dots, -1\}$    | $\{b_{4q}, \dots, b_{4q+3}\} = \{+10, +10, \dots, +10\}$    |
| $N_p=144$                                       | $\{b_{2q}, b_{2q+1}\} = \{-1, -1\}$                      | $\{b_{2q}, b_{2q+1}\} = \{+10, +10\}$                       |

When transmit diversity is employed for the PICH, STTD encoding is used on the PICH bits as described in subclause 5.3.1.1.1.

### 5.3.3.11 CPCH Status Indicator Channel (CSICH)

The CPCH Status Indicator Channel (CSICH) is a fixed rate (SF=256) physical channel used to carry CPCH status information.

A CSICH is always associated with a physical channel used for transmission of CPCH AP-AICH and uses the same channelization and scrambling codes. Figure 25 illustrates the frame structure of the CSICH. The CSICH frame consists of 15 consecutive access slots (AS) each of length 40 bits. Each access slot consists of two parts, a part of duration 4096 chips with no transmission that is not formally part of the CSICH, and a Status Indicator (SI) part consisting of 8 bits  $b_{8i}, \dots, b_{8i+7}$ , where  $i$  is the access slot number. The part of the slot with no transmission is reserved for use by AICH, AP-AICH or CD/CA-ICH. The modulation used by the CSICH is the same as for the PICH. The phase reference for the CSICH is the Primary CPICH.



**Figure 25: Structure of CPCH Status Indicator Channel (CSICH)**

N Status Indicators  $\{SI_0, \dots, SI_{N-1}\}$  shall be transmitted in each CSICH frame. The mapping from  $\{SI_0, \dots, SI_{N-1}\}$  to the CSICH bits  $\{b_0, \dots, b_{119}\}$  is according to table 25. The Status Indicators shall be transmitted in all the access slots of the CSICH frame, even if some signatures and/or access slots are shared between CPCH and RACH.

**Table 25: Mapping of Status Indicators (SI) to CSICH bits**

| Number of SI per frame (N) | $SI_n = 1$   | $SI_n = 0$  |
|----------------------------|--|---|
| N=1                        | $\{b_0, \dots, b_{119}\} = \{-1, -1, \dots, -1\}$        | $\{b_0, \dots, b_{119}\} = \{+10, +10, \dots, +10\}$        |
| N=3                        | $\{b_{40n}, \dots, b_{40n+39}\} = \{-1, -1, \dots, -1\}$ | $\{b_{40n}, \dots, b_{40n+39}\} = \{+10, +10, \dots, +10\}$ |
| N=5                        | $\{b_{24n}, \dots, b_{24n+23}\} = \{-1, -1, \dots, -1\}$ | $\{b_{24n}, \dots, b_{24n+23}\} = \{+10, +10, \dots, +10\}$ |
| N=15                       | $\{b_{8n}, \dots, b_{8n+7}\} = \{-1, -1, \dots, -1\}$    | $\{b_{8n}, \dots, b_{8n+7}\} = \{+10, +10, \dots, +10\}$    |
| N=30                       | $\{b_{4n}, \dots, b_{4n+3}\} = \{-1, -1, -1, -1\}$       | $\{b_{4n}, \dots, b_{4n+3}\} = \{+10, +10, +10, +10\}$      |
| N=60                       | $\{b_{2n}, b_{2n+1}\} = \{-1, -1\}$                      | $\{b_{2n}, b_{2n+1}\} = \{+10, +10\}$                       |

When transmit diversity is employed for the CSICH, STTD encoding is used on the CSICH bits as described in subclause 5.3.1.1.1.

The CPCH Status Indicator mode (CSICH mode) defines the structure of the information carried on the CSICH. At the UTRAN the value of the CPCH Status Indicator mode is set by higher layers. There are two CSICH modes depending on whether Channel Assignment is active or not. The CSICH mode defines the number of status indicators per frame and the content of each status indicator. Layer 1 transmits the CSICH information according to the CSICH mode and the structures defined in the following paragraphs.

## CHANGE REQUEST

z **25.211 CR 153** z rev **-** z Current version: **5.0.0** z

For **HELP** on using this form, see bottom of this page or look at the pop-up text over the z symbols.

**Proposed change affects:** z (U)SIM  ME/UE  Radio Access Network  Core Network

|                        |   |   |                 |  |
|------------------------|---|---|-----------------|--|
| <b>Title:</b>          | z | Downlink bit mapping  |                 |  |
| <b>Source:</b>         | z | TSG RAN WG1   |                 |  |
| <b>Work item code:</b> | z | TEI   | <b>Date:</b>    | z 2002-04-05   |
| <b>Category:</b>       | z | <b>A</b>  | <b>Release:</b> | z REL-5  |
|                        |   | Use <u>one</u> of the following categories:<br><b>F</b> (correction)<br><b>A</b> (corresponds to a correction in an earlier release)<br><b>B</b> (addition of feature),<br><b>C</b> (functional modification of feature)<br><b>D</b> (editorial modification) |                 | Use <u>one</u> of the following releases:<br>2 (GSM Phase 2)<br>R96 (Release 1996)<br>R97 (Release 1997)<br>R98 (Release 1998)<br>R99 (Release 1999)<br>REL-4 (Release 4)<br>REL-5 (Release 5) |
|                        |   | Detailed explanations of the above categories can be found in 3GPP <a href="#">TR 21.900</a> .  |                 |  |

|                                      |   |  |
|--------------------------------------|---|--|
| <b>Reason for change:</b>            | z | The current description of signal formats for the interfaces between different RAN1 specifications does not consider all types of signals. The signal formats output from 25.211 and 25.212 are formally not compatible with the spreading operation in 25.213. In particular, there is ambiguity about binary, real-valued and DTX signals and their mapping. |
| <b>Summary of change:</b>            | z | For all channels, except AICH-type channels, the output format from 25.211 is specified as 0, 1 and "DTX". STTD description, CPICH, PICH and CSICH definitions are updated accordingly.  |
| <b>Consequences if not approved:</b> | z | Misleading information regarding signal formats on the interfaces between RAN1 specifications.   |

|                              |   |   |   |
|------------------------------|---|---|---|
| <b>Clauses affected:</b>     | z | 5.3.1.1.1, 5.3.3.1, 5.3.3.10, 5.3.3.11  |   |
| <b>Other specs affected:</b> | z | <input type="checkbox"/> Other core specifications<br><input type="checkbox"/> Test specifications<br><input type="checkbox"/> O&M Specifications | z |
| <b>Other comments:</b>       | z | Isolated impact analysis: The implementation of this CR does not impact existing UE or UTRAN implementations.                                     |   |

### 5.3.1.1.1 Space time block coding based transmit antenna diversity (STTD)

The open loop downlink transmit diversity employs a space time block coding based transmit diversity (STTD).

The STTD encoding is optional in UTRAN. STTD support is mandatory at the UE.

If higher layers signal that neither P-CPICH nor S-CPICH can be used as phase reference for the downlink DPCH for a radio link in a cell, the UE shall assume that STTD is not used for the downlink DPCH (and the associated PDSCH if applicable) in that cell.

STTD encoding is applied on blocks of 4 consecutive channel bits. A block diagram of a generic STTD encoder for channel bits  $b_0, b_1, b_2, b_3$  is shown in the figure 8 below. Channel coding, rate matching and interleaving is done as in the non-diversity mode. The STTD encoder operates on 4 symbols  $b_0, b_1, b_2, b_3$  as shown in figure 8. For AICH, AP-AICH and CD/CA-ICH, the bit  $b_i$  are real valued signals {0} for DTX bits and {1, -1} for all other channel bits, and  $\bar{b}_i$  is defined as  $-b_i$ . For channels other than AICH, AP-AICH and CD/CA-ICH, the  $b_i$  are 3-valued digits, taking the values 0, 1, "DTX", and  $\bar{b}_i$  is defined as follows: if  $b_i = 0$  then  $\bar{b}_i = 1$ , if  $b_i = 1$  then  $\bar{b}_i = 0$ , otherwise  $\bar{b}_i = -b_i$ .

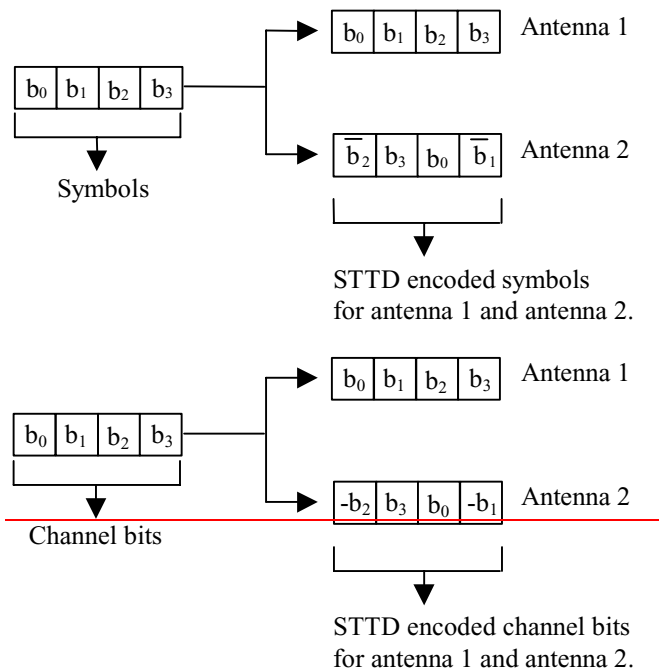
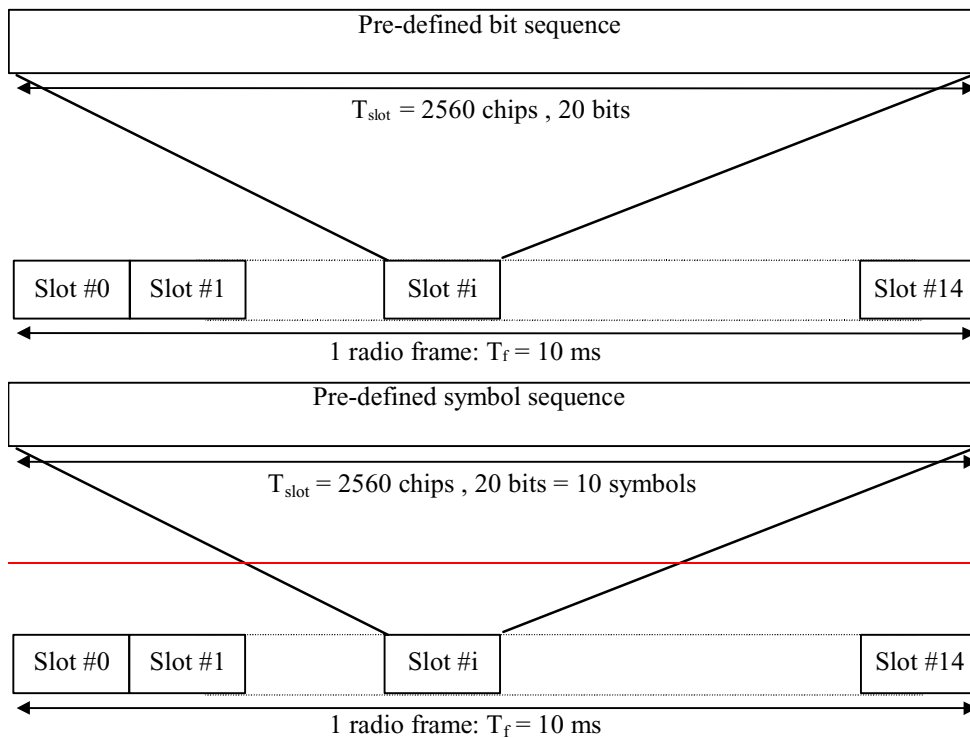


Figure 8: Generic block diagram of the STTD encoder

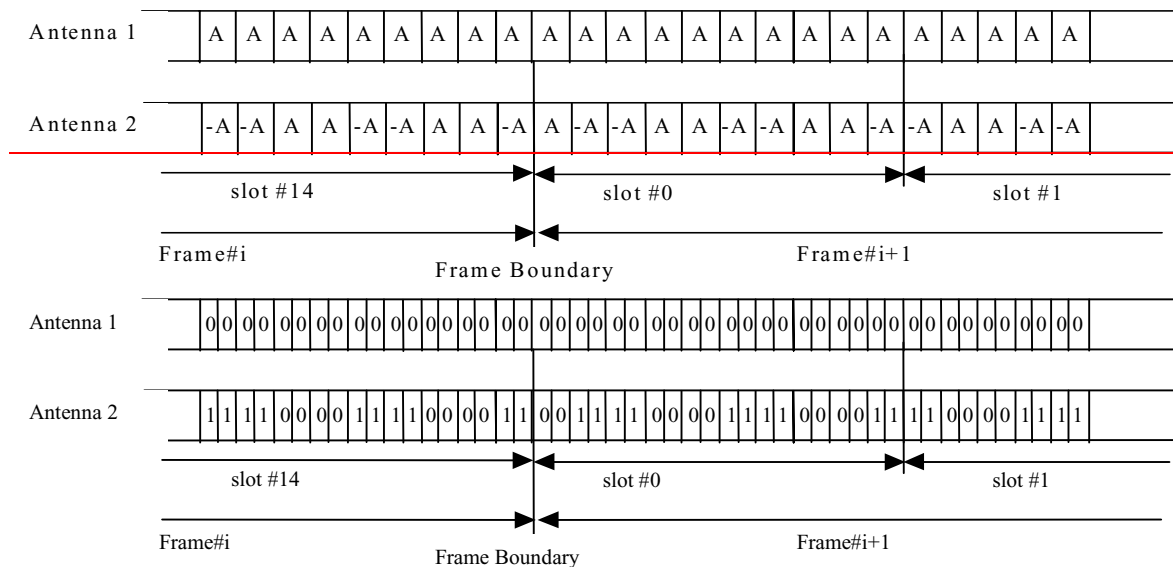
### 5.3.3.1 Common Pilot Channel (CPICH)

The CPICH is a fixed rate (30 kbps, SF=256) downlink physical channel that carries a pre-defined bit/symbol sequence. Figure 13 shows the frame structure of the CPICH.



**Figure 13: Frame structure for Common Pilot Channel**

In case transmit diversity (open or closed loop) is used on any downlink channel in the cell, the CPICH shall be transmitted from both antennas using the same channelization and scrambling code. In this case, the pre-defined **symbol bit** sequence of the CPICH is different for Antenna 1 and Antenna 2, see figure 14. In case of no transmit diversity, the **symbol bit** sequence of Antenna 1 in figure 14 is used.



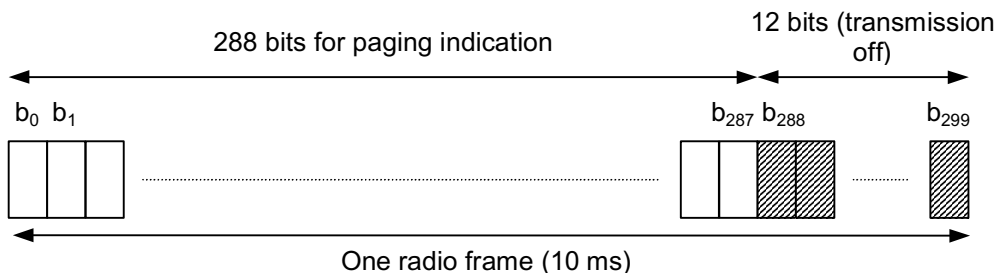
**Figure 14: Modulation pattern for Common Pilot Channel (with A = 1+j)**

There are two types of Common pilot channels, the Primary and Secondary CPICH. They differ in their use and the limitations placed on their physical features.

### 5.3.3.10 Paging Indicator Channel (PICH)

The Paging Indicator Channel (PICH) is a fixed rate (SF=256) physical channel used to carry the paging indicators. The PICH is always associated with an S-CCPCH to which a PCH transport channel is mapped.

Figure 24 illustrates the frame structure of the PICH. One PICH radio frame of length 10 ms consists of 300 bits ( $b_0, b_1, \dots, b_{299}$ ). Of these, 288 bits ( $b_0, b_1, \dots, b_{287}$ ) are used to carry paging indicators. The remaining 12 bits are not formally part of the PICH and shall not be transmitted (DTX). The part of the frame with no transmission is reserved for possible future use.



**Figure 24: Structure of Paging Indicator Channel (PICH)**

In each PICH frame,  $N_p$  paging indicators  $\{P_0, \dots, P_{N_p-1}\}$  are transmitted, where  $N_p=18, 36, 72, \text{ or } 144$ .

The PI calculated by higher layers for use for a certain UE, is associated to the paging indicator  $P_q$ , where  $q$  is computed as a function of the PI computed by higher layers, the SFN of the P-CCPCH radio frame during which the start of the PICH radio frame occurs, and the number of paging indicators per frame ( $N_p$ ):

$$q = \left( PI + \left[ \left( (18 \times (SFN + \lfloor SFN / 8 \rfloor + \lfloor SFN / 64 \rfloor + \lfloor SFN / 512 \rfloor)) \bmod 144 \right) \times \frac{N_p}{144} \right] \right) \bmod N_p$$

Further, the PI calculated by higher layers is associated with the value of the paging indicator  $P_q$ . If a paging indicator in a certain frame is set to "1" it is an indication that UEs associated with this paging indicator and PI should read the corresponding frame of the associated S-CCPCH.

The PI bitmap in the PCH data frames over Iub contains indication values for all higher layer PI values possible. Each bit in the bitmap indicates if the paging indicator associated with that particular PI shall be set to 0 or 1. Hence, the calculation in the formula above is to be performed in Node B to make the association between PI and  $P_q$ .

The mapping from  $\{P_0, \dots, P_{N_p-1}\}$  to the PICH bits  $\{b_0, \dots, b_{287}\}$  are according to table 24.

**Table 24: Mapping of paging indicators  $P_q$  to PICH bits**

| Number of paging indicators per frame ( $N_p$ ) | $P_q = 1$  | $P_q = 0$   |
|---|--|---|
| $N_p=18$  | $\{b_{16q}, \dots, b_{16q+15}\} = \{-1, -1, \dots, -1\}$ | $\{b_{16q}, \dots, b_{16q+15}\} = \{+10, +10, \dots, +10\}$ |
| $N_p=36$  | $\{b_{8q}, \dots, b_{8q+7}\} = \{-1, -1, \dots, -1\}$    | $\{b_{8q}, \dots, b_{8q+7}\} = \{+10, +10, \dots, +10\}$    |
| $N_p=72$  | $\{b_{4q}, \dots, b_{4q+3}\} = \{-1, -1, \dots, -1\}$    | $\{b_{4q}, \dots, b_{4q+3}\} = \{+10, +10, \dots, +10\}$    |
| $N_p=144$                                       | $\{b_{2q}, b_{2q+1}\} = \{-1, -1\}$                      | $\{b_{2q}, b_{2q+1}\} = \{+10, +10\}$                       |

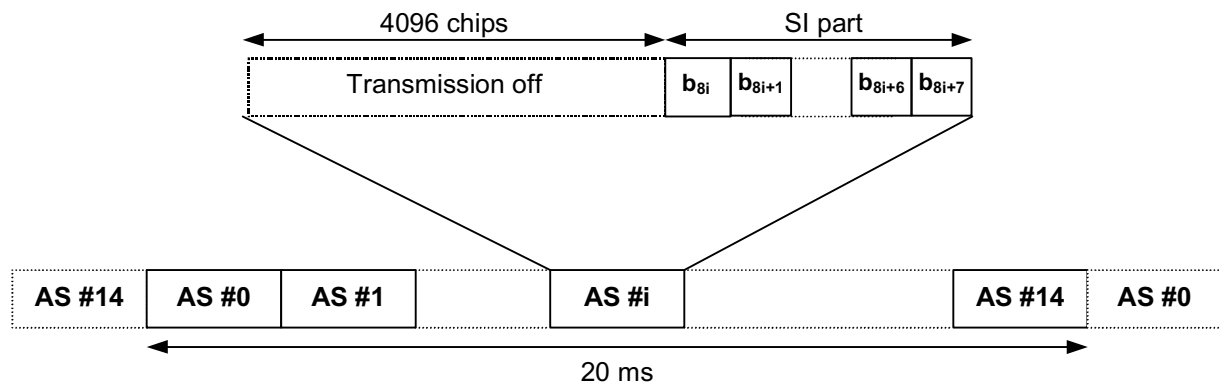
When transmit diversity is employed for the PICH, STTD encoding is used on the PICH bits as described in subclause 5.3.1.1.1.

### 5.3.3.11 CPCH Status Indicator Channel (CSICH)

The CPCH Status Indicator Channel (CSICH) is a fixed rate (SF=256) physical channel used to carry CPCH status information.

A CSICH is always associated with a physical channel used for transmission of CPCH AP-AICH and uses the same channelization and scrambling codes. Figure 25 illustrates the frame structure of the CSICH. The CSICH frame consists of 15 consecutive access slots (AS) each of length 40 bits. Each access slot consists of two parts, a part of duration 4096 chips with no transmission that is not formally part of the CSICH, and a Status Indicator (SI) part consisting of 8 bits  $b_{8i}, \dots, b_{8i+7}$ , where  $i$  is the access slot number. The part of the slot with no transmission is reserved for use by AICH, AP-AICH or CD/CA-ICH. The modulation used by the CSICH is the same as for the PICH. The phase reference for the CSICH is the Primary CPICH.





**Figure 25: Structure of CPCH Status Indicator Channel (CSICH)**

N Status Indicators  $\{SI_0, \dots, SI_{N-1}\}$  shall be transmitted in each CSICH frame. The mapping from  $\{SI_0, \dots, SI_{N-1}\}$  to the CSICH bits  $\{b_0, \dots, b_{119}\}$  is according to table 25. The Status Indicators shall be transmitted in all the access slots of the CSICH frame, even if some signatures and/or access slots are shared between CPCH and RACH.

**Table 25: Mapping of Status Indicators (SI) to CSICH bits**

| Number of SI per frame (N) | $SI_n = 1$   | $SI_n = 0$  |
|----------------------------|--|---|
| N=1                        | $\{b_0, \dots, b_{119}\} = \{-1, -1, \dots, -1\}$        | $\{b_0, \dots, b_{119}\} = \{+10, +10, \dots, +10\}$        |
| N=3                        | $\{b_{40n}, \dots, b_{40n+39}\} = \{-1, -1, \dots, -1\}$ | $\{b_{40n}, \dots, b_{40n+39}\} = \{+10, +10, \dots, +10\}$ |
| N=5                        | $\{b_{24n}, \dots, b_{24n+23}\} = \{-1, -1, \dots, -1\}$ | $\{b_{24n}, \dots, b_{24n+23}\} = \{+10, +10, \dots, +10\}$ |
| N=15                       | $\{b_{8n}, \dots, b_{8n+7}\} = \{-1, -1, \dots, -1\}$    | $\{b_{8n}, \dots, b_{8n+7}\} = \{+10, +10, \dots, +10\}$    |
| N=30                       | $\{b_{4n}, \dots, b_{4n+3}\} = \{-1, -1, -1, -1\}$       | $\{b_{4n}, \dots, b_{4n+3}\} = \{+10, +10, +10, +10\}$      |
| N=60                       | $\{b_{2n}, b_{2n+1}\} = \{-1, -1\}$                      | $\{b_{2n}, b_{2n+1}\} = \{+10, +10\}$                       |

When transmit diversity is employed for the CSICH, STTD encoding is used on the CSICH bits as described in subclause 5.3.1.1.1.

The CPCH Status Indicator mode (CSICH mode) defines the structure of the information carried on the CSICH. At the UTRAN the value of the CPCH Status Indicator mode is set by higher layers. There are two CSICH modes depending on whether Channel Assignment is active or not. The CSICH mode defines the number of status indicators per frame and the content of each status indicator. Layer 1 transmits the CSICH information according to the CSICH mode and the structures defined in the following paragraphs.