**3GPP TSG- WG4 Meeting #116**

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| *CR-Form-v12.3* |
| **CHANGE REQUEST** |
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|  |  | **CR** |  | **rev** | **1** | **Current version:** |  |  |
|  |
| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **x** | Radio Access Network |  | Core Network |  |

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| ***Title:***  |  |
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| ***Source to WG:*** |  |
| ***Source to TSG:*** |  |
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| ***Work item code:*** |  |  | ***Date:*** |  |
|  |  |  |  |  |
| ***Category:*** |  |  | ***Release:*** |  |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-17 (Release 17)Rel-18 (Release 18)Rel-19 (Release 19) Rel-20 (Release 20)* |
|  |  |
| ***Reason for change:*** | This CR provides a clarifition to how the specification refers to a SSB when defining the requirements for UE supporting operating in a 3MHz BW cell.  |
|  |  |
| ***Summary of change:*** | 1. Correcting the table headers for Index reading for a cell with 12 PRB SSB.
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|  |  |
| ***Consequences if not approved:*** | Specification remains unclear |
|  |  |
| ***Clauses affected:*** | 9.2.5, 9.2.6, 9.3.4, 9.3.9 |
|  |  |
|  | **Y** | **N** |  |  |
| ***Other specs*** |  | **x** |  Other core specifications  | TS/TR ... CR ...  |
| ***affected:*** |  | **x** |  Test specifications | TS/TR ... CR ...  |
| ***(show related CRs)*** |  | **x** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |   |
|  |  |
| ***This CR's revision history:*** |  |

<Change #1>

9.2.5 Intrafrequency measurements without measurement gaps

9.2.5.1 Intrafrequency cell identification

The UE shall be able to identify a new detectable intra-frequency cell within Tidentify\_intra\_without\_index if the UE is not indicated to report SSB based RRM measurement result with the associated SSB index(*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured), or the UE is indicated that the neighbour cell is synchronous with the serving cell (*deriveSSB-IndexFromCell* is enabled). Otherwise the UE shall be able to identify a new detectable intra frequency cell within Tidentify\_intra\_with\_index. The UE shall be able to identify a new detectable intra frequency SS block of an already detected cell within Tidentify\_intra\_without\_index. It is assumed that *deriveSSB-IndexFromCell* is always enabled for FR1 TDD and FR2 with SCS smaller or equal to 480 kHz.

Tidentify\_intra\_without\_index = (TPSS/SSS\_sync\_intra + TSSB\_measurement\_period\_intra) ms

Tidentify\_intra\_with\_index = (TPSS/SSS\_sync\_intra + T SSB\_measurement\_period\_intra + TSSB\_time\_index\_intra) ms

Where:

 TPSS/SSS\_sync\_intra: it is the time period used in PSS/SSS detection

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC ≤ 40ms, TPSS/SSS\_sync\_intra is given in table 9.2.5.1-11; otherwise, TPSS/SSS\_sync\_intra is given in table 9.2.5.1-2.

- For UE indicating *no-gap-no-interruption* via *NeedForInterruptionInfoNR-r18*, TPSS/SSS\_sync\_intra is given in table 9.2.5.1-1 for FR1 and table 9.2.5.1-2 for FR2. For UE indicating *no-gap-with-interruption* via *NeedForInterruptionInfoNR-r18*, TPSS/SSS\_sync\_intra is given in table 9.2.5.1-17 for FR1 and table 9.2.5.1-18 for FR2.

- Otherwise, TPSS/SSS\_sync\_intra is given in tables 9.2.5.1-1, 9.2.5.1-2, 9.2.5.1-4 (CC with deactivated SCell) or 9.2.5.1-5 (CC with deactivated SCell) or 9.2.5.1-9 (CC with deactivated SCell) or 9.2.5.1-11 or 9.2.5.1-12 (CC with deactivated PSCell) or 9.2.5.1-13 (CC with deactivated PSCell).

 TSSB\_time\_index\_intra: it is the time period used to acquire the index of the SSB being measured

- For UE indicating *support3MHz-ChannelBW-Symmetric-r18* and configured to operate on a target cell with 12 PRB SSB in FR1, TSSB\_time\_index\_intra\_less\_than\_5Mhz is given in table 9.2.5.1-23.

- For UE indicatting *no-gap-no-interruption* via *NeedForInterruptionInfoNR-r18*, TSSB\_time\_index\_intra is given in table 9.2.5.1-3 for FR1 and table 9.2.5.1-15 for FR2-2. For UE indicating *no-gap-with-interruption* via *NeedForInterruptionInfoNR-r18*, TSSB\_time\_index\_intra is given in table 9.2.5.1-19 for FR1.

- Otherwise, TSSB\_time\_index\_intra is given in tables 9.2.5.1-3, 9.2.5.1-15 (FR2-2), 9.2.5.1-6 (CC with deactivated SCell), 9.2.5.1-10 (CC with deactivated SCell) or 9.2.5.1-14 (CC with deactivated PSCell).

- TSSB\_measurement\_period\_intra: equal to a measurement period of SSB based measurement

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC ≤ 40ms, TSSB\_measurement\_period\_intra is given in table 9.2.5.2-7; otherwise, TSSB\_measurement\_period\_intra is given in table 9.2.5.2-2.

- For UE indicating *no-gap-no-interruption* via *NeedForInterruptionInfoNR-r18*, TSSB\_measurement\_period\_intra is given in table 9.2.5.2-1 for FR1 and table 9.2.5.2-2 for FR2. For UE indicating *no-gap-with-interruption* via *NeedForInterruptionInfoNR-r18*, TSSB\_measurement\_period\_intra is given in table 9.2.5.2-10 for FR1 and table table 9.2.5.2-11 for FR2.

- For power class 6 UE supporting *measEnhCAInterFreqFR2-r18* when *highSpeedMeasFlagFR2* is configured, the TSSB\_measurement\_period\_intra given in table 9.2.5.2-7 (if SMTC ≤ 40ms) and table 9.2.5.2-2 (if SMTC > 40ms) shall apply for SCC.

- Otherwise, TSSB\_measurement\_period\_intra is given in table 9.2.5.2-1, table 9.2.5.2-2, table 9.2.5.2-3 (CC with deactivated SCell), 9.2.5.2-4 (CC with deactivated SCell), 9.2.5.2-5 or 9.2.5.2-6 (CC with deactivated SCell), 9.2.5.2-8 (CC with deactivated PSCell) or 9.2.5.2-9 (CC with deactivated PSCell).

- CSSFintra: it is a carrier specific scaling factor and is determined

- according to CSSFoutside\_gap,i in clause 9.1.5.1 for measurement conducted outside measurement gaps, i.e.

- when intra-frequency SMTC is fully non overlapping or partially overlapping with GAP, or

- when intra-frequency SMTC is fully non overlapping with GAP for UE indicating *no-gap-with-interruption*, or

- when intra-frequency SMTC is fully non overlapping or partially overlapping with GAP for UE indicating *no-gap-no-interruption*, or

 For a UE that supports Pre-MG, an SMTC occasion is only considered to be overlapped by Pre-MG if the Pre-MG is activated.

 if the high layer in TS 38.331 [2] signalling of *smtc2* is configured, the assumed periodicity of intra-frequency SMTC occasions corresponds to the value of higher layer parameter *smtc2*; Otherwise the assumed periodicity of intra-frequency SMTC occasions corresponds to the value of higher layer parameter *smtc1*.

 Mpss/sss\_sync\_w/o\_gaps: For a UE supporting FR2-1 power class 1 or 5, Mpss/sss\_sync\_w/o\_gaps =40. For a UE supporting power class 2, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2-1 power class 3, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2-1 power class 4, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2-1 power class 6, Mpss/sss\_sync\_w/o\_gaps =24. For a UE supporting FR2-2 power class 1, Mpss/sss\_sync\_w/o\_gaps = 60. For a UE supporting FR2-2 power class 2, Mpss/sss\_sync\_w/o\_gaps = 36. For a UE supporting FR2-2 power class 3, Mpss/sss\_sync\_w/o\_gaps = 36.

 Mmeas\_period\_w/o\_gaps: For a UE supporting FR2-1 power class 1 or 5, Mmeas\_period\_w/o\_gaps =40. For a UE supporting FR2-1 power class 2, Mmeas\_period\_w/o\_gaps =24. For a UE supporting FR2-1 power class 3, Mmeas\_period\_w/o\_gaps =24. For a UE supporting power class 4, Mmeas\_period\_w/o\_gaps =24. For a UE supporting power class 6, Mmeas\_period\_w/o\_gaps =24. For a UE supporting FR2-2 power class 1, Mmeas\_period\_w/o\_gaps = 60. For a UE supporting FR2-2 power class 2, Mmeas\_period\_w/o\_gaps = 36. For a UE supporting FR2-2 power class 3, Mmeas\_period\_w/o\_gaps = 36.

MSSB\_index\_intra: For a UE supporting FR2-2 power class 1, MSSB\_index\_intra = 72 samples. For a UE supporting FR2-2 power class 2, MSSB\_index\_intra = 48 samples. For a UE supporting FR2 power class 3, MSSB\_index\_intra = 48 samples.

When UE supports concurrent GAPs, i.e., supports the following capability or capabilities’ combination:

- concurrentMeasGap-r17, or

- concurrentMeasGapsPreMG-r18, or

- concurrentMeasGapsNCSG-r18,

Or when UE supports *musim-GapPreference-r17* or both concurrent measurement GAPs and *musim-GapPreference-r17* and UE concurrent GAPs or periodic MUSIM gaps or both concurrent gaps and periodic MUSIM gaps are configured

 Kp is the scaling factor for an SSB frequency layer to be measured without GAP. Kp = Ntotal / Navailable, where Navailable and Ntotal are calculated as follows:

- For a window W of duration max(SMTC period, xRP\_max), where xRP\_max is the maximum xRP across all configured per-UE GAPs, periodic MUSIM gaps, and/or per-FR GAPs within the same FR as the SSB frequency layer, and starting from the beginning of any SMTC occasion:

- Ntotal is the total number of SMTC occasions within the window, including those overlapped with GAP and MUSIM gap occasions within the window, and

- Navailable is the number of SMTC occasions that are not overlapped with any non-dropped GAP or non-dropped MUSIM gap occasions within the window W, after accounting for measurement GAP and MUSIM gap collisions by applying the collision rules for GAP and MUSIM gap in clauses 9.1.8.3, 9.1.10.4, 9.1.10.5, 9.1.12.3, and 9.1.13.3, respectively.

 Kp = 1 when Navailable = 0.

- xRP = MGRP when configured GAP is activated Pre-MG or MG, and xRP = VIRP when configured GAP is NCSG, also xRP = MGRP for periodic MUSIM gap.

 Requirements in this clause do not apply when Navailable = 0 due to fully overlapping between SMTC occasions and MUSIM gap occasions within the window W.

When UE supports [*MUSIM-GapConfig-17]* and the SMTC occasion of the target frequency layer is overlapping with the configured aperiodic MUSIM gap, longer cell identification period for the target frequency layer is expected.

- Otherwise, when the UE is not configured with or UE does not support concurrent GAPs and the UE is not configured with periodic MUSIM gaps or UE does not support MUSIM gaps:

 When intra-frequency SMTC is fully non overlapping with measurement gaps or NCSG, or intra-frequency SMTC is fully overlapping with MGs or NCSG, Kp=1

 When intra-frequency SMTC is partially overlapping with measurement gaps, Kp = 1/(1- (SMTC period /MGRP)), where SMTC period < MGRP. When intra-frequency SMTC is partially overlapping with NCSG, Kp = 1/(1- (SMTC period /VIRP)), where SMTC period < VIRP. For calculation of Kp, if the high layer signalling (TS 38.331 [2]) of *smtc2* is configured, for cells indicated in the *pci-List* parameter in *smtc2*, the SMTC periodicity corresponds to the value of higher layer parameter *smtc2*; for the other cells, the SMTC periodicity corresponds to the value of higher layer parameter *smtc1.* If the higher layer signaling in TS 38.331 [2] signalling of *smtc2* is present and *smtc1* is fully overlapping with measurement gaps and *smtc2* is partially overlapping with measurement gaps, requirements are not specified for Tidentify\_intra\_without\_index or Tidentify\_intra\_with\_index

 For FR2,

 Klayer1\_measurement=1,

- if all of the reference signals configured for RLM, BFD, CBD or L1-RSRP for beam reporting on any FR2 serving frequency in the same band outside measurement gap are not fully overlapped by intra-frequency SMTC occasions, or

- if all of the reference signal configured for RLM, BFD, CBD or L1-RSRP for beam reporting on any FR2 serving frequency in the same band outside measurement gap and fully-overlapped by intra-frequency SMTC occasions are not overlapped with any of the SSB symbols and the RSSI symbols, and 1 symbol before each consecutive SSB symbols and the RSSI symbols, and 1 symbol after each consecutive SSB symbols and the RSSI symbols, given that *SSB-ToMeasure* and *SS-RSSI-Measurement* are configured, where SSB symbols are indicated by the union set of *SSB-ToMeasure* from all the configured measurement objects on the same serving carrier which can be merged.and RSSI symbols are indicated by *SS-RSSI-Measurement*;

 Klayer1\_measurement=1.5, otherwise.

 If the above-mentioned reference signal configured for L1-RSRP measurement is aperiodic CSI-RS resource, longer cell identification delay would be expected.

 If MCG DRX is in use, cell identification requirements for intra-frequency measurement in MCG specified in table 9.2.5.1-1, table 9.2.5.1-2, table 9.2.5.1-3, table 9.2.5.1-4, table 9.2.5.1-5 and table 9.2.5.1-6 shall depend on the MCG DRX cycle. If SCG DRX is in use, cell identification requirements for intra-frequency measurement in SCG specified in table 9.2.5.1-1, table 9.2.5.1-2, table 9.2.5.1-3, table 9.2.5.1-4, table 9.2.5.1-5, table 9.2.5.1-6, table 9.2.5.1-12, table 9.2.5.1-13 and table 9.2.5.1-14 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

- When the target SSB is completely contained in active BWP of UE or the active downlink BWP is initial BWP, the intra-frequency measurement shall be conducted without gap and without interruption regardless of the NeedForGaps’ status reporting.

**Table 9.2.5.1-1: Time period for PSS/SSS detection, (Frequency range FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | max( 600 ms, ceil( 5 x Kp) x SMTC period )Note 1 x CSSFintra |
| DRX cycle≤ 320 ms | max( 600 ms, ceil(M2 Note 2x 5 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320 ms | ceil(5 x Kp) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms,otherwise M2=1.NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 4: When *highSpeedMeasCA-Scell-r17* is configured and UE supports *measurementEnhancementCA-r17*, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1. |

**Table 9.2.5.1-2: Time period for PSS/SSS detection, (Frequency range FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | max(600 ms, ceil(Mpss/sss\_sync\_w/o\_gaps x KFR x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320 ms | max(600 ms, ceil(1.5 x Mpss/sss\_sync\_w/o\_gaps x KFR x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320 ms | ceil(Mpss/sss\_sync\_w/o\_gaps x KFR x Kp x Klayer1\_measurement) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: KFR is a scaling factor depending on the frequency range and the SSB SCS. For FR2-1, KFR = 1. For FR2-2: KFR = 1 if the SCS of the SSB of the cell being detected is 120 kHz, KFR = 2 if the SCS of the SSB of the cell being detected is 480 kHz, and KFR = 3 if the SCS of the SSB of the cell being detected is 960 kHz. |

**Table 9.2.5.1-3: Time period for time index detection (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra** |
| No DRX | max(120 ms, ceil( 3 x Kp )x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 320 ms | max(120 ms, ceil (M2 Note 2 x 3 x Kp) x max(SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320 ms | Ceil(3 x Kp) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms,otherwise M2=1NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 4: When *highSpeedMeasCA-Scell-r17* is configured and UE supports *measurementEnhancementCA-r17*, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 |

**Table 9.2.5.1-4: Time period for PSS/SSS detection, CC with deactivated SCell (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320 ms | Ceil(5 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320 ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to CC with deactivated SCell in SCG. |

**Table 9.2.5.1-5: Time period for PSS/SSS detection, CC with deactivated SCell (FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320 ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320 ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to CC with deactivated SCell in SCG. |

**Table 9.2.5.1-6: Time period for time index detection, CC with deactivated SCell (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra** |
| No DRX | Ceil(3 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320 ms | Ceil(3 x Kp) x max(measCycleSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320 ms | Ceil(3 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: The requirements also apply to CC with deactivated SCell in SCG. |

**Table 9.2.5.1-7: Void**

**Table 9.2.5.1-8: Void**

**Table 9.2.5.1-9: Time period for PSS/SSS detection, CC with deactivated SCell (FR1), when *highSpeedMeasCA-Scell-r17* is configured**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | Ceil(5 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320 ms |  Ceil(5 x Kp) x max(measCycleSCell, M2 Note 1xDRX cycle) x CSSFintra |
| DRX cycle> 320 ms | Ceil(5 x Kp) x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 |

**Table 9.2.5.1-10: Time period for time index detection, CC with deactivated SCell (FR1)，when *highSpeedMeasCA-Scell-r17* is configured**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra** |
| No DRX | Ceil(3 x Kp) x measCycleSCell x CSSFintra |
| DRX cycle≤ 320 ms |  Ceil(3 x Kp) x max(measCycleSCell, M2 Note 1xDRX cycle) x CSSFintra |
| DRX cycle> 320 ms | Ceil(3 x Kp)x max(measCycleSCell, DRX cycle) x CSSFintra |
| NOTE 1: M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1 |

**Table 9.2.5.1-11: Time period for PSS/SSS detection when *highSpeedMeasFlagFR2-r17* is configured, (FR2) when SMTC period ≤ 40ms**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | max(600 ms, ceil(M1Note 2 x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFintra |
| DRX cycle≤ 80 ms | max(600 ms, ceil(M1Note 2 x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFintra |
| 80 ms< DRX cycle≤ 320 ms | ceil(1.5x Mpss/sss\_sync\_w/o\_gaps Note 3 x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle) x CSSFintra |
| DRX cycle>320 ms | ceil(Mpss/sss\_sync\_w/o\_gaps Note 3 x Kp x Klayer1\_measurement) x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: For UE supporting power class 6, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2NOTE 3: Void |

**Table 9.2.5.1-12: Time period for PSS/SSS detection, CC with deactivated PSCell (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | Ceil(5 x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320 ms | Ceil(5 x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320 ms | Ceil(5 x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

**Table 9.2.5.1-13: Time period for PSS/SSS detection, CC with deactivated PSCell (FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320 ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320 ms | Ceil(Mpss/sss\_sync\_w/o\_gaps x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

**Table 9.2.5.1-14: Time period for time index detection, CC with deactivated PSCell (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra** |
| No DRX | Ceil(3 x Kp) x measCyclePSCell x CSSFintra |
| DRX cycle≤ 320 ms | Ceil(3 x Kp) x max(measCyclePSCell, 1.5xDRX cycle) x CSSFintra |
| DRX cycle> 320 ms | Ceil(3 x Kp) x max(measCyclePSCell, DRX cycle) x CSSFintra |

**Table 9.2.5.1-15: Time period for time index detection (Frequency range FR2-2)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra** |
| No DRX | max(200 ms, ceil(MSSB\_index\_intra x Kp x SMTC period) x CSSFintra |
| DRX cycle≤ 320 ms | max(200 ms, ceil(1.5 x MSSB\_index\_intra x Kp) x max(SMTC period, DRX cycle) x CSSFintra) |
| DRX cycle>320 ms | Ceil(MSSB\_index\_intra x Kp )x DRX cycle x CSSFintra |

**Table 9.2.5.1-16: Void**

**Table 9.2.5.1-17: Time period for PSS/SSS detection for UE indicating *no-gap-with-interruption*, (Frequency range FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | max( 600 ms, 5 x max (80 ms, SMTC period ))Note 1 x CSSFintra |
| [DRX cycle≤ 320 ms] | max( 600 ms, ceil(M2 Note 2x 5) x [max(80 ms, SMTC period,DRX cycle)]) x CSSFintra |
| [DRX cycle>320 ms] | 5 x [DRX cycle x] CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms;,otherwise M2=1.NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 4: When *highSpeedMeasCA-Scell-r17* is configured and UE supports *measurementEnhancementCA-r17*, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1.NOTE 5: Requirements only apply when measurement gap is not configured, or measurement gap is fully non-overlapped with SMTC on any carrier on which UE indicates *no-gap-with-interruption*. |

**Table 9.2.5.1-18: Time period for PSS/SSS detection for UE indicating *no-gap-with-interruption*, (Frequency range FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | max(600 ms, ceil(Mpss/sss\_sync\_w/o\_gaps x KFR x Klayer1\_measurement)x max (80 ms, SMTC period ))Note 1 x CSSFintra |
| [DRX cycle≤ 320 ms] | max(600 ms, ceil(1.5 x Mpss/sss\_sync\_w/o\_gaps x KFR x Klayer1\_measurement)x [max(80 ms, SMTC period,DRX cycle)]) x CSSFintra |
| [DRX cycle>320 ms] | [ceil(Mpss/sss\_sync\_w/o\_gaps x KFR x Klayer1\_measurement) x DRX cycle] x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: KFR is a scaling factor depending on the frequency range and the SSB SCS. For FR2-1, KFR = 1.NOTE 3: Requirements only apply when measurement gap is not configured, or measurement gap is fully non-overlapped with SMTC on any carrier on which UE indicates *no-gap-with-interruption*. |

**Table 9.2.5.1-19: Time period for time index detection for UE indicating *no-gap-with-interruption* (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra** |
| No DRX | max(120 ms, 3x max (80 ms, SMTC period ))Note 1 x CSSFintra |
| [DRX cycle≤ 320 ms] | max(120 ms, ceil (M2 Note 2 x 3) x [max(80 ms, SMTC period,DRX cycle)]) x CSSFintra |
| [DRX cycle>320 ms] | 3 x DRX cycle x CSSFintra |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms;,otherwise M2=1NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 4: When *highSpeedMeasCA-Scell-r17* is configured and UE supports *measurementEnhancementCA-r17*, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1NOTE 5: Requirements only apply when measurement gap is not configured, or measurement gap is fully non-overlapped with SMTC on any carrier on which UE indicates *no-gap-with-interruption*. |

Editor’s note: RAN4 has to decide the UE behaviour when DRX is condifured whether interruptions are allowed.

**Table 9.2.5.1-20: Void**

**Table 9.2.5.1-21: Void**

**Table 9.2.5.1-22: Void**

**Table 9.2.5.1-23: Time period for time index detection, cell with 12 PRB SSB (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra\_less\_than\_5Mhz** |
| No DRX | max(120 ms, ceil(7 x Kp) x SMTC period)  |
| DRX cycle≤ 320 ms | max(120ms, ceil(M2 x 7 x Kp) x max(SMTC period,DRX cycle)) |
| DRX cycle>320 ms | ceil(7 x Kp) x DRX cycle  |
| NOTE 1: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2=1.NOTE 2: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16*. |

<Change #2>

9.2.6 Intra-frequency measurements with measurement gaps

9.2.6.1 Void

9.2.6.2 Intra-frequency cell identification

When a measurement gap is provided or an activated Pre-MG is provided without any pre-MG status changed during the measurement period, the UE shall be able to identify a new detectable intra-frequency cell within Tidentify\_intra\_without\_index if UE is not indicated to report SSB based RRM measurement result with the associated SSB index (*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured), or the UE has been indicated that the neighbour cell is synchronous with the serving cell (*deriveSSB-IndexFromCell* is enabled). Otherwise UE shall be able to identify a new detectable intra-frequency cell within Tidentify\_intra\_with\_index. The UE shall be able to identify a new detectable intra-frequency SS block of an already detected cell within Tidentify\_intra\_without\_index. It is assumed that *deriveSSB-IndexFromCell* is always enabled for FR1 TDD and FR2 with SCS smaller or equal to 480 kHz.

 Tidentify\_intra\_without\_index = TPSS/SSS\_sync\_intra + TSSB\_measurement\_period\_intra ms

 Tidentify\_intra\_with\_index = TPSS/SSS\_sync\_ntra + TSSB\_measurement\_period\_intra + TSSB\_time\_index\_intra ms

Where:

 TPSS/SSS\_sync\_intra: it is the time period used in PSS/SSS detection given in tables 9.2.6.2-1, 9.2.6.2-2 or 9.2.6.2-9.

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC ≤ 40ms, TPSS/SSS\_sync\_intra is given in table 9.2.6.2-9; otherwise, TPSS/SSS\_sync\_intra is given in table 9.2.6.2-2.

 TSSB\_time\_index\_intra: it is the time period used to acquire the index of the SSB being measured given in table 9.2.6.2-3 or 9.2.6.2-10 (for FR2-2).

- For UE indicating *support3MHz-ChannelBW-Symmetric-r18* and configured to operate on a target cell with 12 PRB SSB in FR1, TSSB\_time\_index\_intra\_less\_than\_5Mhz is given in table 9.2.6.2-12.

 TSSB\_measurement\_period\_intra: equal to a measurement period of SSB based measurement given in table 9.2.6.3-1 or 9.2.6.3-2.

- For UE supporting power class 6 with *highSpeedMeasFlagFR2-r17* configured, if SMTC ≤ 40ms, TSSB\_measurement\_period\_intra is given in table 9.2.6.3-4; otherwise, T SSB\_measurement\_period\_intra is given in table 9.2.6.3-2.

- For power class 6 UE supporting *measEnhCAInterFreqFR2-r18* when *highSpeedMeasFlagFR2-r17* is configured, the TSSB\_measurement\_period\_intra given in table 9.2.6.3-4 (if SMTC ≤ 40ms) and table 9.2.6.3-2 (if SMTC > 40ms) shall apply for SCC.

 CSSFintra: it is a carrier specific scaling factor and is determined according to CSSFwithin\_gap,i in clause 9.1.5.2 for measurement conducted within measurement gaps.

 Kgap is the scaling factor for a SSB frequency layer to be measured within an associated measurement gap pattern. Kgap = 1 when the UE is not configured with or the UE does not support concurrent GAPs or MUSIM gaps. Otherwise, Kgap = Ntotal / Navailable, where Navailable and Ntotal are calculated as follows:

 For a window W of duration max(SMTC period, xRP\_max), where xRP\_max is the maximum xRP across all configured per-UE GAPs, periodic MUSIM gaps and per-FR GAPs within the same FR as the SSB frequency layer, and starting from the beginning of any SMTC occasion:

- Ntotal is the total number of SMTC occasions that are covered by instances of the associated measurement gap within the window W, including those overlapped with other GAP and MUSIM gap occasions within the window, and

 Navailable is the number of SMTC occasions that are covered by instances of the non-dropped associated measurement gap within the window W after accounting for GAP and MUSIM gap collisions by applying the collision rules for GAP and MUSIM gap in clauses 9.1.8.3, 9.1.10.4, 9.1.10.5, 9.1.12.3, and 9.1.13.3, respectively.

- xRP = MGRP when configured GAP is activated Pre-MG or MG, and xRP = VIRP when configured GAP is NCSG, also xRP = MGRP for periodic MUSIM gap.

 When concurrent GAPs are configured, requirements in this clause do not apply if Navailable =0.

When UE supports [*MUSIM-GapConfig-17*] and the configured aperiodic MUSIM gap collides with the measurement gap associated with the target frequency layer, where MUSIM gap collision rule in clause 9.1.10.4 is applied, longer cell identification period for the target intra-frequency is expected.

 Mpss/sss\_sync\_with\_gaps : For a UE supporting FR2-1 power class 1 or 5, Mpss/sss\_sync with\_gaps=40. For a UE supporting FR2-1 power class 2, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2-1 power class 3, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2-1 power class 4, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2-1 power class 6, Mpss/sss\_sync with\_gaps =24. For a UE supporting FR2-2 power class 1, Mpss/sss\_sync with\_gaps = 60. For a UE supporting FR2-2 power class 2, Mpss/sss\_sync with\_gaps = 36. For a UE supporting FR2-2 power class 3, Mpss/sss\_sync with\_gaps = 36.

 Mmeas\_period\_ with\_gaps: For a UE supporting FR2-1 power class 1 or 5, Mmeas\_period\_ with\_gaps =40. For a UE supporting FR2-1 power class 2, Mmeas\_period\_ with\_gaps =24. For a UE supporting FR2-1 power class 3, Mmeas\_period\_ with\_gaps =24. For a UE supporting FR2-1 power class 4, Mmeas\_period with\_gaps =24. For a UE supporting FR2-1 power class 6, Mmeas\_period with\_gaps =24. For a UE supporting FR2-2 power class 1, Mmeas\_period\_ with\_gaps = 60. For a UE supporting FR2-2 power class 2, Mmeas\_period\_ with\_gaps = 36. For a UE supporting FR2-2 power class 3, Mmeas\_period\_ with\_gaps = 36.

- MSSB\_index\_intra: For a UE supporting FR2-2 power class 1, MSSB\_index\_intra = 72. For a UE supporting FR2-2 power class 2, MSSB\_index\_intra = 48. For a UE supporting FR2 power class 3, MSSB\_index\_intra = 48.

If the higher layer signaling in TS 38.331 [2] of *smtc2* is present and *smtc1* is fully overlapping with measurement gaps and *smtc2* is partially overlapping with measurement gaps, requirements are not specified for Tidentify\_intra\_without\_index or Tidentify\_intra\_with\_index.

If MCG DRX is in use, cell identification requirements for intra-frequency measurement in MCG specified in table 9.2.6.2-1, table 9.2.6.2-2, and table 9.2.6.2-3 shall depend on the MCG DRX cycle. If SCG DRX is in use, cell identification requirements for intra-frequency measurement in SCG specified in table 9.2.6.2-1, table 9.2.6.2-2, and table 9.2.6.2-3 shall depend on the SCG DRX cycle. Otherwise, the requirements for when DRX is not in use shall apply.

**Table 9.2.6.2-1: Time period for PSS/SSS detection (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | max(600 ms, 5 x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320 ms | max(600 ms, ceil(M2Note 1x 5 x Kgap) x max(MGRP, SMTC period,DRX cycle)) x CSSFintra |
| DRX cycle>320 ms | Ceil( 5 x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1.NOTE 2: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 3: For a UE supporting concurrent GAPs, if multiple concurrent GAPs are configured, the MGRP is the periodicity of the activated Pre-MG or the MG pattern associated to the intra-frequency layer.NOTE 4: When *highSpeedMeasCA-Scell-r17* is configured, the requirements apply to UE on measurements of secondary component carrier with active SCell. |

**Table 9.2.6.2-2: Time period for PSS/SSS detection (FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | max(600 ms, Mpss/sss\_sync\_with\_gaps x KFR x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320 ms | max(600 ms, ceil(1.5x Mpss/sss\_sync\_with\_gaps x KFR x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| DRX cycle>320 ms | Ceil(Mpss/sss\_sync\_with\_gaps x KFR x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent GAPs, if multiple concurrent GAPs are configured, the MGRP is the periodicity of the activated Pre-MG or the MG pattern associated to the intra-frequency layer.NOTE 2: KFR is a scaling factor depending on the frequency range and the SSB SCS. For FR2-1, KFR = 1. For FR2-2: KFR = 1 if the SCS of the SSB of the cell being detected is 120 kHz, KFR = 2 if the SCS of the SSB of the cell being detected is 480 kHz, and KFR = 3 if the SCS of the SSB of the cell being detected is 960 kHz. |

**Table 9.2.6.2-3: Time period for time index detection (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra** |
| No DRX | max(120 ms, ceil(3 x Kgap ) x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320 ms | max(120 ms, ceil(M2Note 1x 3 x Kgap) x max(MGRP, SMTC period,DRX cycle) x CSSFintra) |
| DRX cycle>320 ms | Ceil(3 x Kgap )x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1.NOTE 2: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16* on measurements of the primary component carrier and do not apply to measurements of a secondary component carrier with active SCell.NOTE 3: For a UE supporting concurrent GAPs, if multiple concurrent GAPs are configured, the MGRP is the periodicity of the activated Pre-MG or the MG pattern associated to the intra-frequency layer.NOTE 4: When *highSpeedMeasCA-Scell-r17* is configured, the requirements apply to UE on measurements of secondary component carrier with active SCell. |

**Table 9.2.6.2-7: Void**

**Table 9.2.6.2-8: Void**

**Table 9.2.6.2-8: Void**

**Table 9.2.6.2-9: Time period for PSS/SSS detection when *highSpeedMeasFlagFR2-r17* is configured, (FR2) when SMTC period ≤ 40ms**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_intra** |
| No DRX | max(600 ms, M1Note 2 x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 80 ms | max(600 ms, ceil(M1Note2 x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| 80 ms< DRX cycle≤ 320 ms | max(600 ms, ceil(Mpss/sss\_sync\_with\_gaps x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFintra |
| DRX cycle>320 ms | Ceil( Mpss/sss\_sync\_with\_gaps x Kgap ) x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: For a UE supporting concurrent GAPs, if multiple concurrent GAPs are configured, the MGRP is the periodicity of the activated Pre-MG or the MG pattern associated to the intra-frequency layer.NOTE 2: For UE supporting power class 6, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2NOTE 3: Void |

**Table 9.2.6.2-10: Time period for time index detection (FR2-2)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra** |
| No DRX | max(200 ms, ceil(MSSB\_index\_intra x Kgap x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320 ms | max(200 ms, ceil(1.5 x MSSB\_index\_intra x Kgap) x max(MGRP, SMTC period, DRX cycle) x CSSFintra) |
| DRX cycle>320 ms | Ceil(MSSB\_index\_intra x Kgap)x DRX cycle x CSSFintra |

**Table 9.2.6.2-11: Void**

**Table 9.2.6.2-12: Time period for time index detection, cell with 12 PRB SSB (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_intra\_less\_than\_5Mhz** |
| No DRX | max(120 ms, 7 x max(MGRP, SMTC period)) x CSSFintra |
| DRX cycle≤ 320 ms | max(120ms, ceil(M2 x 7) x max(MGRP, SMTC period,DRX cycle) x CSSFintra) |
| DRX cycle>320 ms | 7 x max(MGRP, DRX cycle) x CSSFintra |
| NOTE 1: VoidNOTE 2: When *highSpeedMeasFlag-r16* is not configured, M2 = 1.5; When *highSpeedMeasFlag-r16* is configured, M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1.NOTE 3: When *highSpeedMeasFlag-r16* is configured, the requirements apply only to UE supporting either *measurementEnhancement-r16* or *intraNR-MeasurementEnhancement-r16*. |

<Change #3>

9.3.4 Inter-frequency measurement with measurement gaps

When measurement gaps are provided, or the UE supports capability of conducting such measurements without gaps, the UE shall be able to identify a new detectable inter-frequency cell within Tidentify\_inter\_without\_index if UE is not indicated to report SSB based RRM measurement result with the associated SSB index (*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured) or *deriveSSB-IndexFromCellInter-r17* is configured for the FR1 and FR2-1 target frequency layers and UE supporting *deriveSSB-IndexFromCellInterNon-NCSG-r17*. Otherwise UE shall be able to identify a new detectable inter-frequency cell within Tidentify\_inter\_with\_index. The UE shall be able to identify a new detectable inter-frequency SS block of an already detected cell within Tidentify\_inter\_without\_index.

Tidentify\_inter\_without\_index = (TPSS/SSS\_sync\_inter + TSSB\_measurement\_period\_inter) ms

Tidentify\_inter\_with\_index = (TPSS/SSS\_sync\_inter + TSSB\_measurement\_period\_inter + TSSB\_time\_index\_inter) ms

Where:

 TPSS/SSS\_sync\_inter: it is the time period used in PSS/SSS detection given in table 9.3.4-1, table 9.3.4-2, table 9.3.4-5 when *highSpeedMeasInterFreq-r17* is configured and UE supports measurementEnhancementInterFreq-r17 and table 9.3.4-9 when *highSpeedMeasFlagFR2-r17* is configured and UE supports *measEnhCAInterFreqFR2-r18*. When the SCG is deactivated, table 9.3.4-7 applies for an inter-frequency carrier configured by SCG and not configured by MCG and table 9.3.4-2 applies for an inter-frequency carrier configured by both SCG and MCG. Regardless of whether the SCG is activated or deactivated, table 9.3.4-2 applies for an inter-frequency carrier configured only by MCG.

- For UE supporting power class 6 and *measEnhCAInterFreqFR2-r18* with *highSpeedMeasFlagFR2-r17* configured, if SMTC ≤ 40ms, TPSS/SSS\_sync\_inter is given in table 9.3.4-9; otherwise, TPSS/SSS\_sync\_inter is given in table 9.3.4-2.

 TSSB\_time\_index\_inter: it is the time period used to acquire the index of the SSB being measured given in table 9.3.4-3, table 9.3.4-6 when *highSpeedMeasInterFreq* is configured and UE supports measurementEnhancementInterFreq-r17, and table 9.3.4-10 when *highSpeedMeasFlagFR2-r17* is configured and UE supports *measEnhCAInterFreqFR2-r18*. When the SCG is deactivated, table 9.3.4-8 applies for an inter-frequency carrier configured by SCG and not configured by MCG and table 9.3.4-4 applies for an inter-frequency carrier configured by both SCG and MCG. Regardless of whether the SCG is activated or deactivated, table 9.3.4-4 applies for an inter-frequency carrier configured only by MCG.

- For UE supporting power class 6 and *measEnhCAInterFreqFR2-r18* with *highSpeedMeasFlagFR2-r17* configured, if SMTC ≤ 40ms, TSSB\_measurement\_period\_inter is given in table 9.3.5-5; otherwise, TSSB\_measurement\_period\_inter is given in table 9.3.5-2.

 TSSB\_measurement\_period\_inter: equal to a measurement period of SSB based measurement given in table 9.3.5-1, table 9.3.5-2, table 9.3.5-3 when *highSpeedMeasInterFreq* is configured and UE supports measurementEnhancementInterFreq-r17, and in table 9.3.5-5 when *highSpeedMeasFlagFR2-r17* is configured and UE supports *measEnhCAInterFreqFR2-r18*. When the SCG is deactivated, table 9.3.5-4 applies for an inter-frequency carrier configured by SCG and not configured by MCG and table 9.3.5-2 applies for an inter-frequency carrier configured by both SCG and MCG. Regardless of whether the SCG is activated or deactivated, table 9.3.5-2 applies for an inter-frequency carrier configured only by MCG.

- For UE indicating *support3MHz-ChannelBW-Symmetric-r18* and configured to operate on a target cell with 12 PRB SSB in FR1, TSSB\_time\_index\_intra\_less\_than\_5Mhz is given in table 9.3.4-11.

- For UE supporting power class 6 and *measEnhCAInterFreqFR2-r18* with *highSpeedMeasFlagFR2-r17* configured, TSSB\_measurement\_period\_inter is given in table 9.3.5-5; otherwise, TSSB\_measurement\_period\_inter is given in table 9.3.5-2.

 Mpss/sss\_sync\_inter: For a UE supporting FR2-1 power class 1 or 5, Mpss/sss\_sync\_inter = 64 samples. For a UE supporting FR2-1 power class 2, Mpss/sss\_sync\_inter = 40 samples. For a UE supporting FR2-1 power class 3, Mpss/sss\_sync\_inter = 40 samples. For a UE supporting FR2-1 power class 4, Mpss/sss\_sync\_inter = 40 samples. For a UE supporting FR2-2 power class 1, Mpss/sss\_sync\_inter = 96. For a UE supporting FR2-2 power class 2, Mpss/sss\_sync\_inter = 60. For a UE supporting FR2-2 power class 3, Mpss/sss\_sync\_inter = 60.

 MSSB\_index\_inter: For a UE supporting FR2-1 power class 1 or 5, MSSB\_index\_inter = 40 samples. For a UE supporting FR2 power class 2, MSSB\_index\_inter = 24 samples. For a UE supporting FR2-1 power class 3, MSSB\_index\_inter = 24 samples. For a UE supporting FR2-1 power class 4, MSSB\_index\_inter = 24 samples. For a UE supporting FR2-2 power class 2 or 3, MSSB\_index\_inter = 48 samples. For a UE supporting FR2 power class 1, MSSB\_index\_inter = 72 samples.

 Mmeas\_period\_inter: For a UE supporting FR2-1 power class 1 or 5, Mmeas\_period\_inter =64. For a UE supporting FR2-1 power class 2, Mmeas\_period\_inter=40. For a UE supporting FR2-1 power class 3, Mmeas\_period\_inter =40. For a UE supporting FR2-1 power class 4, Mmeas\_period\_inter = 40. For a UE supporting FR2-2 power class 1, Mmeas\_period\_inter = 96. For a UE supporting FR2-2 power class 2, Mmeas\_period\_inter = 60. For a UE supporting FR2-2 power class 3, Mmeas\_period\_inter = 60.

 CSSFinter: it is a carrier specific scaling factor and is determined according to CSSFwithin\_gap,i in clause 9.1.5.2 for measurement conducted within measurement gaps.

 Kgap is a scaling factor for a SSB frequency layer to be measured within an associated measurement gap pattern. Kgap = 1 when the UE is not configured with concurrent GAPs or MUSIM gaps. Otherwise, Kgap = Ntotal / Navailable, where Navailable and Ntotal are calculated as follows:

- For a window W of duration max(SMTC period, xRP\_max), where xRP\_max is the maximum xRP across all configured per-UE measurement GAPs, periodic MUSIM gaps, and/or per-FR measurement GAPs within the same FR, and starting from the beginning of any SMTC occasion:

- Ntotal is the total number of SMTC occasions that are covered by instances of the associated measurement gap within the window W, including those overlapped with other GAP occasions and MUSIM gap occasions within the window, and

- Navailable is the number of SMTC occasions that are covered by instances of the non-dropped associated measurement gap within the window W, after accounting for measurement GAP and MUSIM gap collisions by applying the collision rules for the GAP and MUSIM gap in clauses 9.1.8.3, 9.1.10.5, 9.1.12.3, and 9.1.13.3, respectively.

- xRP = MGRP when configured GAP is activated Pre-MG or MG, and xRP = VIRP when configured GAP is NCSG, also xRP = MGRP for periodic MUSIM gap.

 Kgap is only applicable for UE supporting concurrent GAPs or MUSIM gaps. When concurrent GAPs or MUSIM gaps are configured, requirements in this clause do not apply if Navailable =0.

When UE supports [*musim-GapPreference-r17*] and if the configured aperiodic MUSIM gap collides with the measurement gap associated with the target frequency layer, where MUSIM gap collision rule in clause 9.1.10.4 is applied, longer cell identification period for the target inter-frequency is expected.

**Table 9.3.4-1: Time period for PSS/SSS detection (FR1)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TPSS/SSS\_sync\_inter** |
| No DRX |  Max(600 ms, Ceil(8 \* Kgap) × Max(MGRP, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320 ms | Max(600 ms, Ceil(8\*1.5 \* Kgap) × Max(MGRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320 ms | Ceil(8 \* Kgap) × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. The DRX cycle is the DRX cycle of the secondary cell group.NOTE 3: For a UE supporting concurrent GAPs, the MGRP above is the MGRP of the activated Pre-MG or the measurement gap associated with the target frequency layer to be measured if concurrent GAPs are configured. |

**Table 9.3.4-2: Time period for PSS/SSS detection, (FR2)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TPSS/SSS\_sync\_inter** |
| No DRX | Max(600 ms, Ceil(Kgap × Mpss/sss\_sync\_inter x KFR) × Max(MGRP, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320 ms | Max(600 ms, Ceil(1.5 \* Kgap × Mpss/sss\_sync\_inter x KFR) × Max(MGRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320 ms | Ceil(Kgap × Mpss/sss\_sync\_inter x KFR) × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. The DRX cycle is the DRX cycle of the secondary cell group.NOTE 3: For a UE supporting concurrent GAPs, the MGRP above is the MGRP of the activated Pre-MG or the measurement gap associated with the target frequency layer to be measured if concurrent GAPs are configured.NOTE 4: KFR is a scaling factor depending on the frequency range and the SSB SCS. For FR2-1, KFR = 1. For FR2-2: KFR = 1 if the SCS of the SSB of the cell being detected is 120 kHz, KFR = 2 if the SCS of the SSB of the cell being detected is 480 kHz, and KFR = 3 if the SCS of the SSB of the cell being detected is 960 kHz. |

**Table 9.3.4-3: Time period for time index detection (FR1)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TSSB\_time\_index\_inter** |
| No DRX | Max(120 ms, Ceil(3 \* Kgap)× Max(MGRP, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320 ms | Max(120 ms, Ceil(3 × 1.5 \* Kgap) × Max(MGRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320 ms | Ceil(3 \* Kgap)× DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. The DRX cycle is the DRX cycle of the secondary cell group.NOTE 3: For a UE supporting concurrent GAPs, the MGRP above is the MGRP of the activated Pre-MG or the measurement gap associated with the target frequency layer to be measured if concurrent GAPs are configured. |

**Table 9.3.4-4: Time period for time index detection (FR2)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TSSB\_time\_index\_inter** |
| No DRX | Max(200 ms, Ceil(Kgap × MSSB\_index\_inter)× Max(MGRP, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320 ms | Max(200 ms, Ceil(1.5 \* Kgap × MSSB\_index\_inter) × Max(MGRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320 ms | Ceil(Kgap ×MSSB\_index\_inter) × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. The DRX cycle is the DRX cycle of the secondary cell group.NOTE 3: For a UE supporting concurrent GAPs, the MGRP above is the MGRP of the activated Pre-MG or the measurement gap associated with the target frequency layer to be measured if concurrent GAPs are configured. |

**Table 9.3.4-5: Time period for PSS/SSS detection when highSpeedMeasInterFreq-r17 is configured (FR1)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TPSS/SSS\_sync\_inter** |
| No DRX | max(600 ms, N1 × Max(MGRP, SMTC period)) × CSSFinterN1 = 7 |
| DRX cycle ≤ 160 ms | max(600 ms, ceil(N2) x max(MGRP, SMTC period, DRX cycle)) x CSSFinterN2 = 7 x M2 |
| 160 ms < DRX cycle ≤ 320 ms | ceil(N3) x DRX cycle x CSSFinterN3 = 7 x M2 |
| DRX cycle>320 ms | N4 x DRX cycle x CSSFinter |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1NOTE 3: N4=6 if SMTC periodicity > 40 ms, otherwise N4=5 |

**Table 9.3.4-6: Time period for time index detection when *highSpeedMeasInterFreq-r17* is configured (FR1)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TSSB\_time\_index\_inter** |
| No DRX | Max(120 ms, 3 × Max(MGRP, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320 ms | Max(120 ms, Ceil(3 × M2 NOTE3) × Max(MGRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320 ms | 3 × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. The DRX cycle is the DRX cycle of the secondary cell group.NOTE 3: M2 = 1.5 if SMTC periodicity > 40 ms, otherwise M2=1. |

**Table 9.3.4-7: Time period for PSS/SSS detection when the inter-frequency carrier is configured only by SCG and the SCG is deactivated (FR2)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TPSS/SSS\_sync\_inter** |
| No DRX | Max(600 ms, Ceil(Kgap × Mpss/sss\_sync\_inter) × Max(MGRP, measCyclePSCell)) × CSSFinter |
| DRX cycle ≤ 320 ms | Max(600 ms, Ceil(1.5 \* Kgap × Mpss/sss\_sync\_inter) × Max(MGRP, measCyclePSCell, DRX cycle)) × CSSFinter |
| DRX cycle > 320 ms | Ceil(Kgap × Mpss/sss\_sync\_inter) × Max(measCyclePSCell, DRX cycle) × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1. The DRX cycle is the DRX cycle of the secondary cell group.NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. NOTE 3: For a UE supporting concurrent GAPs, the MGRP above is the MGRP of the activated Pre-MG or the measurement gap associated with the target frequency layer to be measured if concurrent GAPs are configured. |

**Table 9.3.4-8: Time period for time index detection when inter-frequency carrier is configured only by SCG and the SCG is deactivated (FR2)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TSSB\_time\_index\_inter** |
| No DRX | Max(200 ms, Ceil(Kgap × MSSB\_index\_inter)× Max(MGRP, measCyclePSCell)) × CSSFinter |
| DRX cycle ≤ 320 ms | Max(200 ms, Ceil(1.5 \* Kgap × MSSB\_index\_inter) × Max(MGRP, measCyclePSCell, DRX cycle)) × CSSFinter |
| DRX cycle > 320 ms | Ceil(Kgap ×MSSB\_index\_inter) × Max(measCyclePSCell, DRX cycle) × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1. NOTE 2: In EN-DC operation, the parameters, timers and scheduling requests referred to in clause 3.6.1 are for the secondary cell group. The DRX cycle is the DRX cycle of the secondary cell group.NOTE 3: For a UE supporting concurrent GAPs, the MGRP above is the MGRP of the activated Pre-MG or the measurement gap associated with the target frequency layer to be measured if concurrent GAPs are configured. |

**Table 9.3.4-9: Time period for PSS/SSS detection when *highSpeedMeasFlagFR2-r17* is configured, (FR2-1) when SMTC period ≤40ms**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_inter** |
| No DRX | max(600 ms, M1Note 3 x Kgap x max(MGRP, SMTC period)) x CSSFinter |
| DRX cycle≤ 80 ms | max(600 ms, ceil(M1Note 3 x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFinter |
| 80 ms< DRX cycle≤ 320 ms | max(600 ms, ceil(Mpss/sss\_sync\_with\_gaps x Kgap) x max(MGRP, SMTC period, DRX cycle))x CSSFinter |
| DRX cycle>320 ms | Ceil( Mpss/sss\_sync\_with\_gaps x Kgap ) x max(MGRP, DRX cycle) x CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: For a UE supporting concurrent GAPs, the MGRP above is the MGRP of the activated Pre-MG or the measurement gap associated with the target frequency layer to be measured if concurrent GAPs are configured.NOTE 3: For UE supporting power class 6 and *measEnhCAInterFreqFR2-r18*, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2 |

**Table 9.3.4-10: Time period for time index detection when when *highSpeedMeasFlagFR2-r17* is configured (Frequency range FR2-1) when SMTC period <= 40 ms**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TSSB\_time\_index\_inter** |
| No DRX | Max(200 ms, Ceil(Kgap × , M1Note 3)× Max(MGRP, SMTC period)) × CSSFinter |
| DRX cycle≤ 80 ms | Max(200 ms, Ceil(1.5 \* Kgap × M1Note 3) × Max(MGRP, SMTC period, DRX cycle)) × CSSFinter |
| 80 ms< DRX cycle≤ 320 ms | Max(200 ms, Ceil(1.5 \* Kgap × MSSB\_index\_inter) × Max(MGRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320 ms | Ceil(Kgap ×MSSB\_index\_inter) × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: For a UE supporting concurrent GAPs, the MGRP above is the MGRP of the activated Pre-MG or the measurement gap associated with the target frequency layer to be measured if concurrent GAPs are configured.NOTE 3: For UE supporting power class 6 and *measEnhCAInterFreqFR2-r18*, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2 |

**Table 9.3.4-11: Time period for time index detection for a UE, target cell with 12 PRB SSB (FR1)**

|  |  |
| --- | --- |
| **Condition NOTE1** | **TSSB\_time\_index\_inter\_less\_than\_5Mhz** |
| No DRX | Max(120 ms, Ceil(6 \* Kgap)× Max(MGRP, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320 ms | Max(120ms, Ceil(6 × M2 \* Kgap) × Max(MGRP, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320 ms | Ceil(6\* Kgap)× DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: When *highSpeedMeasInterFreq-r17* is not configured, M2 = 1.5; When *highSpeedMeasInterFreq-r17* is configured, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2 = 1. |

<Change #4>

9.3.9 Inter-frequency measurements without measurement gaps

9.3.9.1 Inter-frequency Cell identification

UE satisfying the applicability conditions specified in clause 9.3.1 on the requirement in this clause shall be able to identify a new detectable inter-frequency cell within Tidentify\_inter\_without\_index if UE is not indicated to report SSB based RRM measurement result with the associated SSB index (*reportQuantityRsIndexes* or *maxNrofRSIndexesToReport* is not configured) or *deriveSSB-IndexFromCellInter-r17* is configured for the FR1 and FR2-1 target frequency layers and UE supporting *deriveSSB-IndexFromCellInterNon-NCSG-r17*. Otherwise UE shall be able to identify a new detectable inter-frequency cell within Tidentify\_inter\_with\_index. The UE shall be able to identify a new detectable inter-frequency SS block of an already detected cell within Tidentify\_inter\_without\_index.

- For inter-frequency SSB based measurements without measurement gaps in active BWP, it is assumed that when UE performs inter-frequency measurements without measurement gaps in a TDD bands on FR1 and FR2, SFN and frame boundary across serving cell and inter-frequency neighbor cells is aligned

 Tidentify\_inter\_without\_index = (TPSS/SSS\_sync\_inter + TSSB\_measurement\_period\_inter) ms

 Tidentify\_inter\_with\_index = (TPSS/SSS\_sync\_inter + TSSB\_measurement\_period\_inter + TSSB\_time\_index\_inter) ms

Where:

 TPSS/SSS\_sync\_inter: it is the time period used in PSS/SSS detection

- For inter-frequency SSB based measurements without measurement gaps in active BWP, and UE supports *interFrequencyMeas-Nogap-r16*, TPSS/SSS\_sync\_inter is given in table 9.3.9.1-1 and table 9.3.9.1-2.

- For UE supporting measEnhCAInterFreqFR2-r18:

- If *highSpeedMeasFlagFR2-r17* is configured, and SMTC ≤ 40ms, TPSS/SSS\_sync\_inter is given in table 9.3.9.1-6.

- If *highSpeedMeasFlagFR2-r17* is configured, and SMTC >40ms, TPSS/SSS\_sync\_inter is given in table 9.3.9.1-2.

- If *highSpeedMeasFlagFR2-r17* is not configured, TPSS/SSS\_sync\_inter is given in table 9.3.9.1-2.

- For UE indicating *no-gap-no-interruption*, TPSS/SSS\_sync\_inter is given in table 9.3.9.1-1 for FR1 and table 9.3.9.1-2 for FR2

- For UE indicating *no-gap-with-interruption*, TPSS/SSS\_sync\_inter is given in table 9.3.9.1-1a for FR1 and table 9.3.9.1-2a for FR2.

- TSSB\_time\_index\_inter: it is the time period used to acquire the index of the SSB being measured

- For inter-frequency SSB based measurements without measurement gaps in active BWP, and UE supports *interFrequencyMeas-Nogap-r16*, TSSB\_time\_index\_inter is given in table 9.3.9.1-3 and table 9.3.9.1-4.

- For UE supporting measEnhCAInterFreqFR2-r18:

- If *highSpeedMeasFlagFR2-r17* is configured, and SMTC ≤ 40ms, TSSB\_time\_index\_inter is given in table 9.3.9.1-7.

- If *highSpeedMeasFlagFR2-r17* is configured, and SMTC >40ms, TSSB\_time\_index\_inter is given in table 9.3.9.1-4.

- If *highSpeedMeasFlagFR2-r17* is not configured, TSSB\_time\_index\_inter is given in table 9.3.9.1-4.

- For UE *indicating no-gap-no-interruption*, TSSB\_time\_index\_inter is given in table 9.3.9.1-3 for FR1 and table 9.3.9.1-4 for FR2

- For UE indicating *no-gap-with-interruption*, TSSB\_time\_index\_inter is given in table 9.3.9.1-3a for FR1 and table 9.3.9.1-4a for FR2.

- For UE indicating *support3MHz-ChannelBW-Symmetric-r18* and configured to operate on a target cell with 12 PRB SSB in FR1, TSSB\_time\_index\_intra\_less\_than\_5MHz is given in table 9.3.9.1-5.

- TSSB\_measurement\_period\_inter: equal to a measurement period of SSB based measurement

- For inter-frequency SSB based measurements without measurement gaps in active BWP, and UE supports *interFrequencyMeas-Nogap-r16*, TSSB\_measurement\_period\_inter is given in table 9.3.9.2-1, table 9.3.9.2-2, table 9.3.9.2-3 and table 9.3.9.2-3a when *highSpeedMeasInterFreq-r17* is configured and UE supports *measurementEnhancementInterFreq-r17*, and table 9.3.9.2-4 when *highSpeedMeasFlagFR2-r17* is configured and UE supports *measEnhCAInterFreqFR2-r18*.

- For UE indicating *no-gap-no-interruption*, TSSB\_measurement\_period\_inter is given in table 9.3.9.2-1 for FR1, table 9.3.9.2-2 for FR2, and table 9.3.9.2-3 when *highSpeedMeasInterFreq-r17* is configured and UE supports *measurementEnhancementInterFreq-r17*.

- For UE indicating *no-gap-with-interruption*, TSSB\_measurement\_period\_inter is given in table 9.3.9.2-1a for FR1 and table 9.3.9.2-2a for FR2, and table 9.3.9.2-3b when *highSpeedMeasInterFreq-r17* is configured and UE supports *measurementEnhancementInterFreq-r17*.

- For UE supporting power class 6 and *measEnhCAInterFreqFR2-r18* with *highSpeedMeasFlagFR2-r17* configured, if SMTC ≤ 40ms, TSSB\_measurement\_period\_inter is given in table 9.3.9.2-x; otherwise, TSSB\_measurement\_period\_inter is given in table 9.3.9.2-2.

 CSSFinter: it is a carrier specific scaling factor and is determined according to CSSFoutside\_gap,i in clause 9.1.5.1 for measurement conducted outside GAP, i.e. when inter-frequency SMTC is fully non overlapping or partially overlapping with GAPs.

- when inter-frequency SMTC is fully non overlapping or partially overlapping with measurement gaps for UE *indicating no-gap-no-interruption* or

- when inter-frequency SMTC is fully non overlapping with measurement gaps for UE indicating *no-gap-with-interruption*.

For inter-frequency SSB based measurements without measurement gaps in active BWP

 Mpss/sss\_sync\_inter: For a UE supporting FR2-1 power class 1 or 5, Mpss/sss\_sync\_inter = 40. For a UE supporting FR2-1 power class 2, Mpss/sss\_sync\_inter = 24. For a UE supporting FR2-1 power class 3, Mpss/sss\_sync\_inter = 24. For a UE supporting FR2-1 power class 4, Mpss/sss\_sync = 24. For a UE supporting FR2-2 power class 1, Mpss/sss\_sync\_inter = 60. For a UE supporting FR2-2 power class 2, Mpss/sss\_sync\_inter = 36. For a UE supporting FR2-2 power class 3, Mpss/sss\_sync\_inter = 36. For FR1, Mpss/sss\_sync\_inter = 5.

 MSSB\_index\_inter: For a UE supporting FR2-2 power class 1, MSSB\_index\_inter = 72. For a UE supporting FR2-2 power class 2, MSSB\_index\_inter = 48. For a UE supporting FR2-2 power class 3, MSSB\_index\_inter = 48. For FR1, MSSB\_index\_inter = 3.

 Mmeas\_period\_inter: For a UE supporting FR2-1 power class 1 or 5, Mmeas\_period\_inter = 40. For a vehicle mounted UE supporting FR2-1 power class 2, Mpss/sss\_sync\_inter=24. For a UE supporting FR2-1 power class 3, Mmeas\_period\_inter = 24. For a UE supporting FR2-1 power class 4, Mmeas\_period\_inter = 24. For a UE supporting FR2-2 power class 1, Mmeas\_period\_inter = 60. For a UE supporting FR2-2 power class 2, Mpss/sss\_sync\_inter = 36. For a UE supporting FR2-2 power class 3, Mmeas\_period\_inter = 36. For FR1, Mmeas\_period\_inter = 5.

If the UE indicates ‘nogap-noncsg’ via *NeedForGapNCSG-InfoNR* for the inter-frequency measurement or the UE indicates either *no-gap-with-interruption* or *no-gap-no-interruption* via *NeedForInterruptionInfoNR-r18*,

 Mpss/sss\_sync\_inter: For a UE supporting FR2-1 power class 1 or 5, Mpss/sss\_sync\_inter = 64 samples. For a UE supporting FR2-1 power class 2, Mpss/sss\_sync\_inter = 40 samples. For a UE supporting FR2-1 power class 3, Mpss/sss\_sync\_inter = 40 samples. For a UE supporting FR2-1 power class 4, Mpss/sss\_sync\_inter = 40 samples. For a UE supporting FR2-2 power class 1, Mpss/sss\_sync\_inter = 96. For a UE supporting FR2-2 power class 2, Mpss/sss\_sync\_inter = 60. For a UE supporting FR2-2 power class 3, Mpss/sss\_sync\_inter = 60. For FR1, Mpss/sss\_sync\_inter = 8.

 MSSB\_index\_inter: For a UE supporting FR2-1 power class 1 or 5, MSSB\_index\_inter = 40 samples. For a UE supporting FR2 power class 2, MSSB\_index\_inter = 24 samples. For a UE supporting FR2-1 power class 3, MSSB\_index\_inter = 24 samples. For a UE supporting FR2-1 power class 4, MSSB\_index\_inter = 24 samples. For a UE supporting FR2-2 power class 2 or 3, MSSB\_index\_inter = 48 samples. For a UE supporting FR2 power class 1, MSSB\_index\_inter = 72 samples. For FR1, MSSB\_index\_inter = 3.

 Mmeas\_period\_inter: For a UE supporting FR2-1 power class 1 or 5, Mmeas\_period\_inter =64. For a UE supporting FR2-1 power class 2, Mmeas\_period\_inter=40. For a UE supporting FR2-1 power class 3, Mmeas\_period\_inter =40. For a UE supporting FR2-1 power class 4, Mmeas\_period\_inter = 40. For a UE supporting FR2-2 power class 1, Mmeas\_period\_inter = 96. For a UE supporting FR2-2 power class 2, Mmeas\_period\_inter = 60. For a UE supporting FR2-2 power class 3, Mmeas\_period\_inter = 60. For FR1, Mmeas\_period\_inter = 8.

When UE supports *concurrentMeasGap-r17* or *musim-GapPreference-r17* or both concurrent measurement GAP and *musim-GapPreference-r17* and the UE is configured with concurrent GAPs or periodic MUSIM gaps or both concurrent gaps and periodic MUSIM gaps,

Kp is a scaling factor for an SSB frequency layer to be measured without GAP. Kp = Ntotal / Navailable, where Navailable and Ntotal are calculated as follows:

 For a window W of duration max(SMTC period, xRP\_max), where xRP\_max is the maximum xRP across all configured per-UE GAPs, periodic MUSIM gaps, and per-FR GAPs within the same FR as the SSB frequency layer, and starting at the beginning of any SMTC occasion:

 Ntotal is the total number of SMTC occasions within the window, including those overlapped with GAP and MUSIM gap occasions within the window, and

 Navailable is the number of SMTC occasions that are not overlapped with any non-dropped GAP or non-dropped MUSIM gap occasions within the window W, after accounting for GAP and MUSIM gap collisions by applying the collision rules for the measurement GAP and MUSIM gap in clauses 9.1.8.3, 9.1.10.4 and 9.1.10.5, respectively.

- xRP = MGRP when configured GAP is activated Pre-MG or MG, and xRP = VIRP when configured GAP is NCSG, also xRP = MGRP for periodic MUSIM gap.

 Kp = 1 when Navailable = 0.

 Requirements in this clause do not apply when Navailable = 0 due to fully overlapping between SMTC occasions and MUSIM gap occasions within the window W.

Editor Note: FSS for the case when Navailable = 0 due to fully overlapping between SMTC occasions and the union of MUSIM gap and measurement gap occasions within the window W.

When UE supports [*musim-GapPreference-r17]* and the SMTC occasions of the target frequency layer is fully or partially overlapping with the configured aperiodic MUSIM gap, longer cell identification period for the target frequency layer is expected.

Otherwise, when UE is not configured with or UE does not support concurrent GAPs and the UE is not configured with MUSIM gaps:

 When inter-frequency SMTC is fully non overlapping with measurement gaps or NCSG, or interfrequency SMTC is fully overlapping with MGs or NCSG, Kp =1.

 When inter-frequency SMTC is partially overlapping with measurement gaps, Kp = 1/(1- (SMTC period /MGRP)), where SMTC period < MGRP. When inter-frequency SMTC is partially overlapping with NCSG, Kp = 1/(1- (SMTC period /VIRP)), where SMTC period < VIRP.

For FR2,

 Klayer1\_measurement=1,

- if all of the reference signals configured for RLM, BFD, CBD or L1-RSRP for beam reporting on any FR2 serving frequency in the same band outside measurement gap are not fully overlapped by inter-frequency SMTC occasions, or

- if all of the reference signal configured for RLM, BFD, CBD or L1-RSRP for beam reporting on any FR2 serving frequency in the same band outside measurement gap and fully-overlapped by inter-frequency SMTC occasions are not overlapped with any of the SSB symbols and the RSSI symbols, and 1 symbol before each consecutive SSB symbols and the RSSI symbols, and 1 symbol after each consecutive SSB symbols and the RSSI symbols, given that *SSB-ToMeasure* and *SS-RSSI-Measurement* are configured, where SSB symbols are indicated by *SSB-ToMeasure* and RSSI symbols are indicated by *SS-RSSI-Measurement*;

 Klayer1\_measurement=1.5, otherwise.

 If the above-mentioned reference signal configured for L1-RSRP measurement is aperiodic CSI-RS resource, longer cell identification delay would be expected.

For calculation of Kp, if the high layer signalling (TS 38.331 [2]) of *smtc2* is configured, for cells indicated in the *pci-List* parameter in *smtc2*, the SMTC periodicity corresponds to the value of higher layer parameter *smtc2*; for the other cells, the SMTC periodicity corresponds to the value of higher layer parameter *smtc1.*

**Table 9.3.9.1-1: Time period for PSS/SSS detection, (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_inter** |
| No DRX | max( 600 ms, ceil(Mpss/sss\_sync\_inter x Kp) x SMTC period )Note 1 x CSSFinter |
| DRX cycle≤ 320 ms | max( 600 ms, ceil(M2x Mpss/sss\_sync\_inter x Kp) x max(SMTC period,DRX cycle)) x CSSFinter |
| DRX cycle>320 ms | ceil(Mpss/sss\_sync\_inter x Kp) x DRX cycle x CSSFinter |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: VoidNOTE 3: When *highSpeedMeasInterFreq-r17* is not configured, M2 = 1.5; When *highSpeedMeasInterFreq-r17* is configured, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2 = 1 |

**Table 9.3.9.1-1a: Time period for PSS/SSS detection, when UE indicate *no-gap-with-interruption* (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_inter** |
| No DRX | max( 600 ms, Mpss/sss\_sync\_inter x max(80 ms, SMTC period) )Note 1 x CSSFinter |
| [DRX cycle≤ 320 ms] | max( 600 ms, ceil(M2 x Mpss/sss\_sync\_inter) x max(80 ms, SMTC period,DRX cycle)) x CSSFinter |
| [DRX cycle>320 ms] | Mpss/sss\_sync\_inter x DRX cycle x CSSFinter |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: VoidNOTE 3: When *highSpeedMeasInterFreq-r17* is not configured, M2 = 1.5; When *highSpeedMeasInterFreq-r17* is configured, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2 = 1 |

**Table 9.3.9.1-2: Time period for PSS/SSS detection, (FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_inter** |
| No DRX | max(600 ms, ceil(Mpss/sss\_sync\_inter x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFinter |
| DRX cycle≤ 320 ms | max(600 ms, ceil(1.5 x Mpss/sss\_sync\_inter x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFinter |
| DRX cycle>320 ms | ceil(Mpss/sss\_sync\_inter x Kp x Klayer1\_measurement) x DRX cycle x CSSFinter |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: Void |

**Table 9.3.9.1-2a: Time period for PSS/SSS detection, when UE indicate no-gap-with-interruption (FR2)**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_inter** |
| No DRX | max(600 ms, ceil(Mpss/sss\_sync\_inter x Klayer1\_measurement)x max(80 ms, SMTC period))Note 1 x CSSFinter |
| DRX cycle≤ 320 ms | max(600ms, ceil(1.5 x Mpss/sss\_sync\_inter x Klayer1\_measurement)x max(80ms,SMTC period, DRX cycle)) x CSSFinter |
| DRX cycle>320 ms | ceil(Mpss/sss\_sync\_inter x Klayer1\_measurement) x DRX cycle x CSSFinter |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: Void |

**Table 9.3.9.1-3: Time period for time index detection (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_inter** |
| No DRX | max(120 ms, ceil(MSSB\_index\_inter x Kp )x SMTC period)Note 1 x CSSFinter |
| DRX cycle≤ 320 ms | max(120 ms, ceil (M2 x MSSB\_index\_inter x Kp) x max(SMTC period,DRX cycle)) x CSSFinter |
| DRX cycle>320 ms | Ceil(MSSB\_index\_inter x Kp) x DRX cycle x CSSFinter |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: VoidNOTE 3: When *highSpeedMeasInterFreq-r17* is not configured, M2 = 1.5; When *highSpeedMeasInterFreq-r17* is configured, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2 = 1 |

**Table 9.3.9.1-3a: Time period for time index detection, when UE indicate *no-gap-with-interruption* (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_inter** |
| No DRX | max(120 ms, MSSB\_index\_inter x max(80 ms, SMTC period))Note 1 x CSSFinter |
| DRX cycle≤ 320 ms | max(120 ms, ceil (M2 x MSSB\_index\_inter) x [max(80 ms, SMTC period, DRX cycle)]) x CSSFinter |
| DRX cycle>320 ms | MSSB\_index\_inter x DRX cycle x CSSFinter |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: VoidNOTE 3: When *highSpeedMeasInterFreq-r17* is not configured, M2 = 1.5; When *highSpeedMeasInterFreq-r17* is configured, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2 = 1 |

**Table 9.3.9.1-4: Time period for time index detection (FR2)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TSSB\_time\_index\_inter** |
| No DRX | Max(200 ms, Ceil(MSSB\_index\_inter x Kp)× SMTC period) × CSSFinter |
| DRX cycle ≤ 320 ms | Max(200 ms, Ceil(1.5 × MSSB\_index\_inter x Kp) × Max(SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320 ms | Ceil(MSSB\_index\_inter x Kp) × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: Kp is applicable for UE supporting *concurrentMeasGap-r17* |

**Table 9.3.9.1-4a: ime period for time index detection, when UE indicate *no-gap-with-interruption* (FR2)**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TSSB\_time\_index\_inter** |
| No DRX | Max(200 ms, MSSB\_index\_inter × max(80 ms, SMTC period)) × CSSFinter |
| DRX cycle ≤ 320 ms | Max(200 ms, Ceil(1.5 × MSSB\_index\_inter) × Max(80 ms, SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle > 320 ms | MSSB\_index\_inter × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: Kp is applicable for UE supporting [concurrent gaps] and MUSIM gaps |

**Table 9.3.9.1-5: Time period for time index detection, target cell with 12 PRB SSB (FR1)**

|  |  |
| --- | --- |
| **DRX cycle** | **TSSB\_time\_index\_inter\_less\_than\_5 MHz** |
| No DRX | max(120 ms, ceil( 6 x Kp )x SMTC period)Note 1 x CSSFinter |
| DRX cycle≤ 320 ms | max(120 ms, ceil (6 x Kp) x max(SMTC period,DRX cycle)) x CSSFinter |
| DRX cycle>320 ms | Ceil( 6 x Kp) x DRX cycle x CSSFinter |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: When *highSpeedMeasInterFreq-r17* is not configured, M2 = 1.5; When *highSpeedMeasInterFreq-r17* is configured, M2 = 1.5 if SMTC periodicity > 40 ms; otherwise M2 = 1. |

**Table 9.3.9.1-6: Time period for PSS/SSS detection when *highSpeedMeasFlagFR2-r17* is configured (FR2-1) when SMTC period ≤ 40ms**

|  |  |
| --- | --- |
| **DRX cycle** | **TPSS/SSS\_sync\_inter** |
| No DRX | max(600 ms, ceil(M1 Note 2 x Kp x Klayer1\_measurement)x SMTC period)Note 1 x CSSFinter |
| DRX cycle≤ 80 ms | max(600 ms, ceil(M1 Note 2 x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFinter |
| 80 ms< DRX cycle≤ 320 ms | max(600 ms, ceil(1.5 x Mpss/sss\_sync\_inter x Kp x Klayer1\_measurement)x max(SMTC period,DRX cycle)) x CSSFinter |
| DRX cycle>320 ms | ceil(Mpss/sss\_sync\_inter x Kp x Klayer1\_measurement) x DRX cycle x CSSFinter |
| NOTE 1: If different SMTC periodicities are configured for different cells, the SMTC period in the requirement is the one used by the cell being identifiedNOTE 2: For UE supporting power class 6 and *measEnhCAInterFreqFR2-r18*, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2 |

**Table 9.3.9.1-7: Time period for time index detection when *highSpeedMeasFlagFR2-r17* is configured (FR2-1) when SMTC period ≤ 40ms**

|  |  |
| --- | --- |
| **Condition NOTE1,2** | **TSSB\_time\_index\_inter** |
| No DRX | Max(200 ms, Ceil(M1 Note 3 x Kp)× SMTC period) × CSSFinter |
| DRX cycle≤ 80 ms | Max(200 ms, Ceil(M1 Note 3 x Kp) × Max(SMTC period, DRX cycle)) × CSSFinter |
| 80 ms< DRX cycle≤ 320 ms | Max(200 ms, Ceil(1.5 × MSSB\_index\_inter x Kp) × Max(SMTC period, DRX cycle)) × CSSFinter |
| DRX cycle>320 ms | Ceil(MSSB\_index\_inter x Kp) × DRX cycle × CSSFinter |
| NOTE 1: DRX or non DRX requirements apply according to the conditions described in clause 3.6.1NOTE 2: Kp is applicable for UE supporting *concurrentMeasGap-r17*NOTE 3: For UE supporting power class 6 and *measEnhCAInterFreqFR2-r18*, M1= 6 if *highSpeedMeasFlagFR2-r17* = set1 or M1= 18 if *highSpeedMeasFlagFR2-r17* = set2 |

<End Of Changes>