3GPP TSG-RAN WG3 Meeting #129bis R3-257294

**Prague, Czech Republic, October 13-17, 2025**

Agenda Item: 13.2

Source: ZTE Corporation

Title: TP to TR 38.765 for Network Architecture

Document for: Agreement

# Introduction

In this contribution, we introduce the logical architecture for ISAC based on the online agreement and offline discussion in CB:# 21\_ISAC.

# TP to TR 38.765

# 7 Network architecture

*Editor’s note: This section is to capture the study outcome of network architecture. Applicability to gNB bistatic sensing may be considered as part of this network architecture without additional architecture impacts. No inter-gNB coordination will be studied.*

>>>>>>>>>>Start of Change<<<<<<<<<<

This clause attempts to identify and describe the logical architecture to support the sensing in the overall 5G system architecture.

Figure 7.1 depicts a logical architecture for ISAC, where the Nx interface is between the gNB and the SF.



Figure 7.1 Logical architecture for ISAC

This logical architecture is independent of the transport, e.g. direct or via the AMF, between the gNB and the SF.

*Editor’s Note: The details of "Nx" interface need further discussion and decision.*

>>>>>>>>>>End of Change<<<<<<<<<<