3GPP TSG RAN WG2#129bis R2-250xxxx

Wuhan, China, 7th - 11th, April, 2025

**Title:** [Draft] LS on byte-aligned SDU to CT1

**Response to:** -

**Release:** Rel-19

**Work Item:** Ambient\_IoT\_solutions

**Source:** vivo [to be RAN2]

**To:** CT1

**Cc:** RAN1, SA2

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**Attachments:** -

# 1 Overall description

RAN2 has discussed the AIoT MAC PDU format design, and RAN2 has made the following agreements:

**Agreements on MAC PDU format**

1. The MAC PDU should be byte-aligned, assuming the allocated TBS value is in the unit of byte. The actual TBS value depends on RAN1. FFS for R2D trigger message
2. RAN2 assumes that the upper layer data SDU is byte-aligned, and an LS can be sent to CT1.
3. The D2R MAC PDU size will correspond to the TBS size indicated in the R2D message
4. The MAC padding is supported at least for D2R from RAN2 perspective. The device includes padding bits if there is no more data and there is still space available in the TBS.
5. In case where MAC PDU includes both MAC SDU and padding, for D2R a field to indicate how many SDU bits are present is required. FFS how this is provided (i.e. SDU length field or padding length field). The size of length field is FFS.

RAN2 would like to inform CT1 about RAN2’s assumption that the upper layer data SDU contained in the AIoT MAC PDU is byte-aligned.

# 2 Actions

**To CT1:**

**ACTION:** RAN2 respectfully asks CT1 to take into account RAN2’s assumption that upper layer data SDU is byte-aligned and provide feedback if there is any concern.

# 3 Dates of next TSG RAN WG2 meetings

TSG RAN2 Meeting #130 19 - 23 May 2025 Malta, EU

TSG RAN2 Meeting #131 25 - 29 Aug 2025 Bangalore, India