**3GPP TSG-RAN WG2 Meeting #116 electronic R2-2109657**

**Online, Nov. 1st – Nov. 12th, 2021**

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| *CR-Form-v12.1* |
| **CHANGE REQUEST** |
|  |
|  | 38.321 | **CR** |  | **rev** | - | **Current version:** | 16.6.0 |  |
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| *For* [***HE******LP***](http://www.3gpp.org/3G_Specs/CRs.htm#_blank)*on using this form: comprehensive instructions can be found at* [*http://www.3gpp.org/Change-Requests*](http://www.3gpp.org/Change-Requests)*.* |
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| ***Proposed change affects:*** | UICC apps |  | ME | **X** | Radio Access Network | **X** | Core Network |  |

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|  |
| ***Title:***  | Introduction of TRS based SCell activation |
|  |  |
| ***Source to WG:*** | OPPO |
| ***Source to TSG:*** | R2 |
|  |  |
| ***Work item code:*** |  |  | ***Date:*** | 2021-10-20 |
|  |  |  |  |  |
| ***Category:*** | B |  | ***Release:*** | *Rel-17* |
|  | *Use one of the following categories:****F*** *(correction)****A*** *(mirror corresponding to a change in an earlier release)****B*** *(addition of feature),* ***C*** *(functional modification of feature)****D*** *(editorial modification)*Detailed explanations of the above categories canbe found in 3GPP [TR 21.900](http://www.3gpp.org/ftp/Specs/html-info/21900.htm). | *Use one of the following releases:Rel-8 (Release 8)Rel-9 (Release 9)Rel-10 (Release 10)Rel-11 (Release 11)…Rel-15 (Release 15)Rel-16 (Release 16)Rel-17 (Release 17)Rel-18 (Release 18)* |
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| ***Reason for change:*** | Introduction of TRS based SCell activation |
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| ***Summary of change:*** | Define new MAC CE inlcuding both SCell activation and TRS activation with two LCID for “one octet” SCell activation indication and “four octet” SCell activation indication respectively. |
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| ***Consequences if not approved:*** | The feature of TRS based SCell activation is missing. |
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| ***Clauses affected:*** | 3.2, 5.9, 6.1.3.x(new), 6.2.1 |
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|  | **Y** | **N** |  |  |
| ***Other specs*** | **X** |  |  Other core specifications  | TS 38.331 CR ...  |
| ***affected:*** |  | **X** |  Test specifications | TS/TR ... CR ...  |
| ***(show related CRs)*** |  | **X** |  O&M Specifications | TS/TR ... CR ...  |
|  |  |
| ***Other comments:*** |  |
|  |  |
| ***This CR's revision history:*** |  |

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| The begin of changes |

## 3.2 Abbreviations

For the purposes of the present document, the abbreviations given in TR 21.905 [1] and the following apply. An abbreviation defined in the present document takes precedence over the definition of the same abbreviation, if any, in TR 21.905 [1].

AP Aperiodic

BFR Beam Failure Recovery

BSR Buffer Status Report

BWP Bandwidth Part

CE Control Element

CG Cell Group

CI-RNTI Cancellation Indication RNTI

CSI Channel State Information

CSI-IM CSI Interference Measurement

CSI-RS CSI Reference Signal

CS-RNTI Configured Scheduling RNTI

DAPS Dual Active Protocol Stack

DCP DCI with CRC scrambled by PS-RNTI

DL-PRS DownLink-Positioning Reference Signal

IAB Integrated Access and Backhaul

INT-RNTI Interruption RNTI

LBT Listen Before Talk

LCG Logical Channel Group

LCP Logical Channel Prioritization

MCG Master Cell Group

MPE Maximum Permissible Exposure

NUL Normal Uplink

NZP CSI-RS Non-Zero Power CSI-RS

PDB Packet Delay Budget

PHR Power Headroom Report

PS-RNTI Power Saving RNTI

PTAG Primary Timing Advance Group

QCL Quasi-colocation

RS Reference Signal

SCG Secondary Cell Group

SFI-RNTI Slot Format Indication RNTI

SI System Information

SL-RNTI Sidelink RNTI

SLCS-RNTI Sidelink Configured Scheduling RNTI

SpCell Special Cell

SP Semi-Persistent

SP-CSI-RNTI Semi-Persistent CSI RNTI

SPS Semi-Persistent Scheduling

SR Scheduling Request

SS Synchronization Signals

SSB Synchronization Signal Block

STAG Secondary Timing Advance Group

SUL Supplementary Uplink

TAG Timing Advance Group

TCI Transmission Configuration Indicator

TPC-SRS-RNTI Transmit Power Control-Sounding Reference Signal-RNTI

TRS Tracking Reference Signal

UCI Uplink Control Information

V2X Vehicle-to-Everything

ZP CSI-RS Zero Power CSI-RS

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## 5.9 Activation/Deactivation of SCells with/without TRS activation

If the MAC entity is configured with one or more SCells, the network may activate and deactivate the configured SCells. Upon configuration of an SCell, the SCell is deactivated unless the parameter *sCellState* is set to *activated* for the SCell by upper layers.

The configured SCell(s) is activated and deactivated by:

- receiving the SCell Activation/Deactivation MAC CE described in clause 6.1.3.10;

- receiving the SCell Activation/Deactivation MAC CE described in clause 6.1.3.x;

- configuring *sCellDeactivationTimer* timer per configured SCell (except the SCell configured with PUCCH, if any): the associated SCell is deactivated upon its expiry;

- configuring *sCellState* per configured SCell: if configured, the associated SCell is activated upon SCell configuration.

The MAC entity shall for each configured SCell:

1> if an SCell is configured with *sCellState* set to *activated* upon SCell configuration, or an SCell Activation/Deactivation MAC CE is received activating the SCell:

2> if the SCell was deactivated prior to receiving this SCell Activation/Deactivation MAC CE; or

2> if the SCell is configured with *sCellState* set to *activated* upon SCell configuration:

3> if *firstActiveDownlinkBWP-Id* is not set to dormant BWP:

4> activate the SCell according to the timing defined in TS 38.213 [6] for MAC CE activation and according to the timing defined in TS 38.133 [11] for direct SCell activation; i.e. apply normal SCell operation including:

5> SRS transmissions on the SCell;

5> CSI reporting for the SCell;

5> PDCCH monitoring on the SCell;

5> PDCCH monitoring for the SCell;

5> PUCCH transmissions on the SCell, if configured.

3> else (i.e. *firstActiveDownlinkBWP-Id* is set to dormant BWP):

4> stop the *bwp-InactivityTimer* of this Serving Cell, if running.

3> activate the DL BWP and UL BWP indicated by *firstActiveDownlinkBWP-Id* and *firstActiveUplinkBWP-Id* respectively.

2> start or restart the *sCellDeactivationTimer* associated with the SCell according to the timing defined in TS 38.213 [6] for MAC CE activation and according to the timing defined in TS 38.133 [11] for direct SCell activation;

2> if the active DL BWP is not the dormant BWP:

3> (re-)initialize any suspended configured uplink grants of configured grant Type 1 associated with this SCell according to the stored configuration, if any, and to start in the symbol according to rules in clause 5.8.2;

3> trigger PHR according to clause 5.4.6.

1> else if an SCell Activation/Deactivation MAC CE is received deactivating the SCell; or

1> if the *sCellDeactivationTimer* associated with the activated SCell expires:

2> deactivate the SCell according to the timing defined in TS 38.213 [6];

2> stop the *sCellDeactivationTimer* associated with the SCell;

2> stop the *bwp-InactivityTimer* associated with the SCell;

2> deactivate any active BWP associated with the SCell;

2> clear any configured downlink assignment and any configured uplink grant Type 2 associated with the SCell respectively;

2> clear any PUSCH resource for semi-persistent CSI reporting associated with the SCell;

2> suspend any configured uplink grant Type 1 associated with the SCell;

2> flush all HARQ buffers associated with the SCell;

2> cancel, if any, triggered consistent LBT failure for the SCell.

1> if PDCCH on the activated SCell indicates an uplink grant or downlink assignment; or

1> if PDCCH on the Serving Cell scheduling the activated SCell indicates an uplink grant or a downlink assignment for the activated SCell; or

1> if a MAC PDU is transmitted in a configured uplink grant and LBT failure indication is not received from lower layers; or

1> if a MAC PDU is received in a configured downlink assignment:

2> restart the *sCellDeactivationTimer* associated with the SCell.

1> if the SCell is deactivated:

2> not transmit SRS on the SCell;

2> not report CSI for the SCell;

2> not transmit on UL-SCH on the SCell;

2> not transmit on RACH on the SCell;

2> not monitor the PDCCH on the SCell;

2> not monitor the PDCCH for the SCell;

2> not transmit PUCCH on the SCell.

1> if an SCell Activation/Deactivation MAC CE with TRS activation is received activating the SCells and also activating the TRS:

2> indicate to lower layers the information regarding theTRS activation in this MAC CE.;

HARQ feedback for the MAC PDU containing SCell Activation/Deactivation with/without TRS activation MAC CE shall not be impacted by PCell, PSCell and PUCCH SCell interruptions due to SCell activation/deactivation or TRS activation in TS 38.133 [11].

When SCell is deactivated, the ongoing Random Access procedure on the SCell, if any, is aborted.

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### 6.1.3 MAC Control Elements (CEs)

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#### 6.1.3.x SCell Activation/Deactivation with TRS activation MAC CEs

The SCell Activation/Deactivation with TRS activation MAC CE of one octet is identified by a MAC subheader with LCID as specified in Table 6.2.1-1. It consists of an octet for SCell A/D part containing seven C-fields and one R-field and FFS for TRS activation. The SCell Activation/Deactivation with TRS activation MAC CE with one octet SCell A/D part is defined as follows (Figure 6.1.3.x-1).

The SCell Activation/Deactivation with TRS activation MAC CE of four octets is identified by a MAC subheader with LCID as specified in Table 6.2.1-1. It consists of four octets for SCell A/D part containing 31 C-fields and one R-field and FFS for TRS activation. The SCell Activation/Deactivation MAC CE with TRS activation of four octets SCell A/D part is defined as follows (Figure 6.1.3.x-2).

Editor note: FFS the new MAC CE has fixed size or variable size.

- Ci: If there is an SCell configured for the MAC entity with *SCellIndex* i as specified in TS 38.331 [5], this field indicates the activation/deactivation status of the SCell with *SCellIndex* i, else the MAC entity shall ignore the Ci field. The Ci field is set to 1 to indicate that the SCell with *SCellIndex* i shall be activated. The Ci field is set to 0 to indicate that the SCell with *SCellIndex* i shall be deactivated;

- R: Reserved bit, set to 0.



Figure 6.1.3.x-1: SCell Activation/Deactivation with TRS activation MAC CE of one octet



Figure 6.1.3.x-2: SCell Activation/Deactivation with TRS activation MAC CE of four octets

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### 6.2.1 MAC subheader for DL-SCH and UL-SCH

The MAC subheader consists of the following fields:

- LCID: The Logical Channel ID field identifies the logical channel instance of the corresponding MAC SDU or the type of the corresponding MAC CE or padding as described in Tables 6.2.1-1 and 6.2.1-2 for the DL-SCH and UL-SCH respectively. There is one LCID field per MAC subheader. The size of the LCID field is 6 bits. If the LCID field is set to 34, one additional octet is present in the MAC subheader containing the eLCID field and follow the octet containing LCID field. If the LCID field is set to 33, two additional octets are present in the MAC subheader containing the eLCID field and these two additional octets follow the octet containing LCID field;

- eLCID: The extended Logical Channel ID field identifies the logical channel instance of the corresponding MAC SDU or the type of the corresponding MAC CE as described in tables 6.2.1-1a, 6.2.1-1b, 6.2.1-2a and 6.2.1-2b for the DL-SCH and UL-SCH respectively. The size of the eLCID field is either 8 bits or 16 bits.

NOTE: The extended Logical Channel ID space using two-octet eLCID and the relevant MAC subheader format is used, only when configured, on the NR backhaul links between IAB nodes or between IAB node and IAB Donor.

- L: The Length field indicates the length of the corresponding MAC SDU or variable-sized MAC CE in bytes. There is one L field per MAC subheader except for subheaders corresponding to fixed-sized MAC CEs, padding, and MAC SDUs containing UL CCCH. The size of the L field is indicated by the F field;

- F: The Format field indicates the size of the Length field. There is one F field per MAC subheader except for subheaders corresponding to fixed-sized MAC Ces, padding, and MAC SDUs containing UL CCCH. The size of the F field is 1 bit. The value 0 indicates 8 bits of the Length field. The value 1 indicates 16 bits of the Length field;

- R: Reserved bit, set to 0.

The MAC subheader is octet aligned.

Table 6.2.1-1 Values of LCID for DL-SCH

|  |  |
| --- | --- |
| Codepoint/Index | LCID values |
| 0 | CCCH |
| 1–32 | Identity of the logical channel |
| 33 | Extended logical channel ID field (two-octet eLCID field) |
| 34 | Extended logical channel ID field (one-octet eLCID field) |
| 35–44 | Reserved |
| 45 | SCell Activation/Deactivation with TRS activation (four octets SCell A/D part) |
| 46 | SCell Activation/Deactivation with TRS activation (one octet SCell A/D part) |
| 47 | Recommended bit rate |
| 48 | SP ZP CSI-RS Resource Set Activation/Deactivation |
| 49 | PUCCH spatial relation Activation/Deactivation |
| 50 | SP SRS Activation/Deactivation  |
| 51 | SP CSI reporting on PUCCH Activation/Deactivation |
| 52 | TCI State Indication for UE-specific PDCCH |
| 53 | TCI States Activation/Deactivation for UE-specific PDSCH |
| 54 | Aperiodic CSI Trigger State Subselection |
| 55 | SP CSI-RS/CSI-IM Resource Set Activation/Deactivation |
| 56 | Duplication Activation/Deactivation |
| 57 | SCell Activation/Deactivation (four octets) |
| 58 | SCell Activation/Deactivation (one octet) |
| 59 | Long DRX Command |
| 60 | DRX Command |
| 61 | Timing Advance Command |
| 62 | UE Contention Resolution Identity |
| 63 | Padding |

Table 6.2.1-1a Values of two-octet eLCID for DL-SCH

|  |  |  |
| --- | --- | --- |
| Codepoint | Index | LCID values |
| 0 to (216 – 1) | 320 to (216 + 319) | Identity of the logical channel |

Table 6.2.1-1b Values of one-octet eLCID for DL-SCH

|  |  |  |
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| Codepoint | Index | LCID values |
| 0 to 244 | 64 to 308 | Reserved |
| 245 | 309 | Serving Cell Set based SRS Spatial Relation Indication |
| 246 | 310 | PUSCH Pathloss Reference RS Update |
| 247 | 311 | SRS Pathloss Reference RS Update |
| 248 | 312 | Enhanced SP/AP SRS Spatial Relation Indication |
| 249 | 313 | Enhanced PUCCH Spatial Relation Activation/Deactivation |
| 250 | 314 | Enhanced TCI States Activation/Deactivation for UE-specific PDSCH |
| 251 | 315 | Duplication RLC Activation/Deactivation |
| 252 | 316 | Absolute Timing Advance Command |
| 253 | 317 | SP Positioning SRS Activation/Deactivation |
| 254 | 318 | Provided Guard Symbols |
| 255 | 319 | Timing Delta |

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| The end of changes |