**3GPP TSG-RAN WG2 Meeting #112-e R2-200xxx**

Online, 2-13 November 2020

Agenda Item: 6.8.4

Source: CMCC

Title: Summary of email discussion [Post111-e][918][R16 DCCA] SCell SMTC window for Unaligned CA

Document for: Discussion and Decision

# 1 Introduction

This document is for the following offline discussion, particularly for topics in 6.8.4:

* [Post111-e][918][R16 DCCA] SCell SMTC window for Unaligned CA (CMCC)

Discuss the problem and attempt to come up with a solution.

 Intended outcome: Email discussion report + CR (if needed)

 Deadline: Long

In RAN2 #111 meeting, there is a hot discussion on one issue related to SCell slot slit for unaligned CA which had been identified and discussed in RAN1 and the potential RAN2 impact of the issue, triggered by [1]:

** Issue 1: Interpretation of slots indicated by the bitmap in measurementSlots in TS38.331 for unaligned CA case**

Moreover, in RAN# 89 meeting, the way forward in [2] as follows had been endorsed:

* How to handle the ambiguities, including SMTC window & DRX timing, for Case B is up to WGs

To progress the topic, this document provides questions with respect to the issue and potential RAN2 impact of the issue and companies are requested to provide their views on those.

# 2 Discussion

As descripted in [1] [3] [4], the issue comes from that when unaligned CA is applied, there will be slots partially overlapped with the SMTC window. For example, for Pcell=60kHz, SCell=15kHz, N = -1 (ca\_slot\_offset = -1), it is not clear whether slot 0 or slot 1 shown below (in red rectangle) should be the first slot in measurementSlots for the SCell. And in the below figure, SF means subframe, MO means measurement occasion; SMTC window is defined by PCell timing.



On the other hand, in TS 38.331 [3], the definition of “first slot in the SMTC window” is highlighted in yellow, then for unaligned CA case, it is not clear whether a partially overlapped slot with SMTC window should be treated as “a slot in the SMTC window”. Then to avoid ambiguity, some companies propose only complete slots inside the SMTC window are indicated by the bitmap in *measurementSlots.*



Regarding RSSI measurement, RSSI is a measured value not related to cell dimension, but rather an aggregated measured value from all cells in a specific frequency layer. Then, in the definition of SS reference signal received quality (SS-RSRQ) in TS38.215 [4], it is noted that:

***- For intra-frequency measurements, NR Carrier RSSI is measured with timing reference corresponding to the serving cell in the frequency layer***

This means a reference RSSI measurement timing based on one serving cell in this frequency layer needs to be determined. If there is only one SCell (no PCell) in this frequency layer, and the SCell has a slot offset compared to PCell, then this issue arises.

Additionally, with the definition of “*periodicityAndOffset”*in TS38.331 for SMTC configuration, the offset is subframe-based granularity, however, in the case of unaligned CA or slot-aligned CA, the offset between Pcell/PScell and Scellis is slot-based granularity, which will produce the case that [2] exemplified, however, even in non-CA case, such example may present between two TDD bands when their frame boundaries are not aligned, furtherly, it may present in asynchronized FDD system for inter-frequency measurement, therefore, RAN2 is required to handle this issue in a unified way.

Therefore, when unaligned CA is applied, there will be slots partially overlapped with the SMTC window.

**Question 1: For unaligned CA case, do you agree that it is not clear of the definition of “first slot in the SMTC window” in TS 38.331, i.e. whether a partially overlapped slot with SMTC window should not be treated as “a slot in the SMTC window” or not.**

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| **Company** | **Yes/No** | **Comment / alternative proposal** |
| CMCC | Yes | There is the ambiguity in the definition of the SMTC window requiring clarification. |
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Regarding RSSI measurement, a reference RSSI measurement timing based on one serving cell in this frequency layer needs to be determined.

**Question 2: Do you agree that the issue will arise if there is only one SCell (no PCell) in this frequency layer, and the SCell has a slot offset compared to PCell?**

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| **Company** | **Yes/No** | **Comment / alternative proposal** |
| CMCC | Yes |  |
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If you confirm the above issues, then please provide your answer the question 3:

**Question 3: Do you agree the following proposal:**

**For the IE *measurementSlots* defined in 38.331, only complete slots inside the SMTC window are indicated by the bitmap in *measurementSlots* in case of both intra- frequency measurement and inter-frequency measurement, and add the corresponding clarification for the slot bitmap interpretation of *measurementSlots* in TS38.331.**

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| **Company** | **Yes/No** | **Comment / alternative proposal** |
| CMCC | Yes | The clarification for the ambiguity is needed.  |
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# **3 Conclusion**

TBD

# 4 References

1. R2-2001627 Impact of CG/SPS with periodicities non dividing HF length Sequans Communications[R2-2008365](https://www.3gpp.org/ftp/TSG_RAN/WG2_RL2/TSGR2_111-e/Docs/R2-2008365.zip) Issue on SCell Slot Slit for Unaligned CA CMCC
2. RP-202030 Moderator’s Summary for Email Discussion [89E][12][R16\_DCCA\_unaligned\_frames], MediaTek

1. **R1-2007008** FL summary on support of unaligned frame boundary for R16 NR inter-band, CMCC
2. R1-2005626, Remaining issues on Rel-16 carrier aggregation, MediaTek Inc..
3. 3GPP TS 38.331 V16.0.0, ”NR; Radio Resource Control (RRC) protocol specification”
4. 3GPP TS 38.215 V16.2.0, ”NR; Physical layer measurements”

# **6 Proposals in summary contribution**