**3GPP TSG-RAN WG4 Meeting #116R4-2511777**

**Bangalore, India, 25th – 29th August 2025**

**Title:** WF on FDD PC2 duty cycle and measurement channel

**Agenda Item:** 4.2.2

**Source: Qualcomm Incorporated**

**Document for:** Approval

# PC2 FDD duty cycle and measurement channel

**Background:**

38.101-1 has a conflict: maximum output power and configured maximum output power requirements specify a power reduction for PC2 UEs when default duty cycle of 50% is exceeded, unless support for larger duty cycle is signalled. However, no RMC with reduced duty cycle is specified resulting in test condition being in conflict from core specification. à A specification update may be required to align core specification and testing.

On the other hand, there is also conflict between existing TS 38.101-1 and TR 38.861 regarding the applicability of duty cycle based SAR solution to FDD. The conclusion of TS 38.861 is that duty cycle solution is not consensus. à specification error correction in TS 38.101-1 and RAN2 capability description may be required.

**Way forward**: In future meetings to ensure ran4 specification is correct, choose one of the following alternatives

- **Option 1**: Introduce new RMC with up to 50% duty cycle for PC2 FDD, starting point is R4-2511382

- **Option 2**: Restrict duty cycle applicability only to TDD, changes needed in 6.2.1 and 6.2.4 and RAN2 capability descriptions. Inform RAN5 that test procedures shall ensure that UE maintains PC2 power level throughout the conformance tests.