Companies providing input to this email discussion are requested to leave contact information below.

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| **Company** | **Delegate name** | **Email address** |
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| ASUSTeK | Lider Pan | lider\_pan@asus.com |
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# 1 Comments on CR

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| **Company** | **Clause number** | **Original text in CR** | **Suggested modification or comments** | **Rapporteur response** |
| CATT | 6.2.2 | ***n3c-Suport***  This field indicates the support of N3C MP. If the field is present, the UE can perform early detection of candidate N3C relay UEs. If absent, a UE is not required to perform early detection of candidate N3C relay UEs. | ***n3c-Support***  This field indicates the support of N3C MP. If the field is present, the UE can perform early detection of candidate N3C relay UEs. If absent, a UE is not required to perform early detection of candidate N3C relay UEs. |  |
| CATT | 6.3.1 | t400-U2U-r18 ENUMERATED {ms200, ms400, ms600, ms800, ms1200, ms2000, ms3000, ms4000} OPTIONAL, -- Need M | t400-U2U-r18 ENUMERATED {ms200, ms400, ms600, ms800, ms1200, ms2000, ms3000, ms4000} OPTIONAL, -- Need R |  |
| CATT | 6.3.1  6.3.5 |  | For the new introdeced t400-U2U-r18, just wonder any procedure text’s addition is also needed? |  |
| ASUSTeK | 5.8.9.1.2 | 5.8.9.1.2 Actions related to transmission of *RRCReconfigurationSidelink* message  The UE shall set the contents of *RRCReconfigurationSidelink* message as follows:  1> for each sidelink DRB that is to be released, according to clause 5.8.9.1a.1.1, due to configuration by *sl-ConfigDedicatedNR,* *SIB12*, *SidelinkPreconfigNR*, by upper layers, or due to end-to-end sidelink release:  2> set the entryincluded in the *slrb-ConfigToReleaseList* corresponding to the sidelink DRB;  … | It seems to us that the term “end-to-end sidelink release” is not used at any other places in the RRC Spec. In our understanding, this should be “end-to-end sidelink DRB release”. |  |
| ASUSTeK | 5.8.9.1a.1.2 and  5.8.9.7.1 | 5.8.9.1a.1.2 Sidelink DRB release operations  …  1> if the sidelink radio link failure is detected for a specific destination; or  1> if the sidelink DRB release is triggered by end-to-end PC5 connection failure due to per-hop PC5 link failure, in accordance with clause 5.8.9.3a:  2> release the PDCP entity, RLC entity and the logical channel of the sidelink DRB for the specific destination.  …  5.8.9.7.1 PC5 Relay RLC channel release  …  1> if the PC5 Relay RLC channel release was triggered by end-to-end DRB release as specified in 5.8.9.1a.1.2; or  1> if the PC5 Relay RLC channel release was triggered by end-to-end DRB modification as specified in 5.8.9.1a.2.2:  2> if the PC5 Relay RLC channel release was triggered due to per-hop PC5 link failure; or  2> if the PC5 Relay RLC channel release was triggered after the reception of the *RRCReconfigurationSidelink* message; or  2> after receiving the *RRCReconfigurationCompleteSidelink* message, if the PC5 Relay RLC channel release was triggered due to the configuration received within the *sl-ConfigDedicatedNR*;  3> release the RLC entity and the corresponding logical channel associated with the end-to-end DRB;  … | It seems the RLC channel release due to **per-hop PC5 link failure** is performed twice in clauses 5.8.9.1a.1.2 and 5.8.9.7.1.  In our understanding, the cause of the PC5 Relay RLC channel release in clause 5.8.9.7.1 could be due to **end-to-end PC5 link failure** (including T400 expiry, integrity check failure, and end-to-end PC5 connection failure due to reception of *NotificationMessageSidelink*, as specified in clause 5.8.9.3a) instead of **per-hop PC5 link failure**.  Besides, as specified in clause 5.8.9.1.2, the sidelink RRC reconfiguration procedure is initiated due to end-to-end sidelink DRB release. Thus, we think the action of RLC channel release should be performed after receiving the *RRCReconfigurationCompleteSidelink* message.  If the above understandings are correct, the following modification is suggested:  5.8.9.7.1 PC5 Relay RLC channel release  …  1> if the PC5 Relay RLC channel release was triggered by end-to-end DRB release as specified in 5.8.9.1a.1.2; or  1> if the PC5 Relay RLC channel release was triggered by end-to-end DRB modification as specified in 5.8.9.1a.2.2:  2> after receiving the *RRCReconfigurationCompleteSidelink* message, if the PC5 Relay RLC channel release was triggered due to ~~per-hop~~end-to-end PC5 link failure; or  2> if the PC5 Relay RLC channel release was triggered after the reception of the *RRCReconfigurationSidelink* message; or  2> after receiving the *RRCReconfigurationCompleteSidelink* message, if the PC5 Relay RLC channel release was triggered due to the configuration received within the *sl-ConfigDedicatedNR*;  3> release the RLC entity and the corresponding logical channel associated with the end-to-end DRB;  … |  |
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# 2 Comments on RIL list

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| **Company** | **Suggested modification or comments** | **Rapporteur response** |
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