3GPP TSG RAN WG2 Meeting #119-e Draft R2-xxxx

**Electronic meeting, 17- 29 Aug, 2022**

**Agenda item:** 6.11.1

**Source:** Intel Corporation

**Title:** Report of [Post119-e][414][POS] LPP capability CR (Intel)

**Document for:**  Discussion and decision

# Introduction

This is the report of following email discussion:

* [Post119-e][414][POS] LPP capability CR (Intel)

Scope: Check and finalise the CR in R2-2208802

Intended outcome: Agreed CR

Deadline: Short (for RP)

# Annex: companies’ point of contact

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| **Company** | **Point of contact** | **Email address** |
| Intel Corporation | Yi Guo | Yi.guo@intel.com |
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# Discussion

The CR R2-2208802 has been updated:

**1 capture offline comments**

* “there are some -r16/-r17 and/or non-Italic items left in NR-UL-SRS-Capability, NR-Multi-RTT-MeasurementCapability.”

**2 Added/updated capability based on RAN1 feature list in R2-2209117/R1-2207923**

* a ppw-maxNumOfDL-BandwidthFR1-r17 ENUMERATED {mhz5, mhz10, mhz20, mhz40, mhz50, mhz80, mhz100},

ppw-maxNumOfDL-BandwidthFR2-r17 ENUMERATED {mhz50, mhz100, mhz200, mhz400}

* b clarify spatialRelationsSRS-PosRRC-Inactive is only applicable for FR2 as
* c Indicates whether the UE supports spatial relations for SRS for positioning in RRC\_INACTIVE state on FR2.

**3 Updated capability supportedDL-PRS-ProcessingSamples-RRC-Inactive-r17 based on RAN4 LS R4-2214489;**

* a dummify original supportedDL-PRS-ProcessingSamples-RRC-Inactive-r17 and add new supportedDL-PRS-ProcessingSamples-RRC-Inactive-r17 as per band

**Discussion point 3.1: Do you have any comments on the updated CR ?**

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| **Company’s name** | **Yes or No?** | **Comments, if any** |
| Qualcomm |  | *NR-DL-PRS-ProcessingCapability-r16:*  I think there should be a field description for the *dummy* field:  ***dummy***  This field is not used in the specification. If received it shall be ignored by the receiver.  The new *supportedDL-PRS-ProcessingSamples-RRC-Inactive-r17* field description:  Please move this after the description of *lowerRxBeamSweepingThan8-FR2* (i.e., to the last row of the table).  *NR-DL-PRS-ProcessingCapability* field descriptions:  *prs-ProcessingWindowType2*: *prs-ProcessingCapabilityBandList-r16*:  -r16 should be removed  *NR-UE-TEG-Capability* field descriptions:  *nr-UE-TxTEG-ID-MaxSupport*:  s*upportedSRS-PosResources-r16:*  -r16 should be removed |
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# Summary report and proposals

**For agreement:**

**Online discussion:**

# Reference

1. R2-2207385 Corrections on LPP capabilies Intel Corporation
2. R2-2208492 Change request about UE capability for PRS measurement within a PPW vivo
3. R2-2208792 (Summary of AI 6.11.2.1) Qualcomm
4. R2-2209117/R1-2207923 RAN1 feature lists
5. R4-2214489 Reply LS on DL-PRS measurements with reduced samples capability